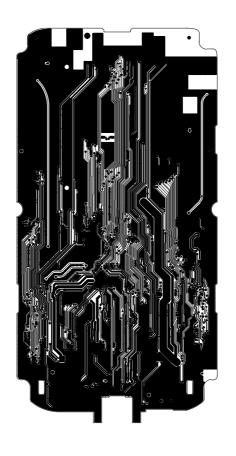
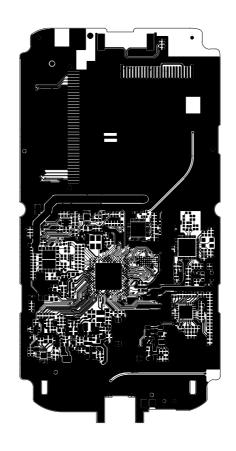
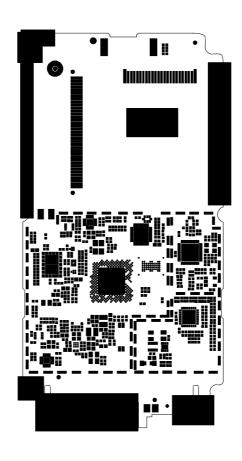


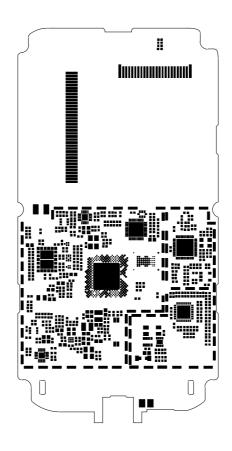
SIZE	QTY	SYM	PLATED	TOL
0.1	3301	+	YES	+/-0.0
0.25	2032	X	YES	+/-0.0
0.9	3	<b>♦</b>	NO	+/-0.0
0.8	2	+	NO	+/-0.0
1	7	+	NO	+/-0.0
1.6	1	+	NO	+/-0.0

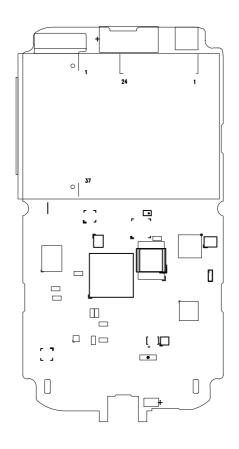


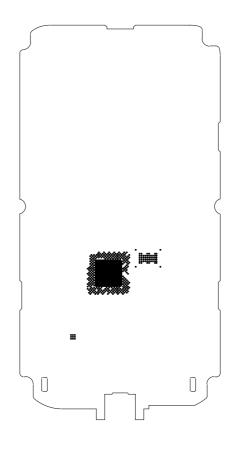


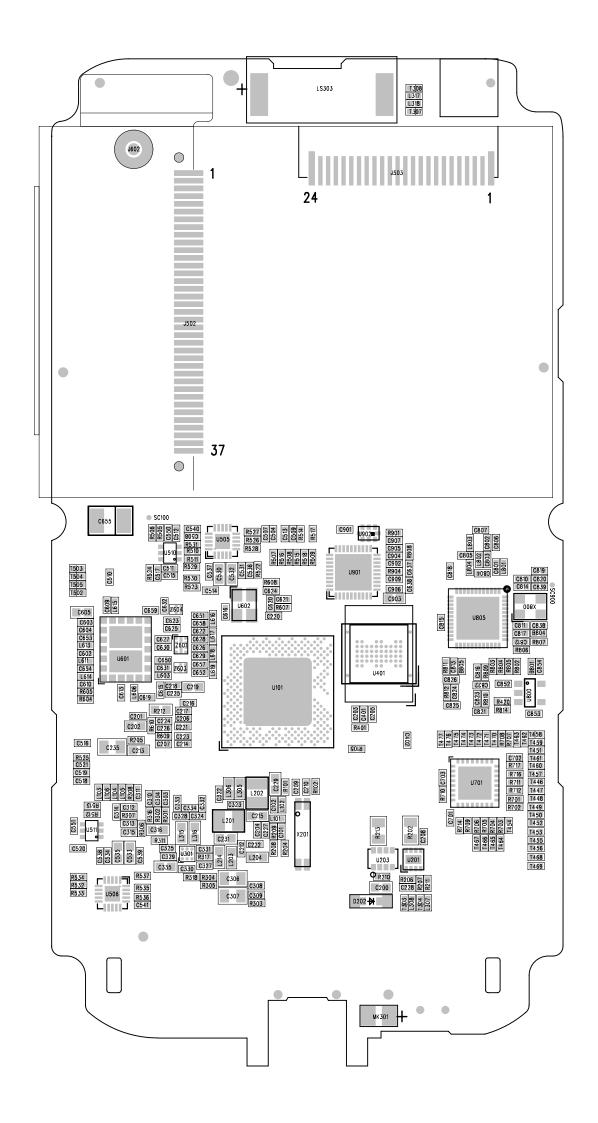


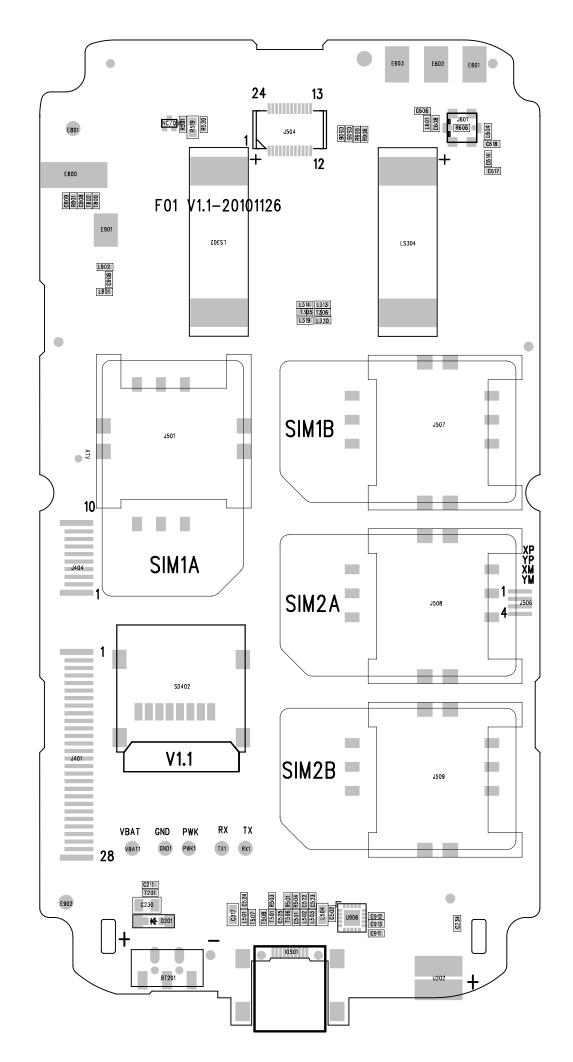












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