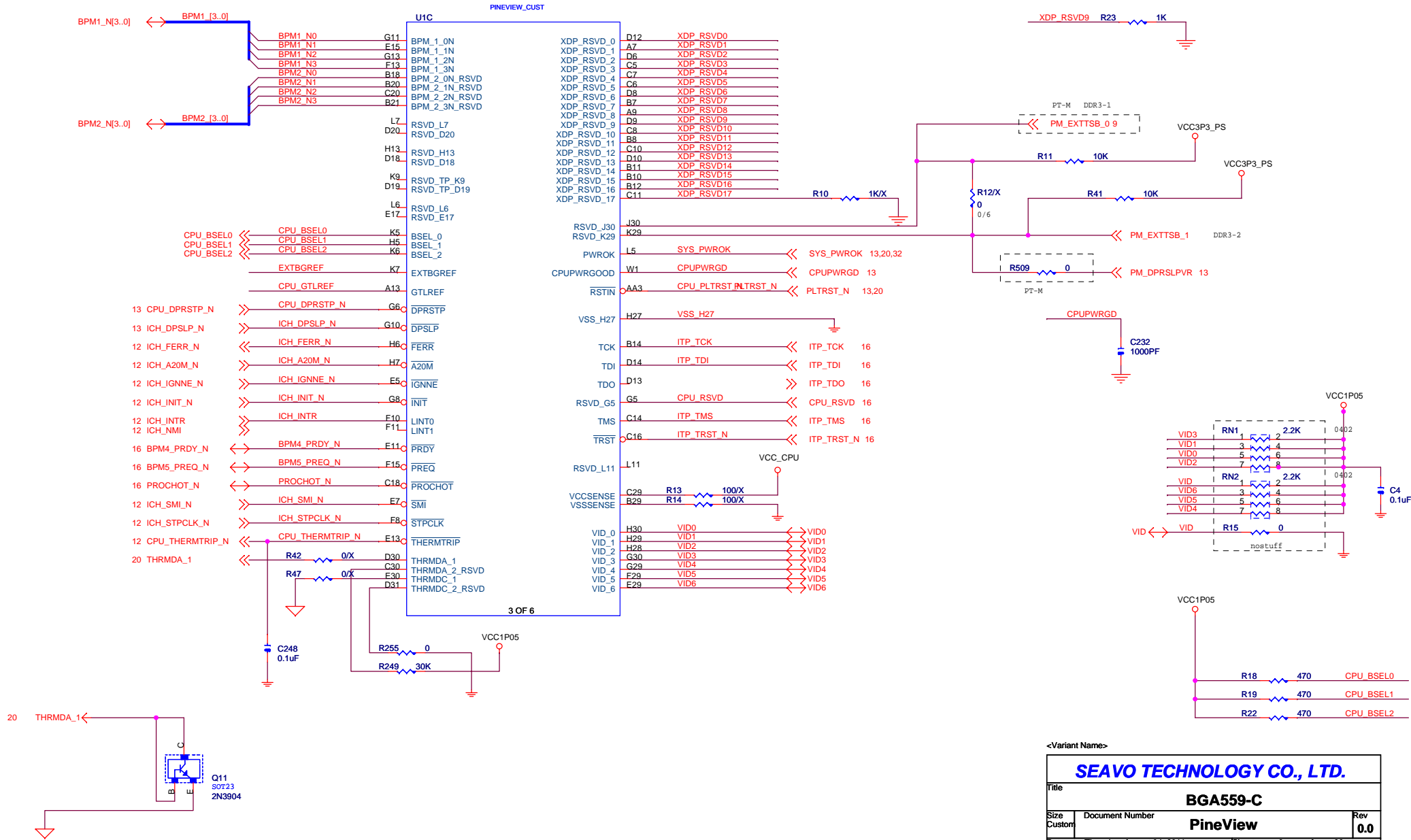


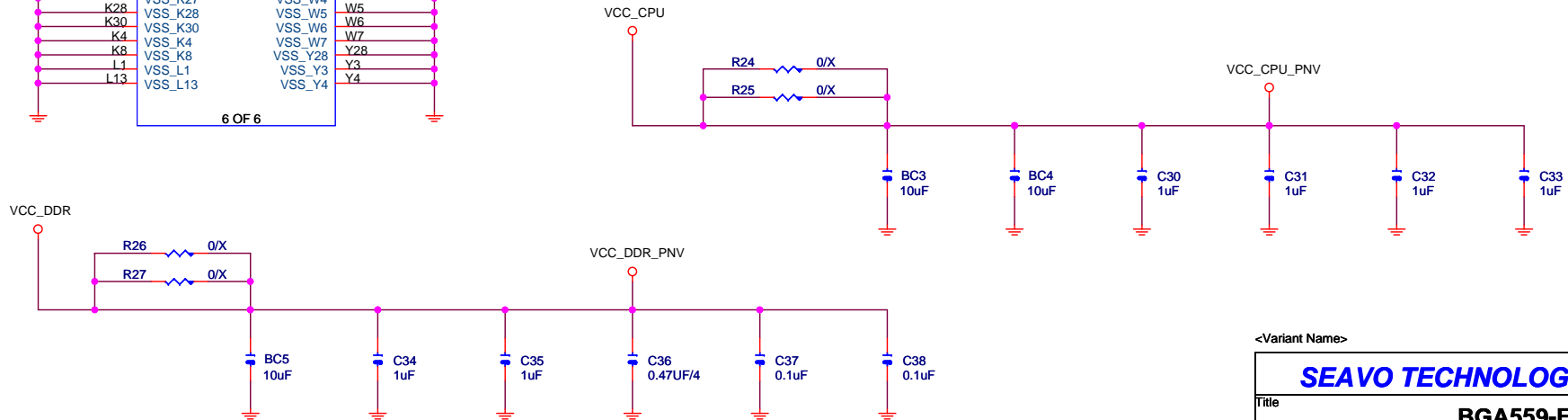
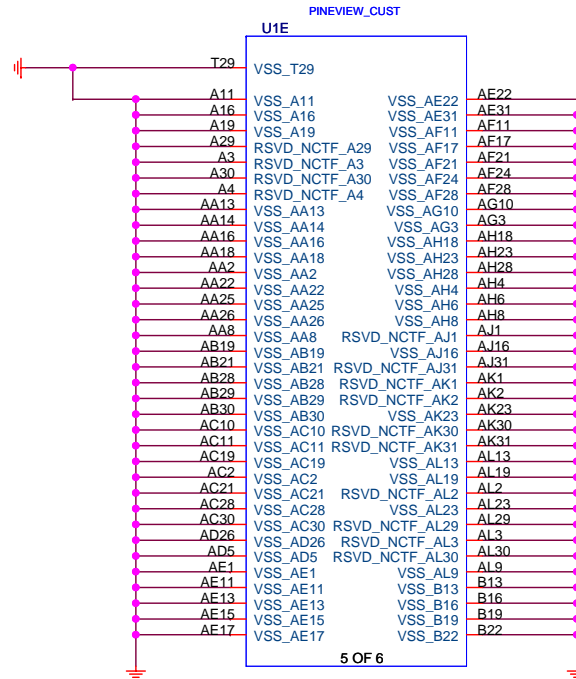
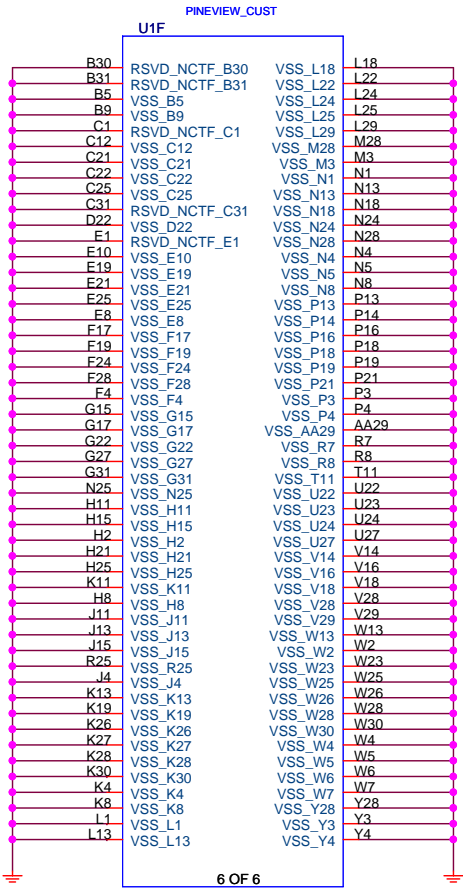
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SEAVO TECHNOLOGY CO., LTD.		
Title BGA559-B		
Size	Document Number	Rev
Custom	PineView	0.0
Date:	Thursday, July 21, 2011	Sheet 2 of 33



<Variant Name>

SEAVO TECHNOLOGY CO., LTD.			
Title		BGA559-C	
Size	Document Number	PineView	
Custom		Rev 0.0	
Date:	Thursday, August 04, 2011	Sheet	3 of 33



<Variant Name>

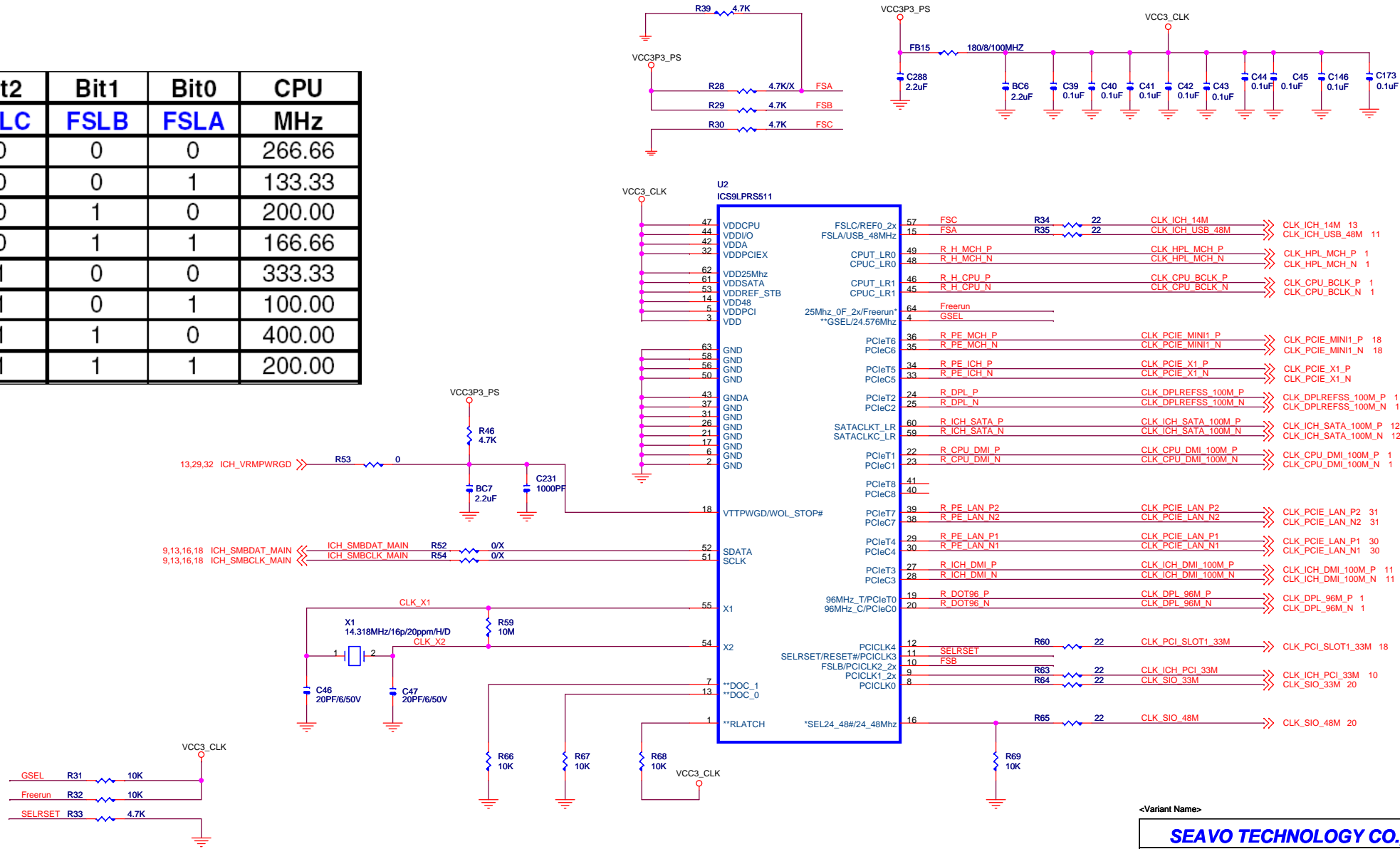
SEAVO TECHNOLOGY CO., LTD.

Title **BGA559-E F**

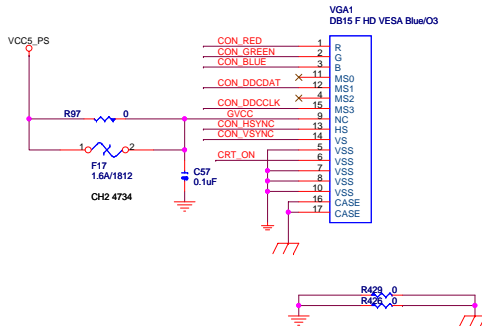
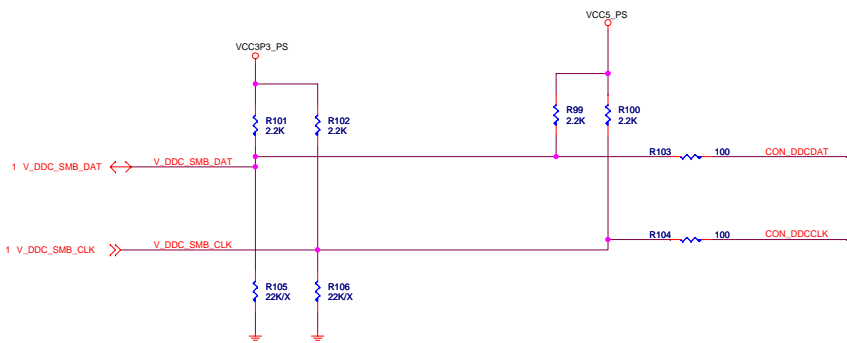
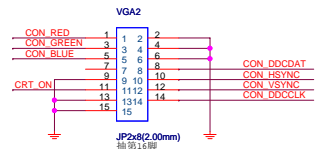
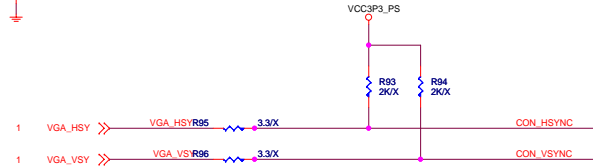
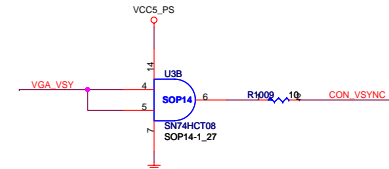
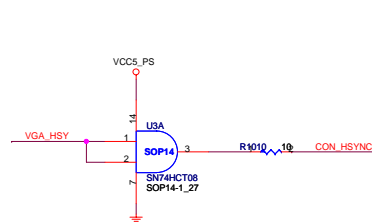
Size Custom	Document Number	PineView	Rev 0.0
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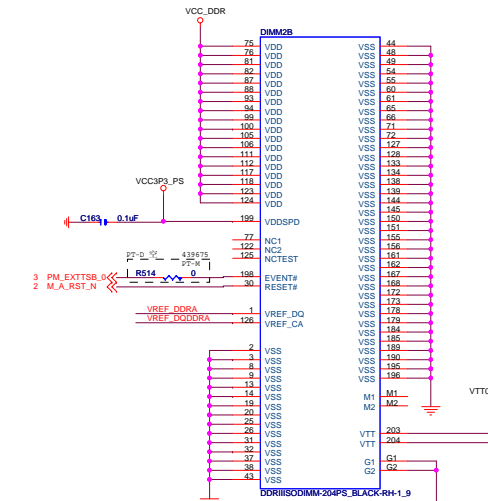
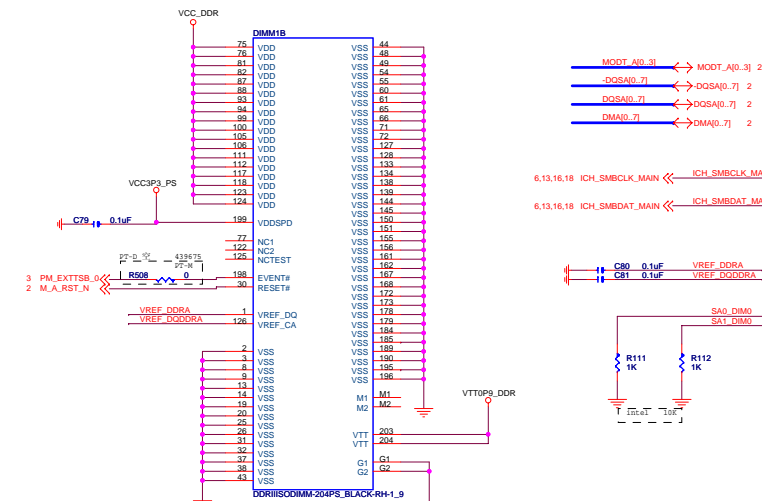
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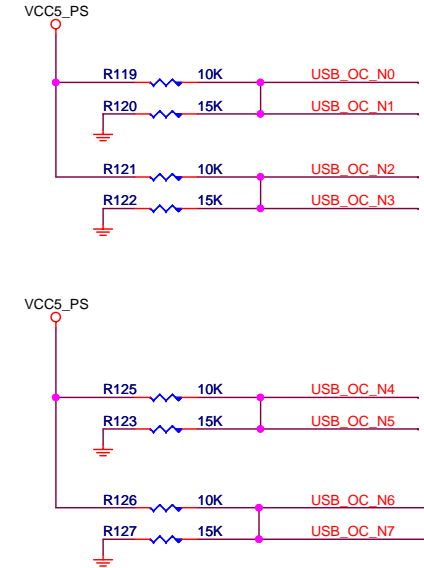
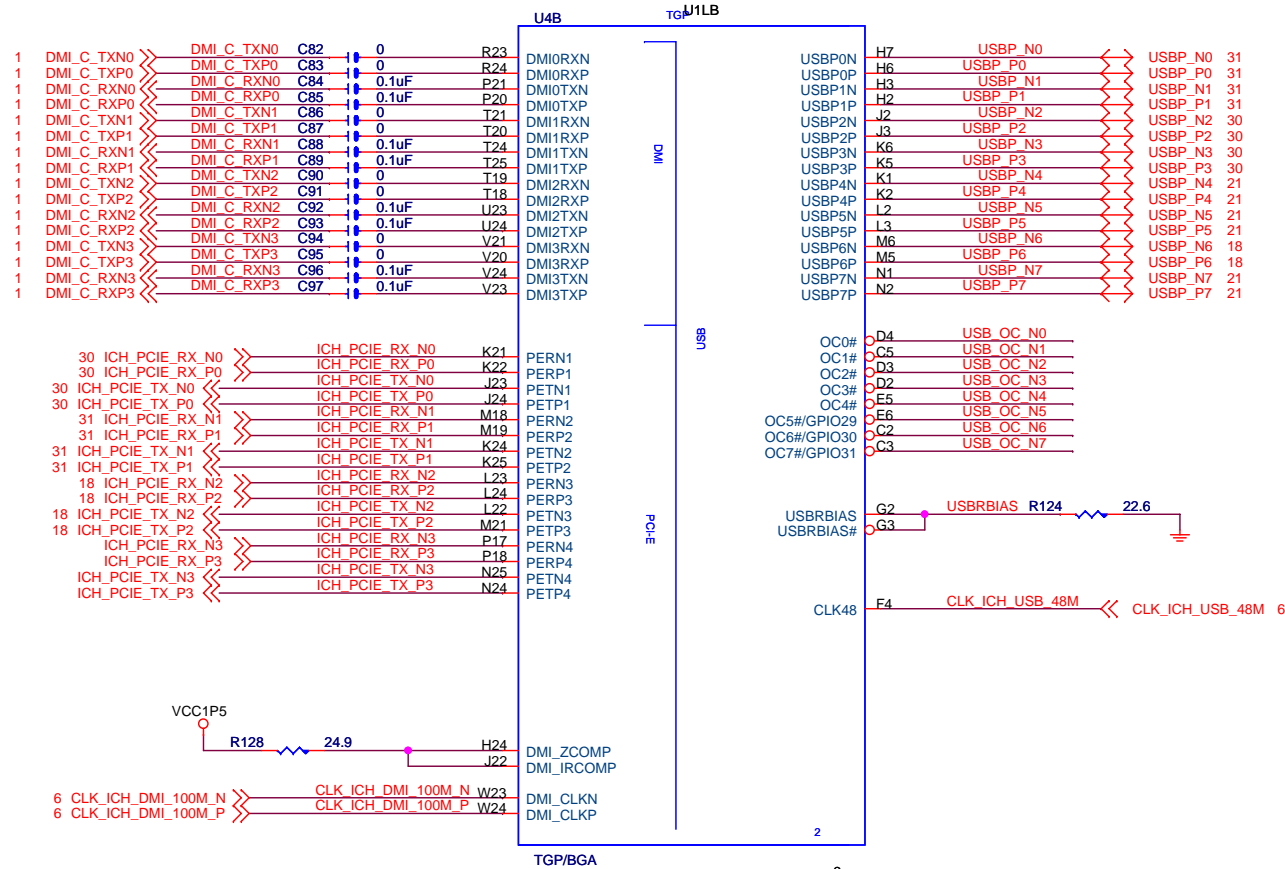
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FSLC	FSLB	FSLA	MHz
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0	0	1	133.33
0	1	0	200.00
0	1	1	166.66
1	0	0	333.33
1	0	1	100.00
1	1	0	400.00
1	1	1	200.00



<Variant Name>			
SEAVO TECHNOLOGY CO., LTD.			
Title			
CK505			
Size	Document Number	Rev	
Custom		PineView	
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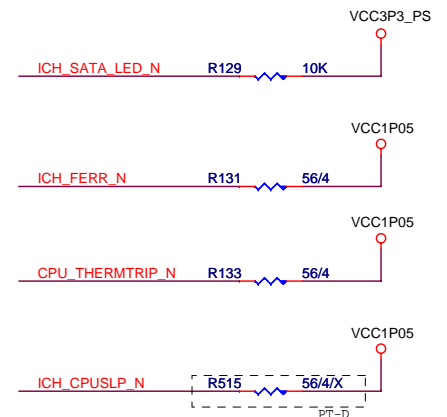
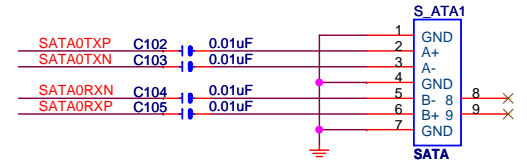
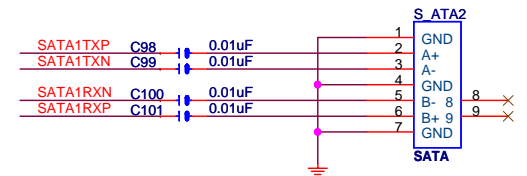
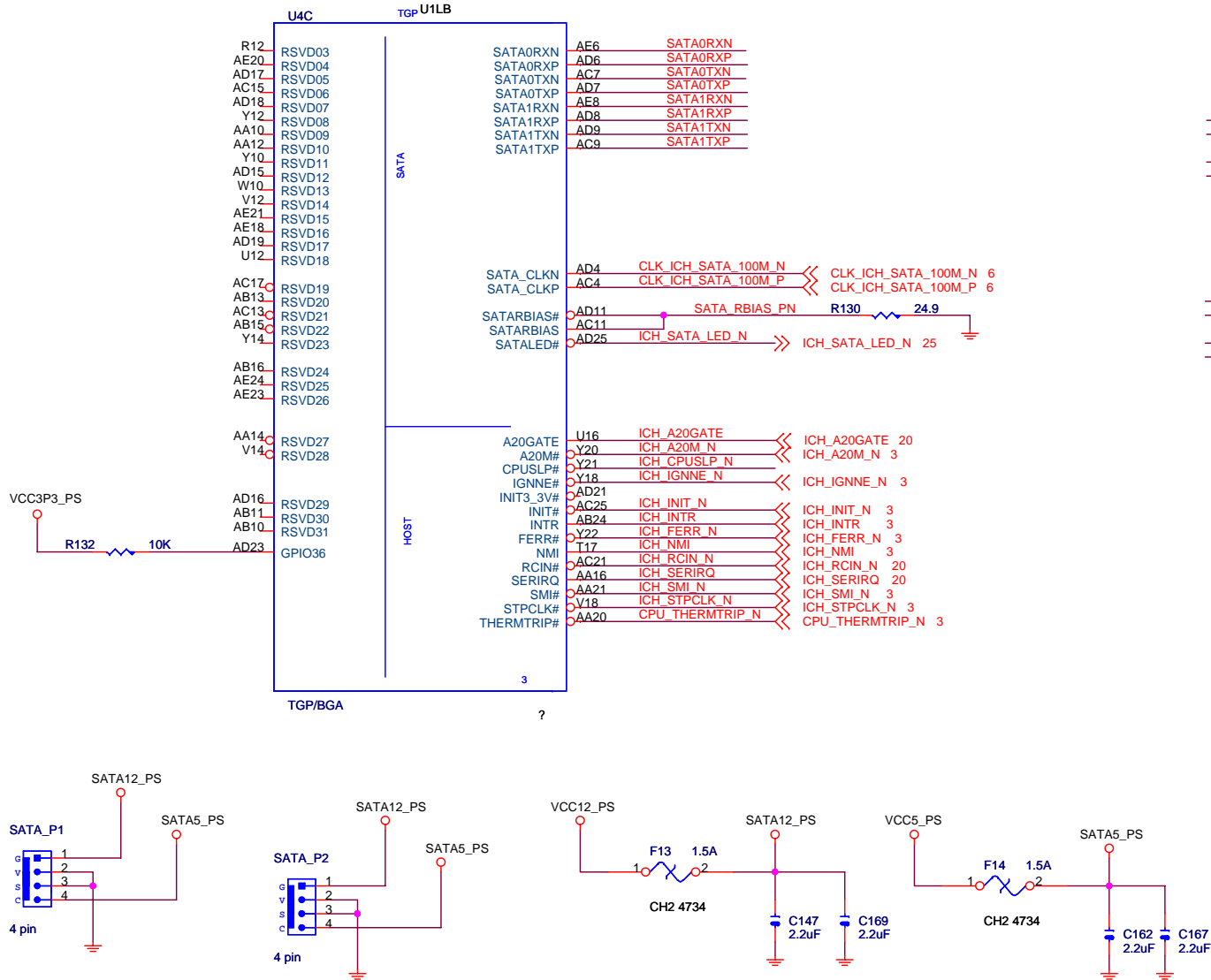




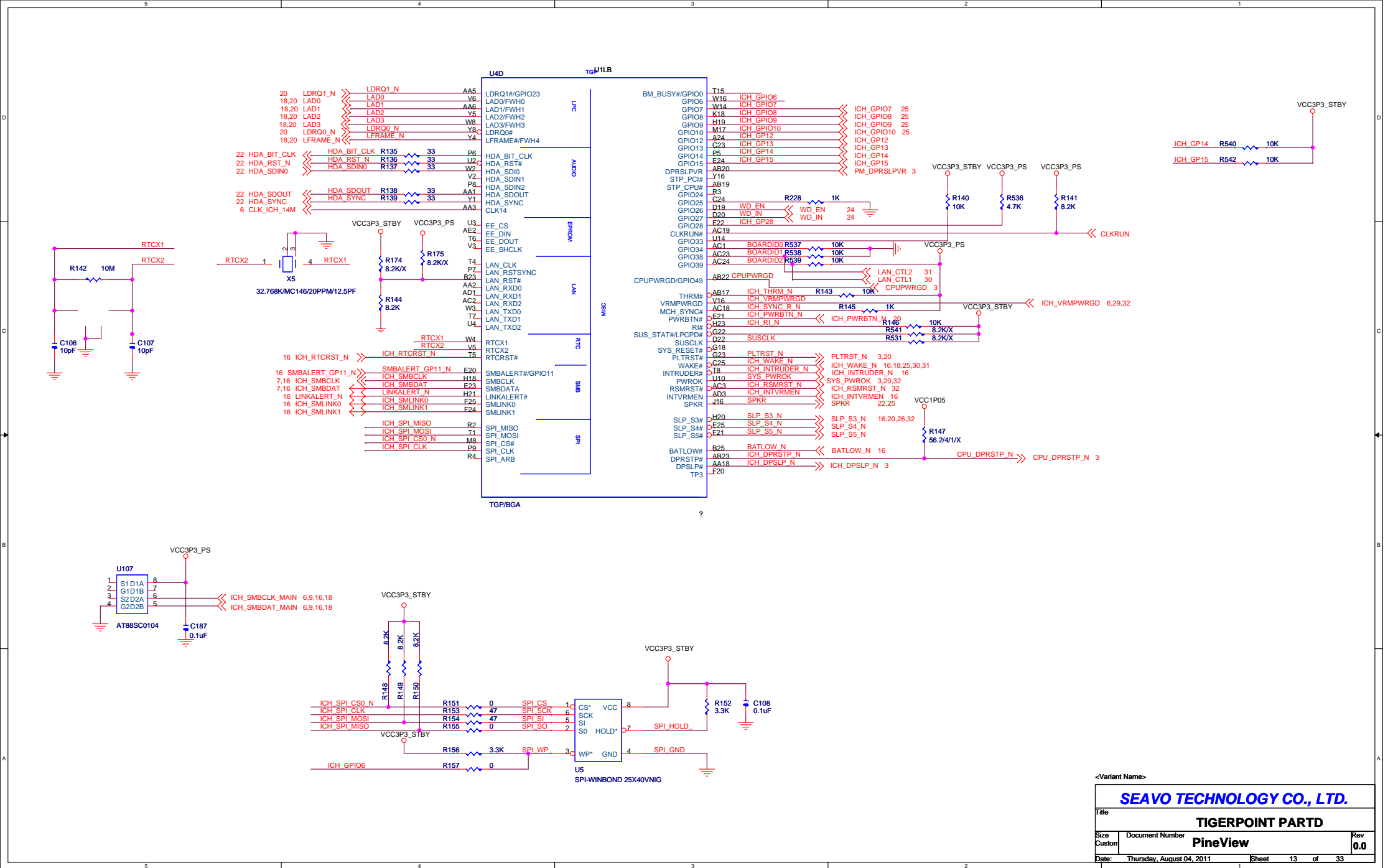


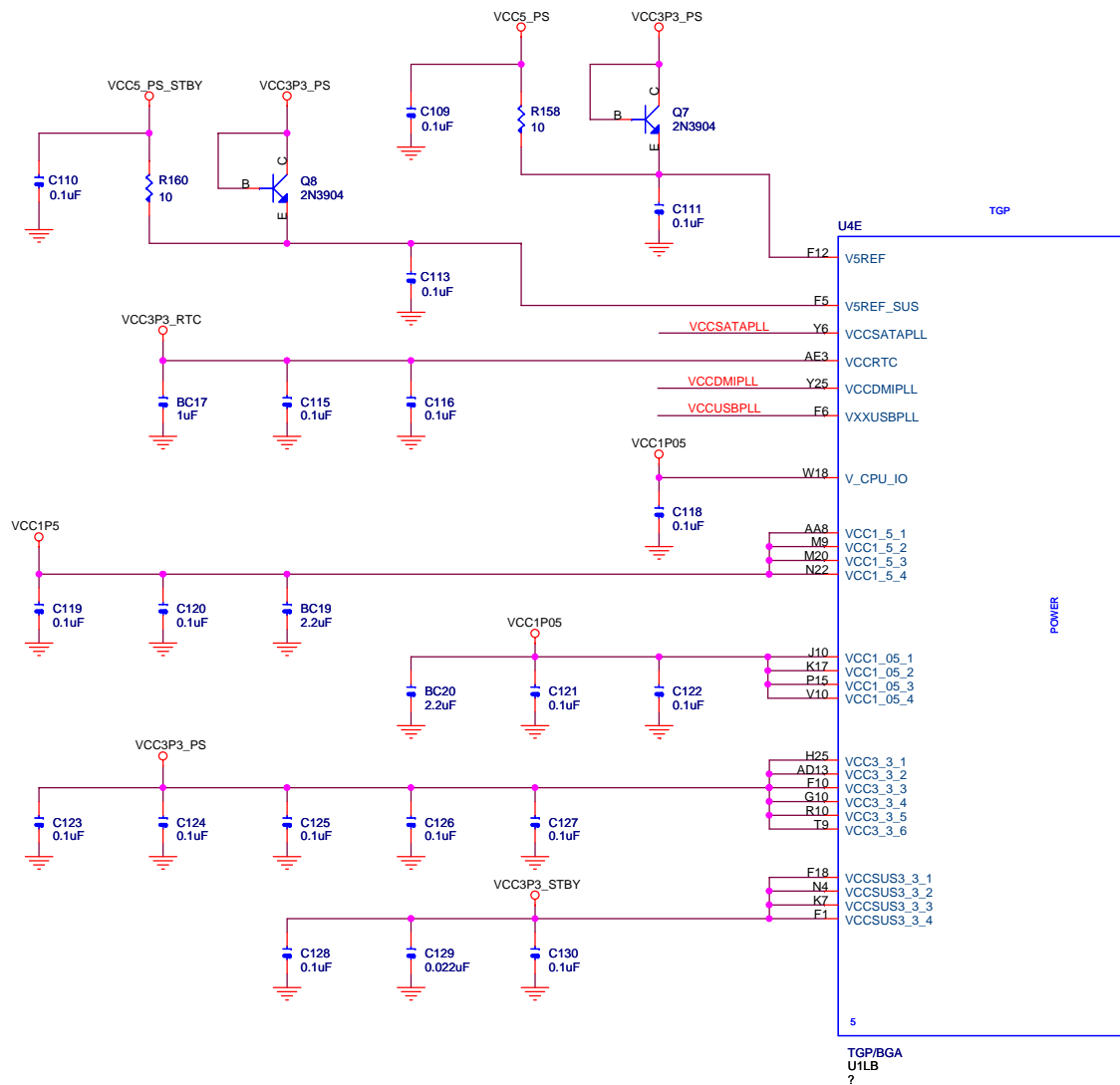
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SEAVO TECHNOLOGY CO., LTD.			
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TIGERPOINT PARTB PCIE USB DMI			
Size	Document Number	Rev	
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Date:	Thursday, July 21, 2011	Sheet	11 of 33



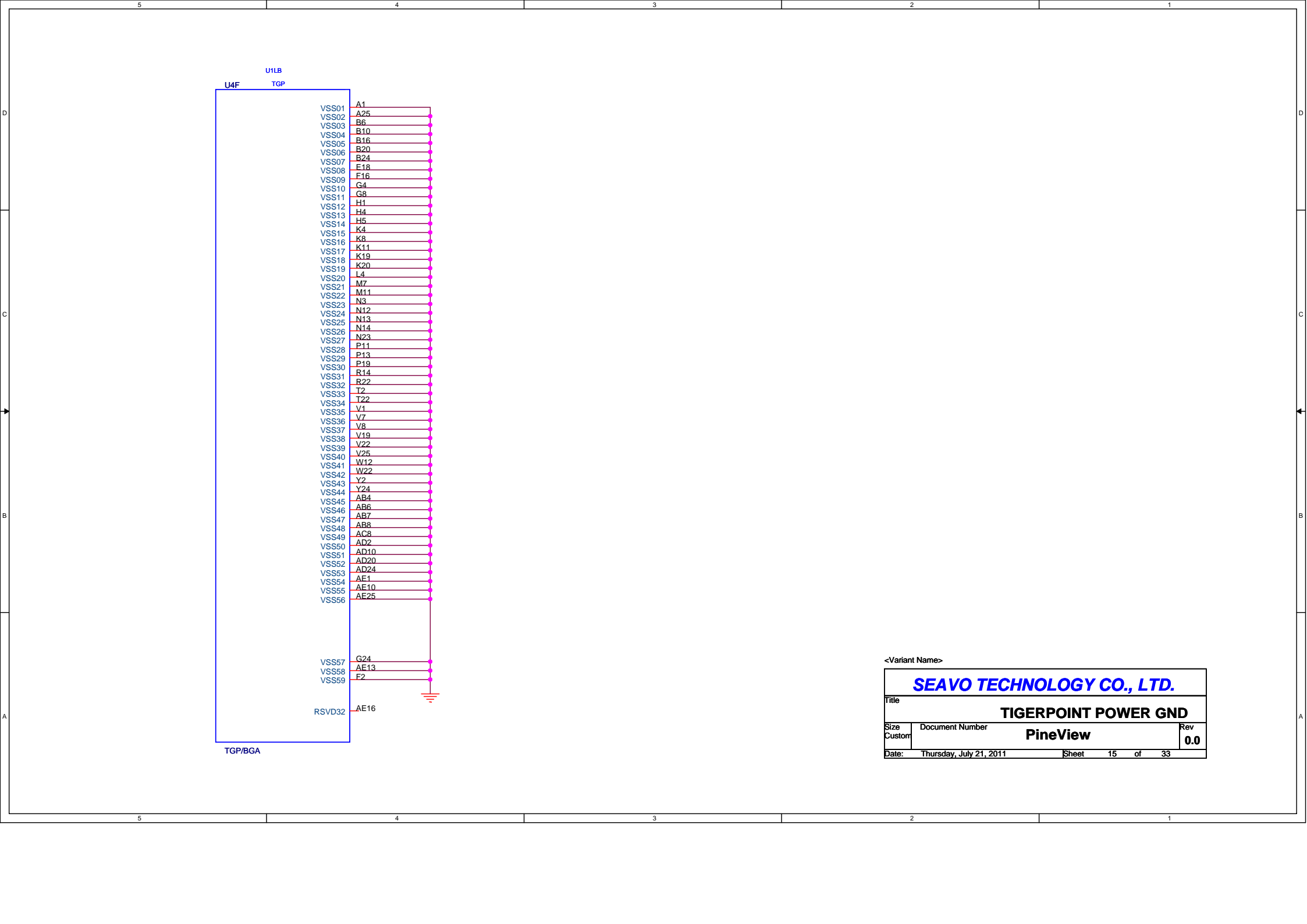
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SEAVO TECHNOLOGY CO., LTD.			
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Size	Document Number	PineView	
Custom		Rev 0.0	
Date:	Thursday, July 21, 2011	Sheet	12 of 33

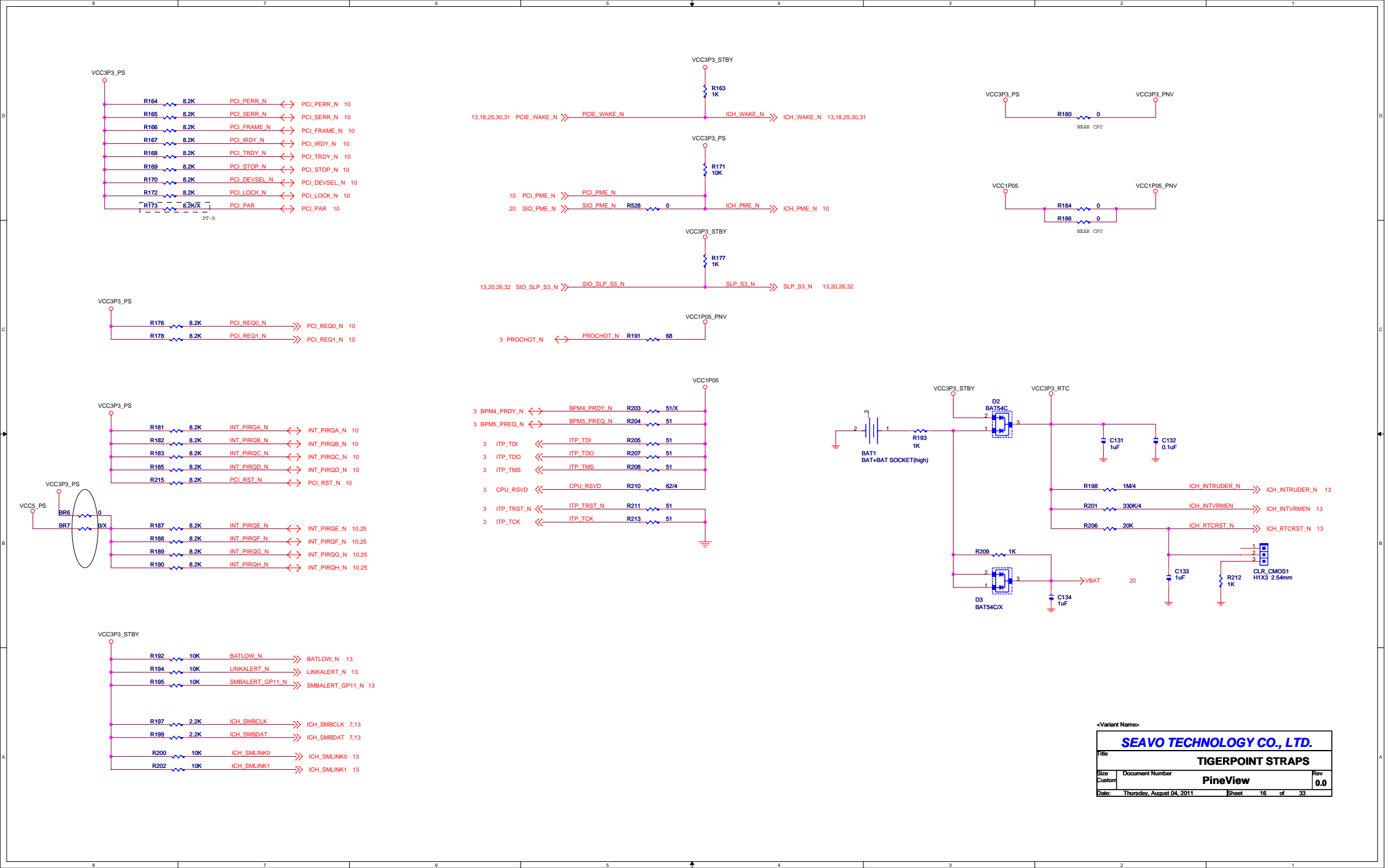


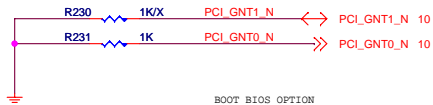


<Variant Name>

SEAVO TECHNOLOGY CO., LTD.			
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Size	Document Number	Rev	
Custom		PineView	
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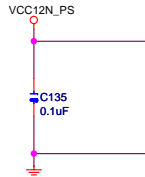
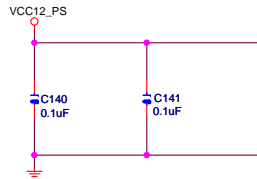
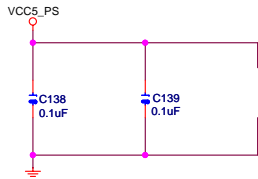
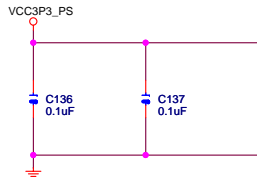






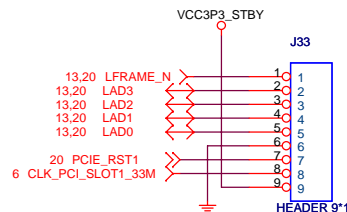
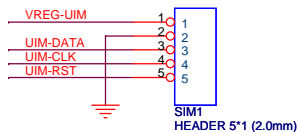
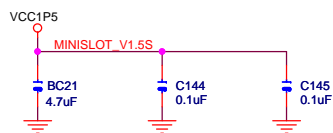
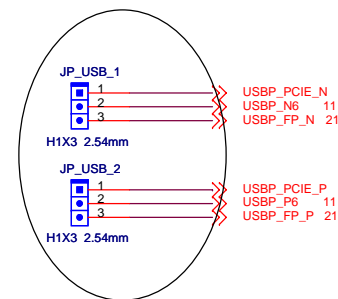
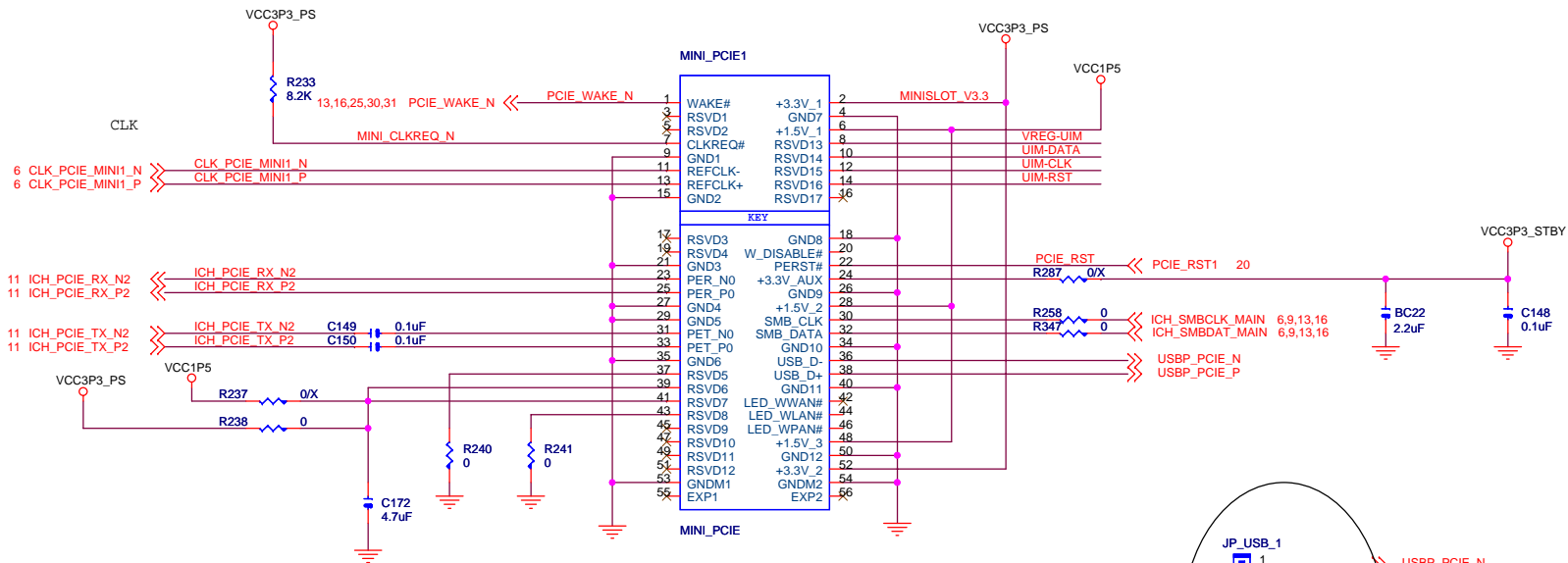
BOOT BIOS OPTION

GNT0_N	SPI_CS1_N	BOOT BIOS
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1	0	PCI



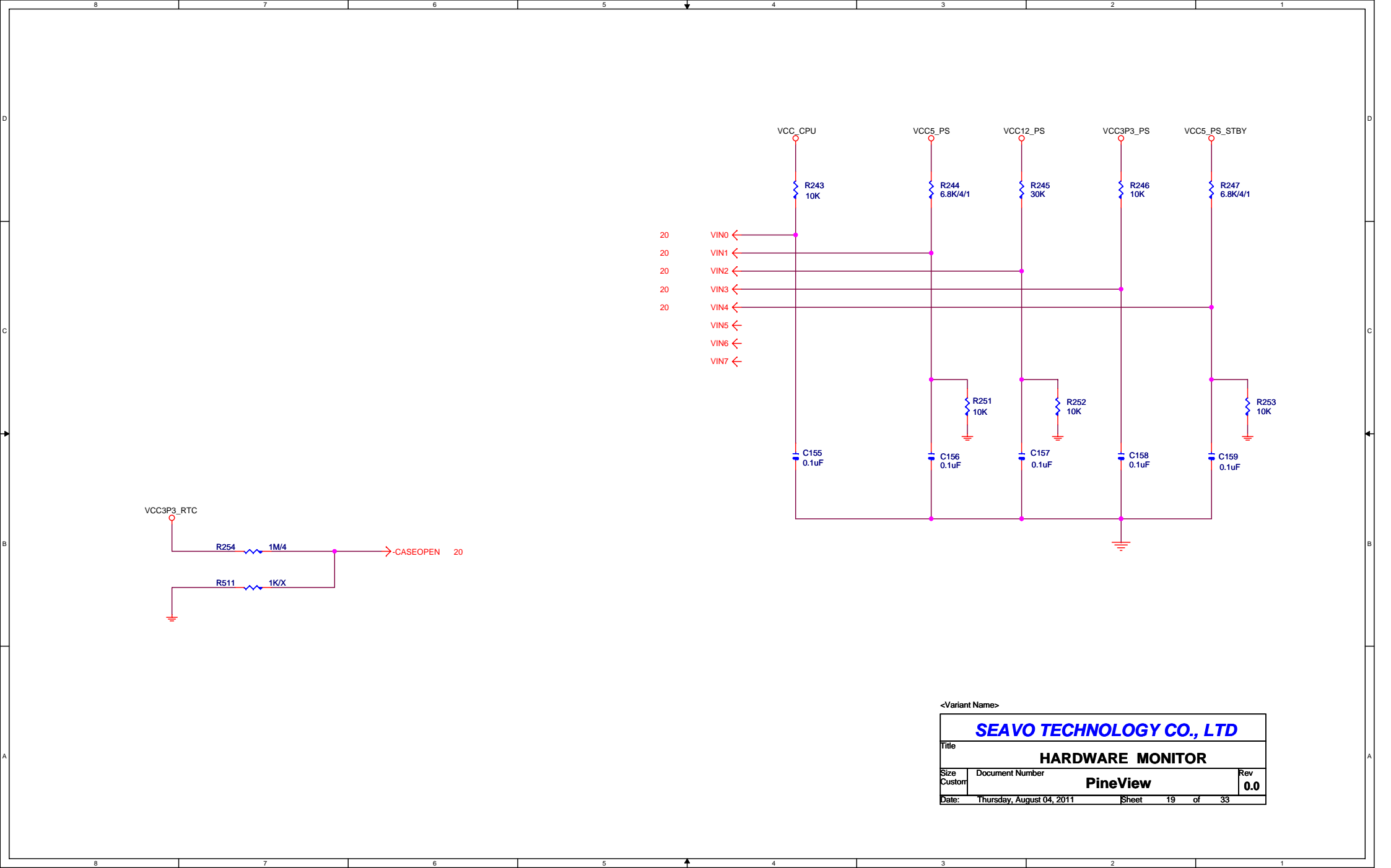
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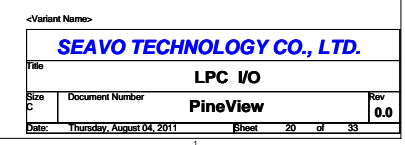
SEAVO TECHNOLOGY CO., LTD.		
Title		
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Size	Document Number	Rev
Custom	PineView	0.0
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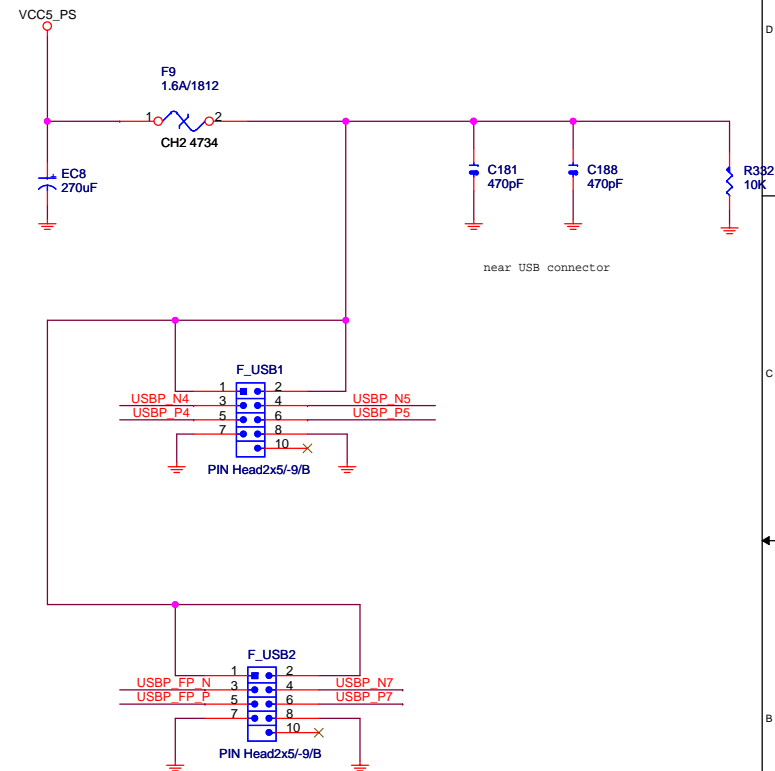
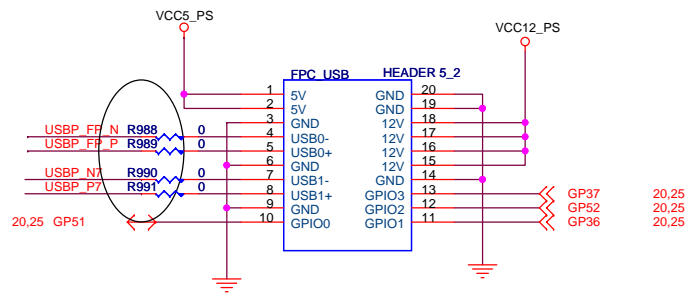
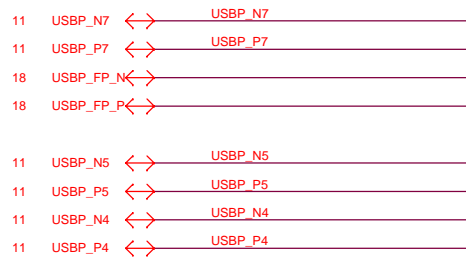


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SEAVO TECHNOLOGY CO., LTD.			
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Size	Document Number	PineView	
Custom			Rev 0.0
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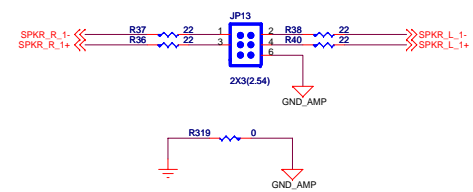
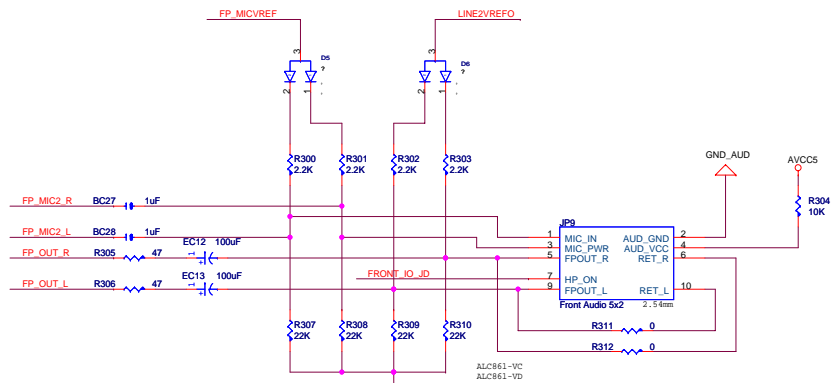


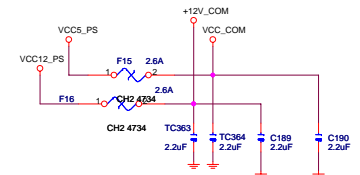
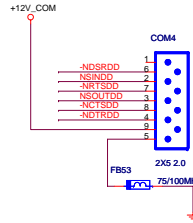
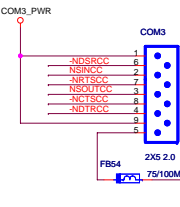
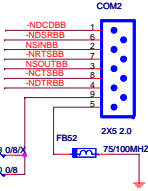
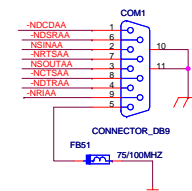
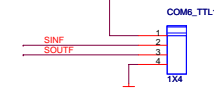
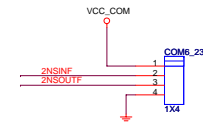
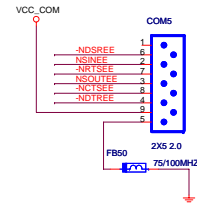
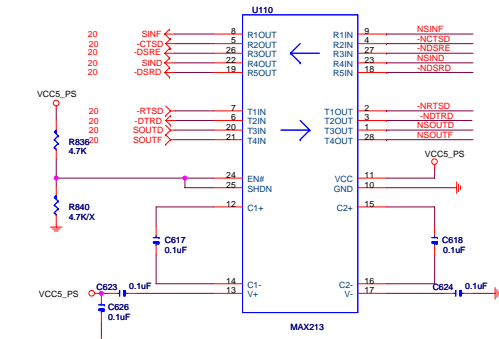
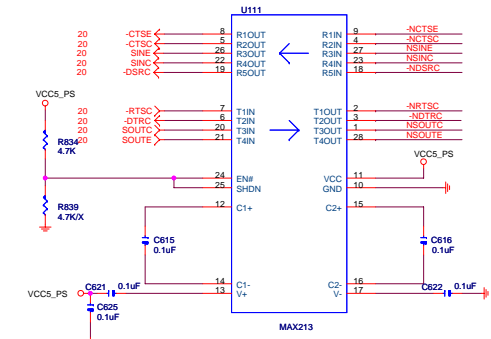
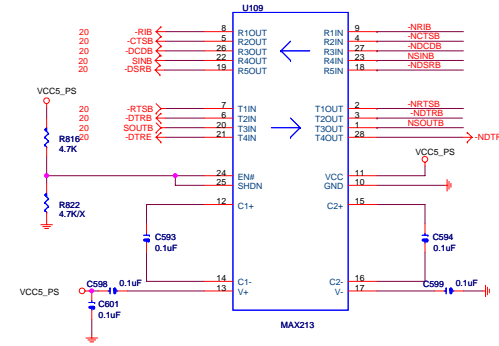
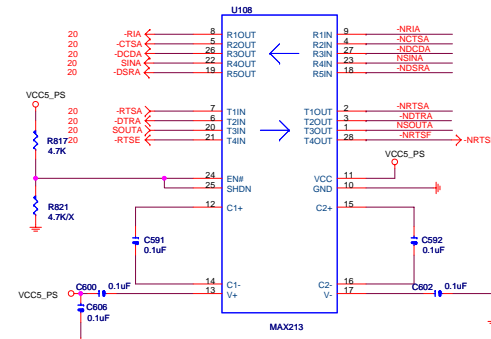


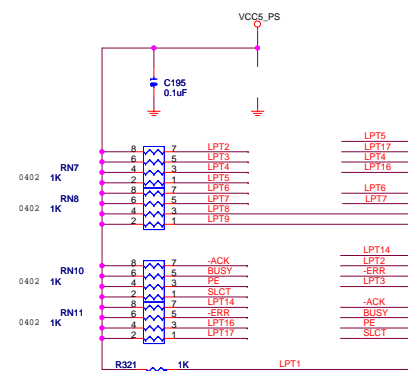
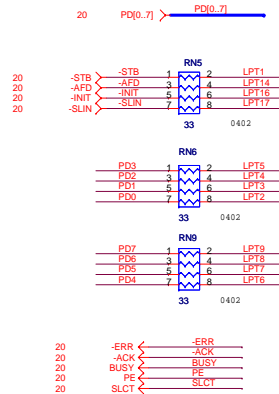
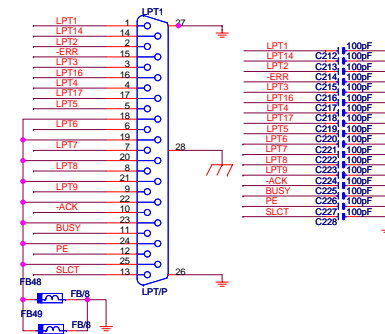
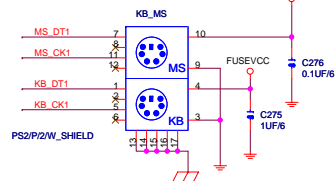
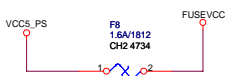


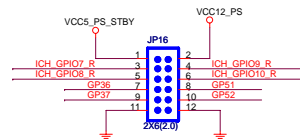
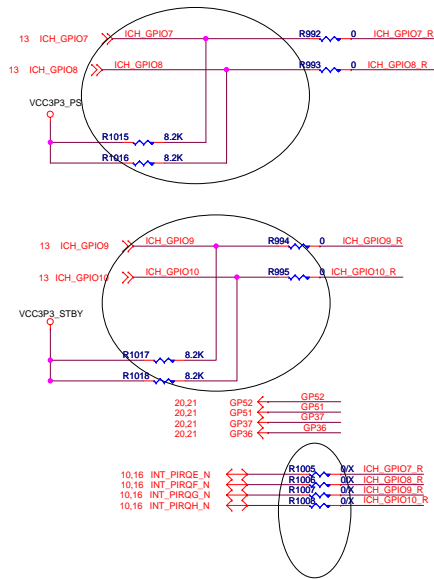
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SEAVO TECHNOLOGY CO., LTD.			
Title		FRONT USB CONNECTOR	
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Custom			
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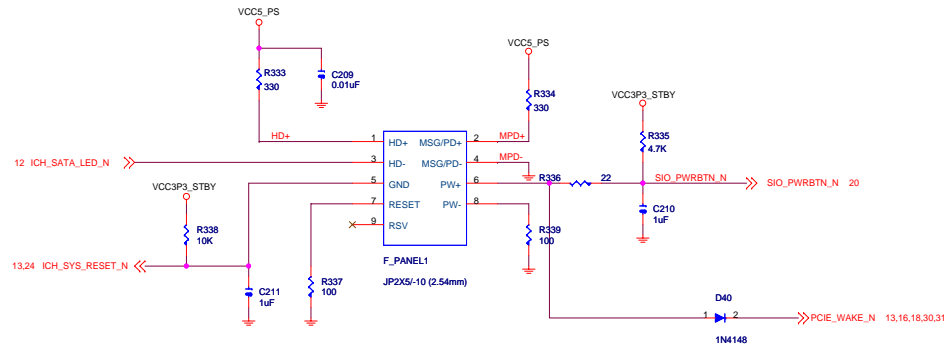
States for green LED

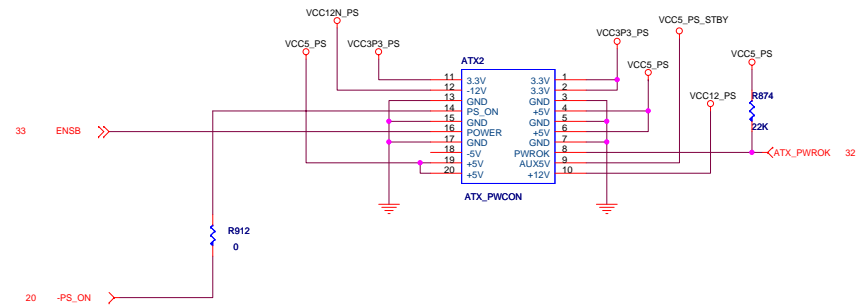
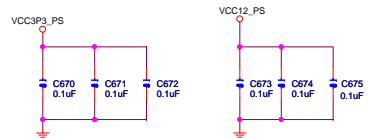
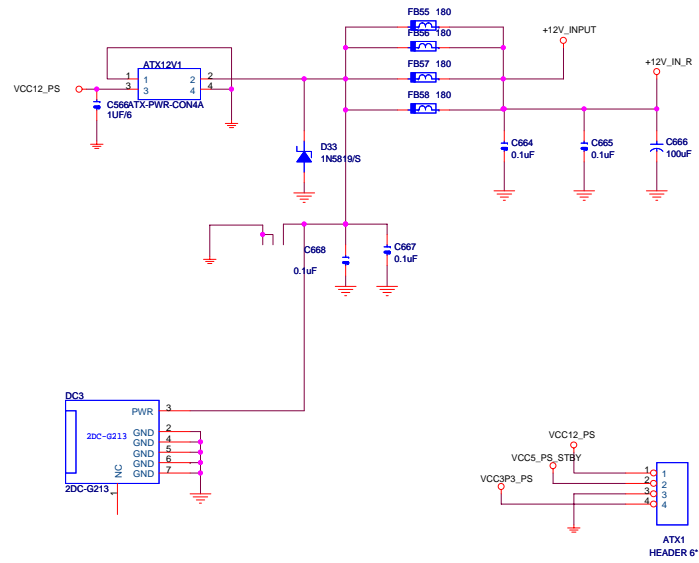
LED States	ACPI States	GPIO28
ON	S1,S3	0
OFF	S0,S5	1

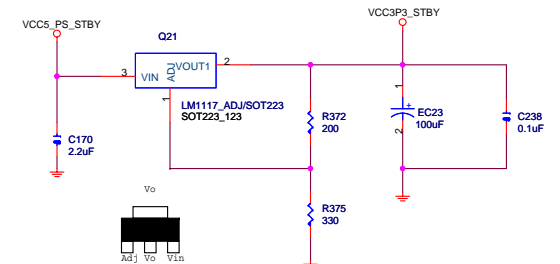
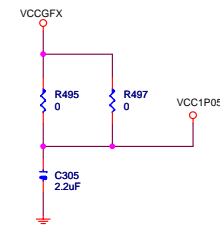
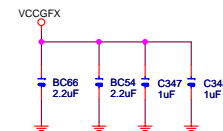
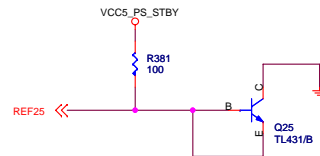
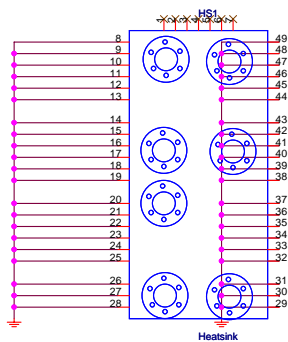
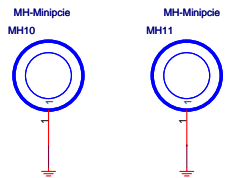
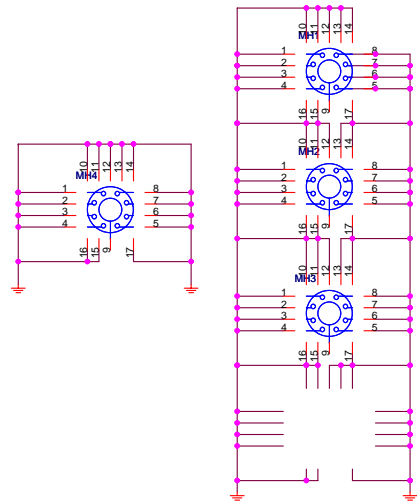
(GPO22 DEFAULT HIGH, main power)

States for a single-color power LED

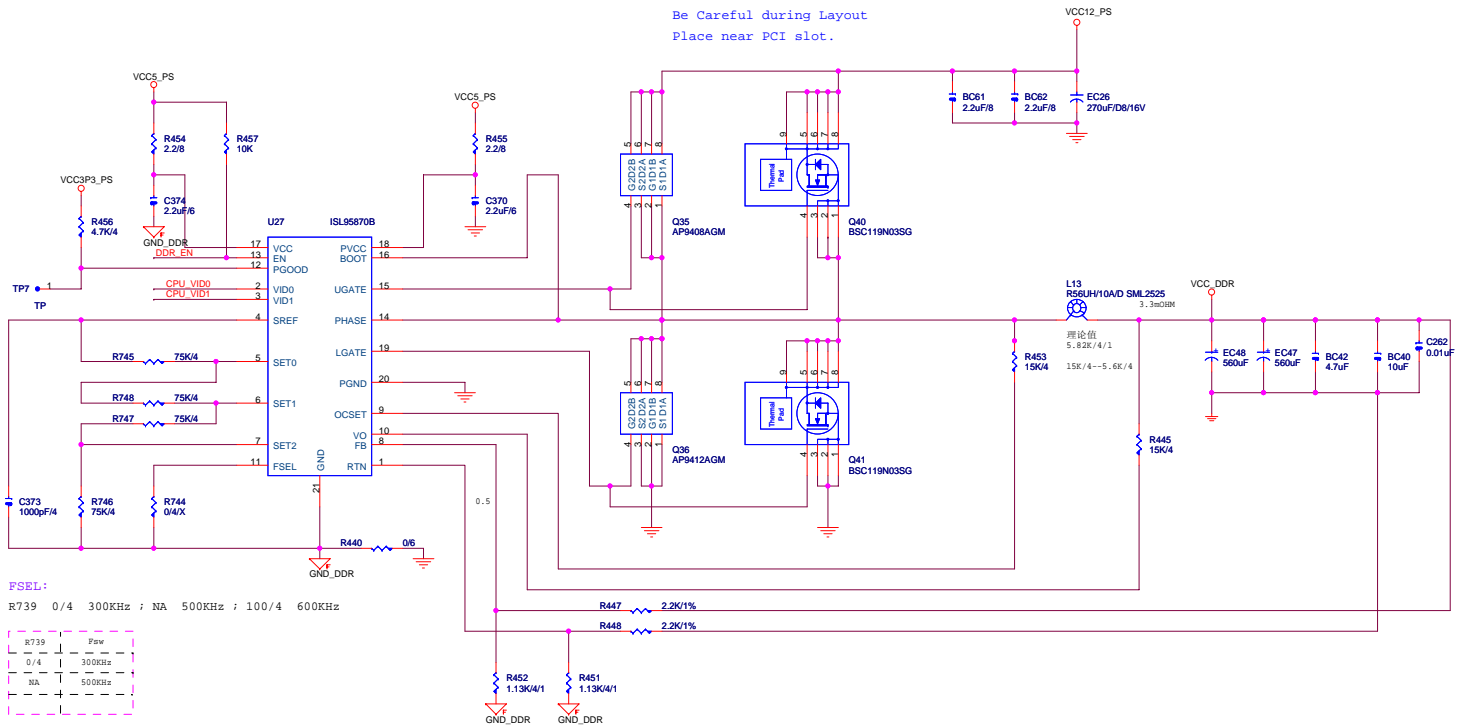
LED States	ACPI States	GPIO25	GPIO27	GPIO24
OFF	S1,S3,S5	1	1	NO1
Steady Green	S0	1	1	1
Blinking Green	S0(message waiting)	1	0	1







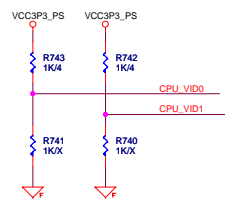
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SEAVO TECHNOLOGY CO., LTD.			
Title			
POLY SWITCH			
Size	Document Number	Rev	
Custom	PineView	0.0	
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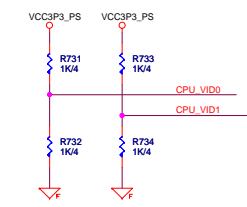
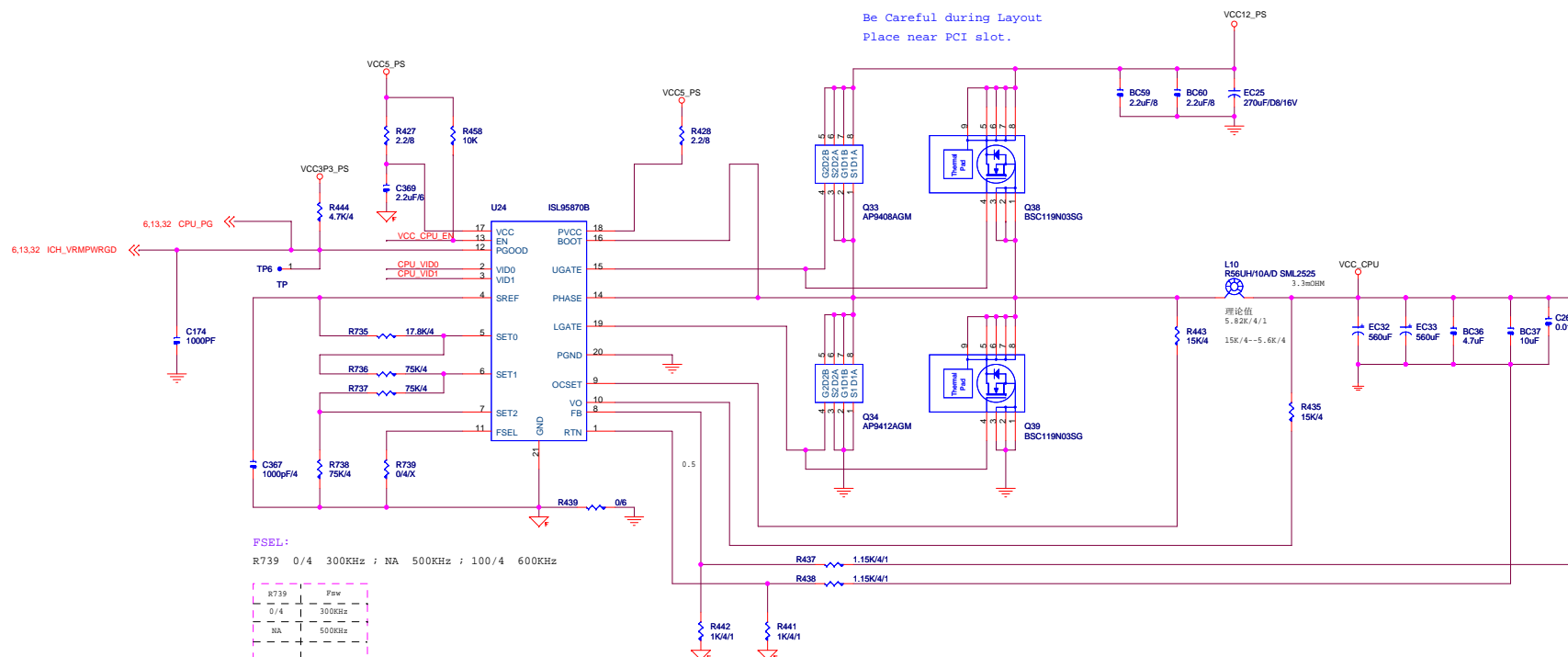


FSEL:

R739 0/4 300KHz ; NA 500KHz ; 100/4 600KHz

R739	0/4	300KHz	NA	500KHz	100/4	600KHz
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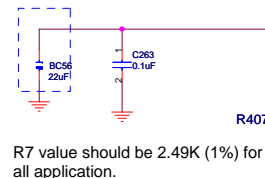




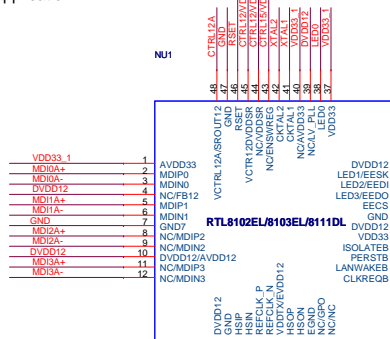
C14 is only for RTL8111DL.

Remove R5 and R6 for RTL8102EL/8103EL application.

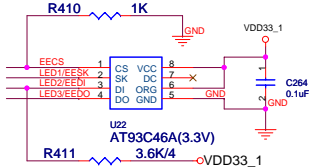
R5 and R6 are used in the RTL8111DL application.
Remove R6 if switching regulator is enabled. Remove R5 if external power 1.2V is used.



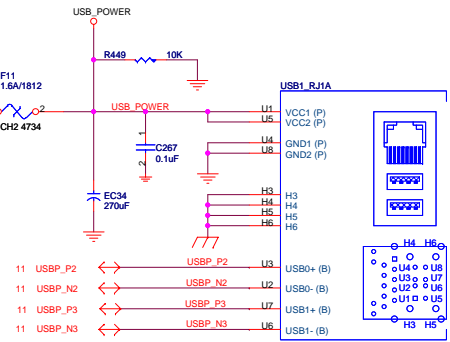
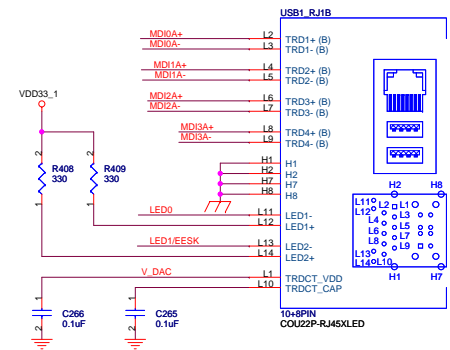
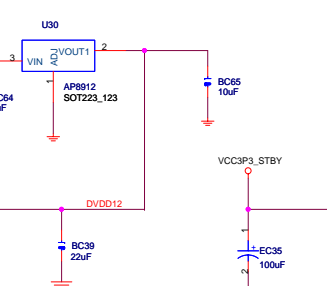
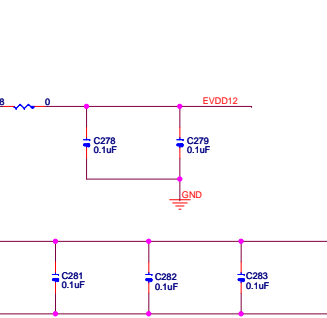
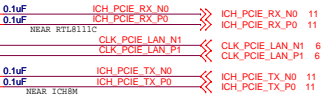
R7 value should be 2.49K (1%) for all application.



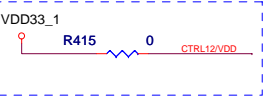
R13 is only required by RTL8102EL and RTL8103EL.



Pin 23 is GPO pin for 8111DL. It is used for DSM function.



For RTL8111DL, use this block



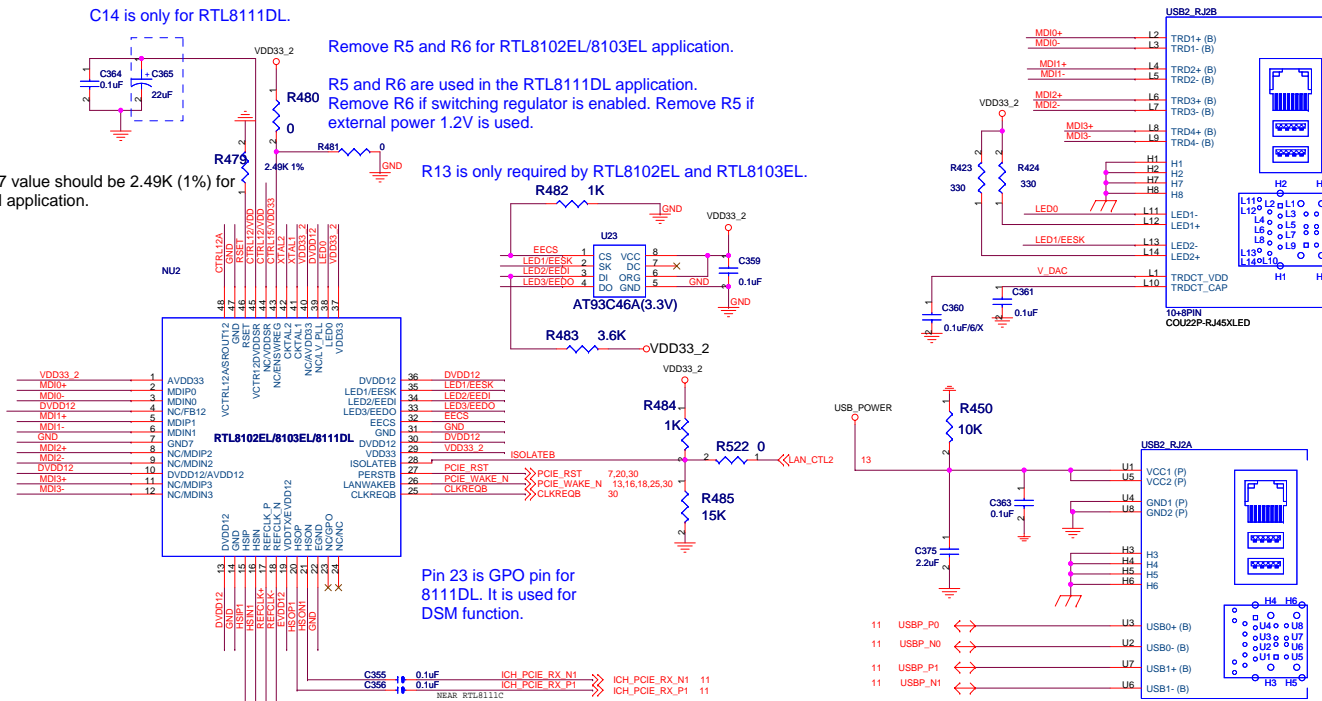
C14 is only for RTL8111DL.

Remove R5 and R6 for RTL8102EL/8103EL application.

R5 and R6 are used in the RTL8111DL application.
Remove R6 if switching regulator is enabled. Remove R5 if external power 1.2V is used.

R13 is only required by RTL8102EL and RTL8103EL.

R7 value should be 2.49K (1%) for all application.



Pin 23 is GPO pin for 8111DL. It is used for DSM function.

For RTL8111DL, use this block



For RTL8102EL/8103EL, use this block.

