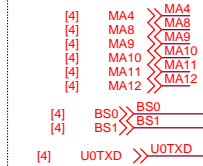
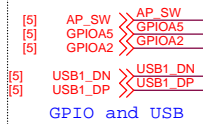


10/100M PHY Interface

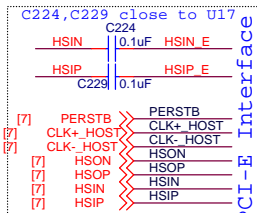


Configure Interface



GPIO and USB

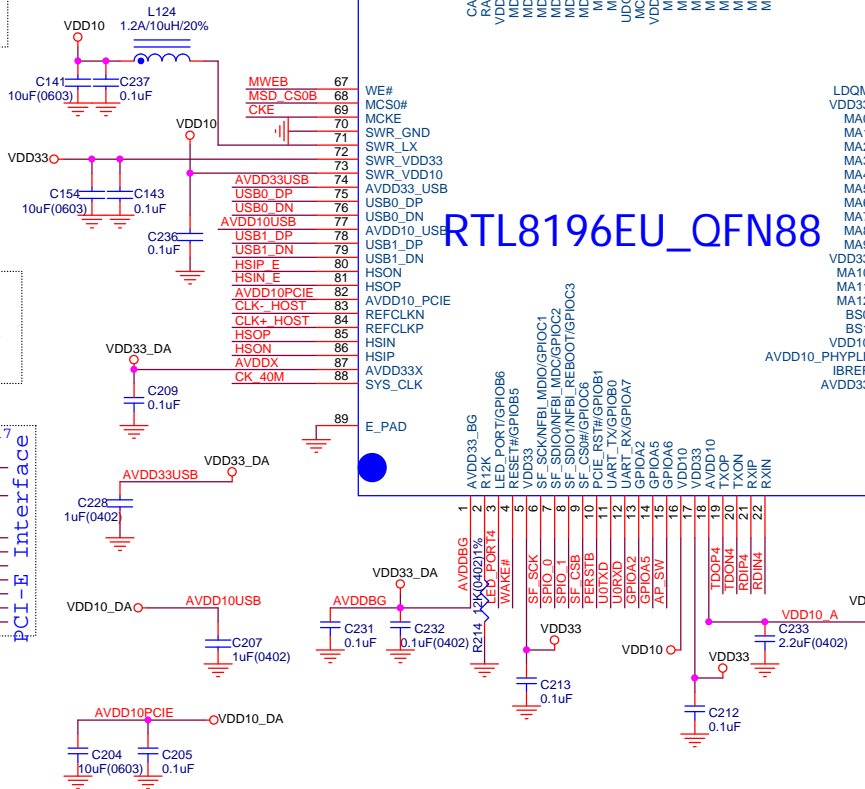
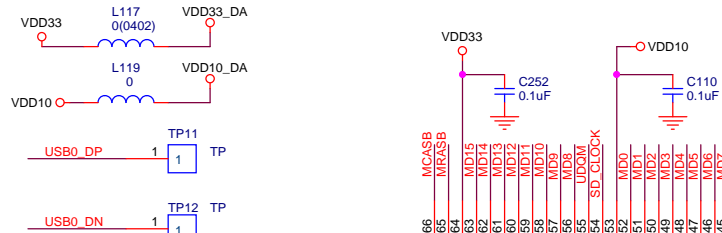
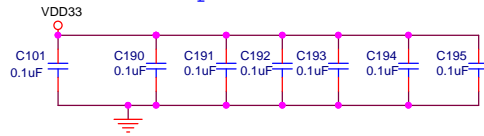
LED Interface



PCI-E Interface

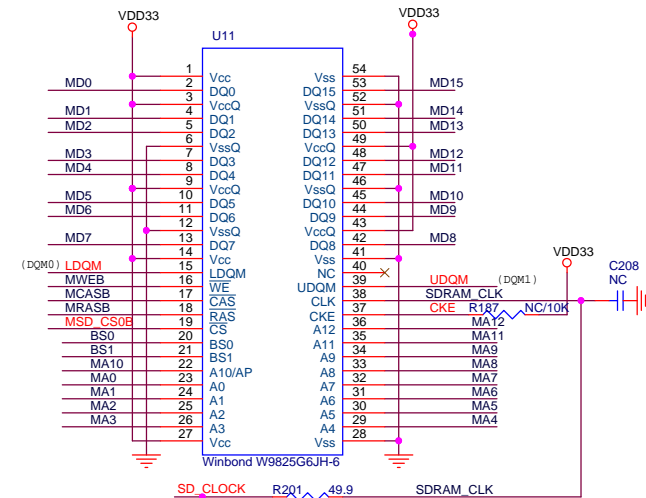


VDD33 Capacitors For SDRAM

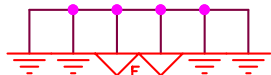
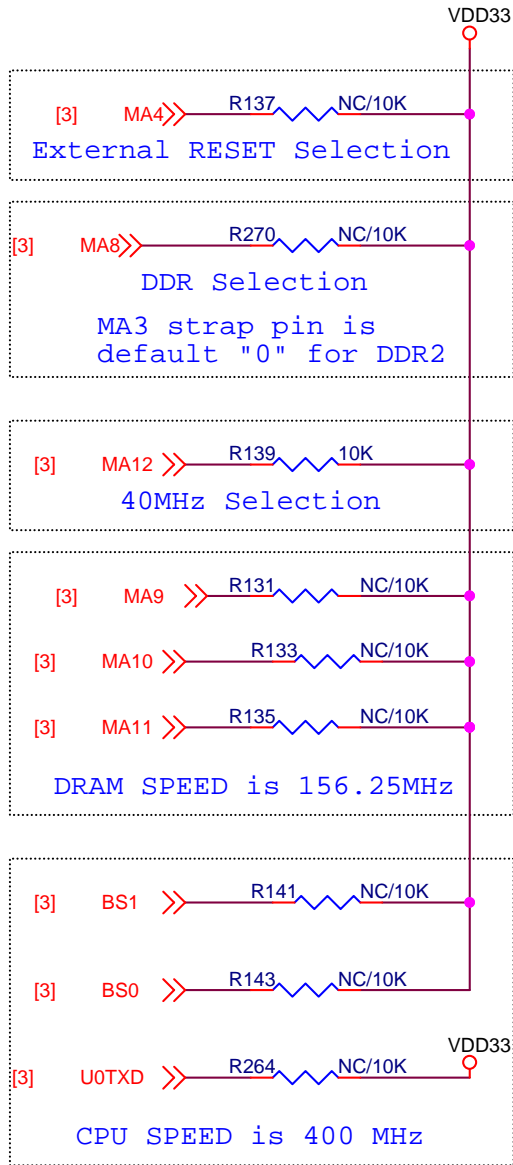


RTL8196EU_QFN88

16M 16bit SDARM x1



Strap Pin Definition

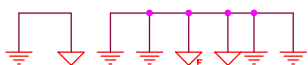
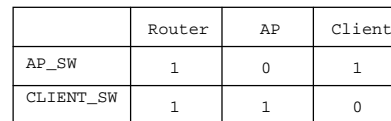
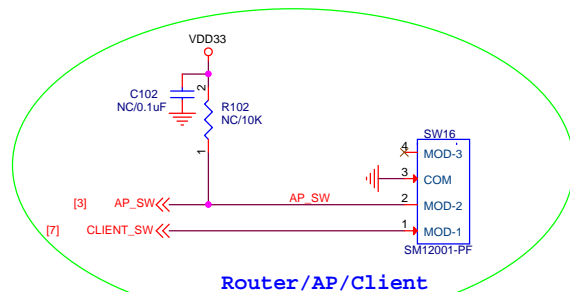
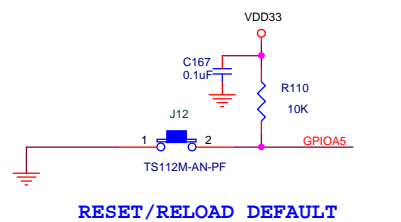
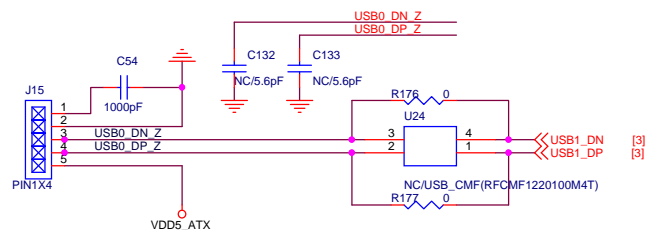


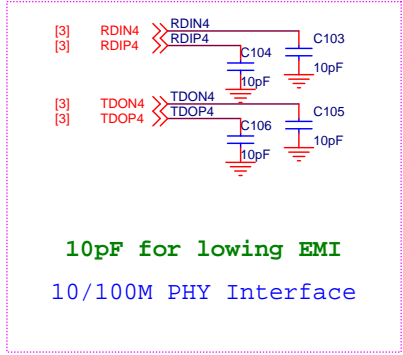
pin name	function name	function detail
PIN_M_A4	ENABLE_EXT_RSTN	ENABLE_EXT_RSTN 1=enable 0=disable
PIN_M_A8	DRAM_TYPE[1]	DRAM_TYPE[1] 1=DDR 0=SDR (Default)
PIN_M_A3	DRAM_TYPE[0]_INV	DRAM_TYPE[0]_INV 1=DDR1 0=DDR2 (Default)
PIN_M_A12	sel_40m	1=40MHZ 0=25MHZ(default)

PIN_M_A9 PIN_M_A10 PIN_M_A11	ck_m2x_freq_sel[0] ck_m2x_freq_sel[1] ck_m2x_freq_sel[2]	ck_m2x_freq_sel[2:0] (MHZ) 000: 156.25 001: Reserved 010: Reserved 011: Reserved 100: Reserved 101: 125 110: Reserved 111: 168.75
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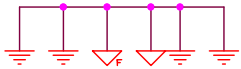
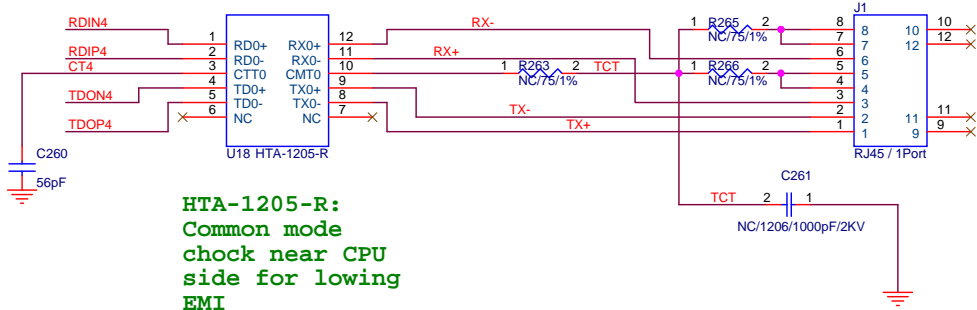
DDR_BS1 DDR_BS0 U0TXD	ck_cpu_freq_sel[3] ck_cpu_freq_sel[2] ck_cpu_freq_sel[1]	ck_cpu_freq_sel[3:1] (MHZ) 000: 400MHz 001: 380MHz 010: 360MHz 011: 340MHz 100: 320MHz 101: 300MHz 110: 280MHz 111: 260MHz
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Ethernet
Transformer



Title		
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MDI 10/100M		
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