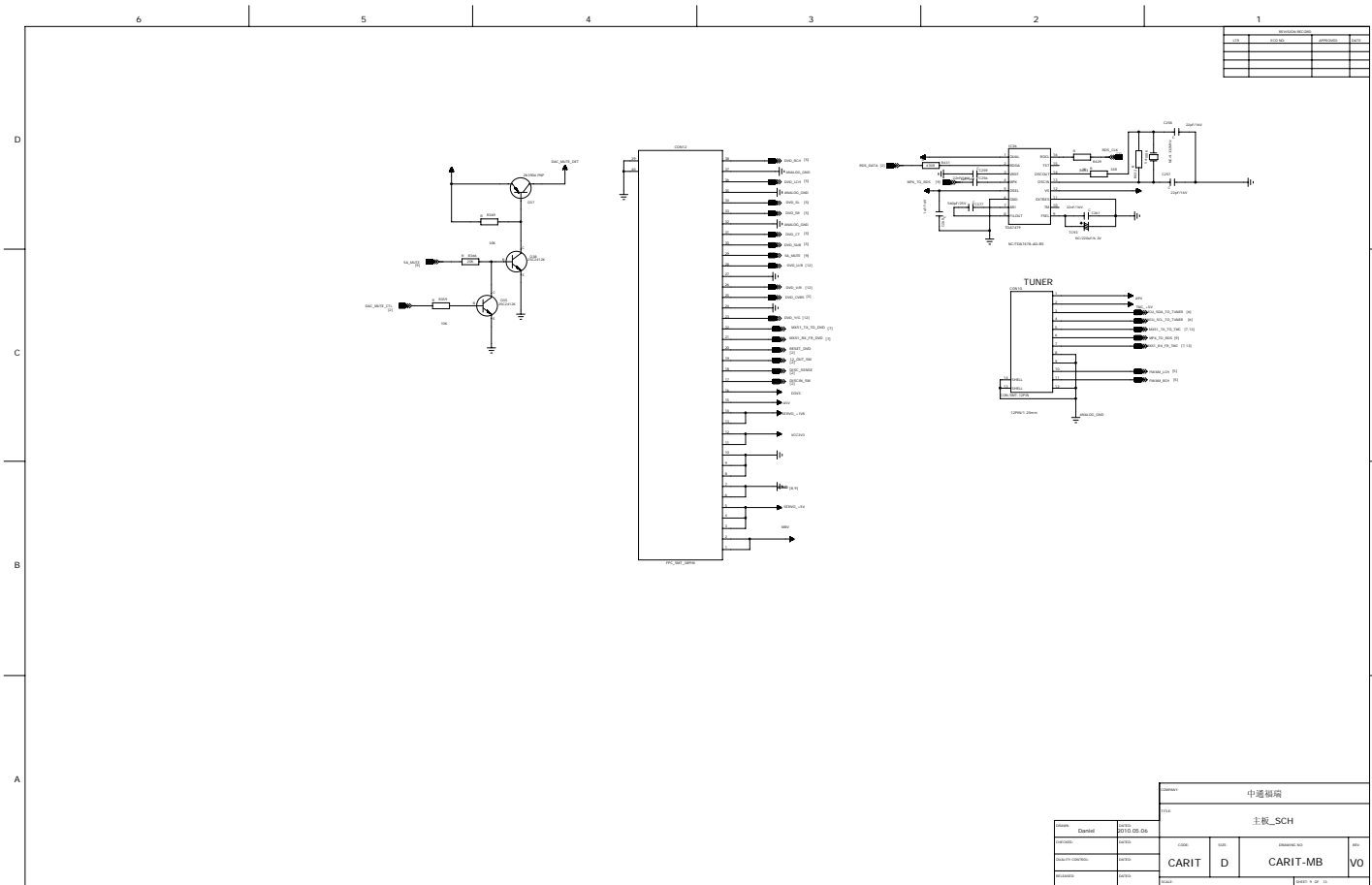


Revision History			
Rev	Rev No.	Rev Date	Rev Description
1	1.0	2023-10-10	Initial Release

中環編碼			
主板_SCH			
Rev	Rev No.	Rev Date	Rev Description
1	1.0	2023-10-10	Initial Release
2	2.0	2023-10-10	Initial Release
3	3.0	2023-10-10	Initial Release
4	4.0	2023-10-10	Initial Release
5	5.0	2023-10-10	Initial Release
6	6.0	2023-10-10	Initial Release
7	7.0	2023-10-10	Initial Release
8	8.0	2023-10-10	Initial Release
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10	10.0	2023-10-10	Initial Release
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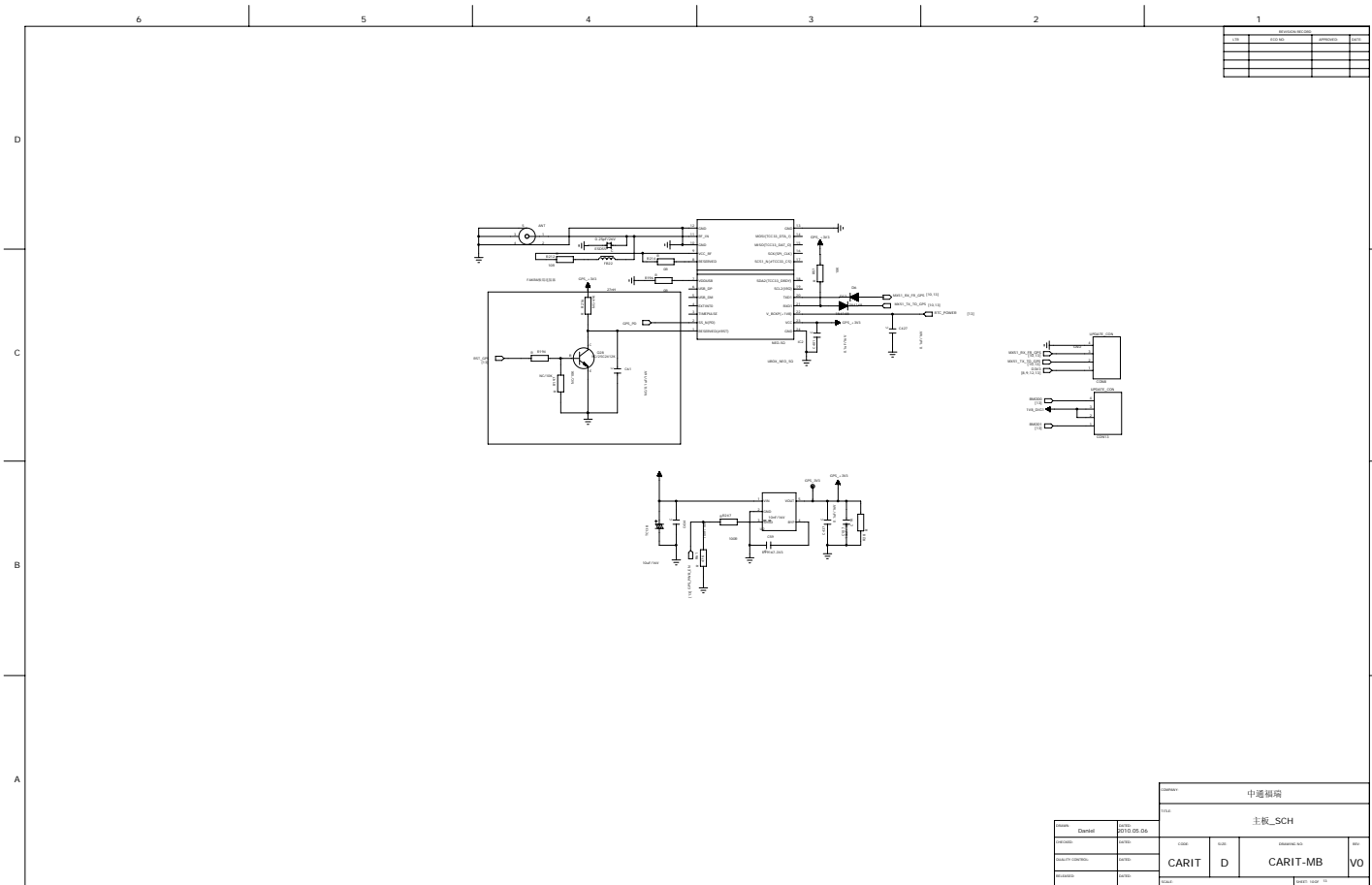


Component List			
Ref	Value	Package	Notes

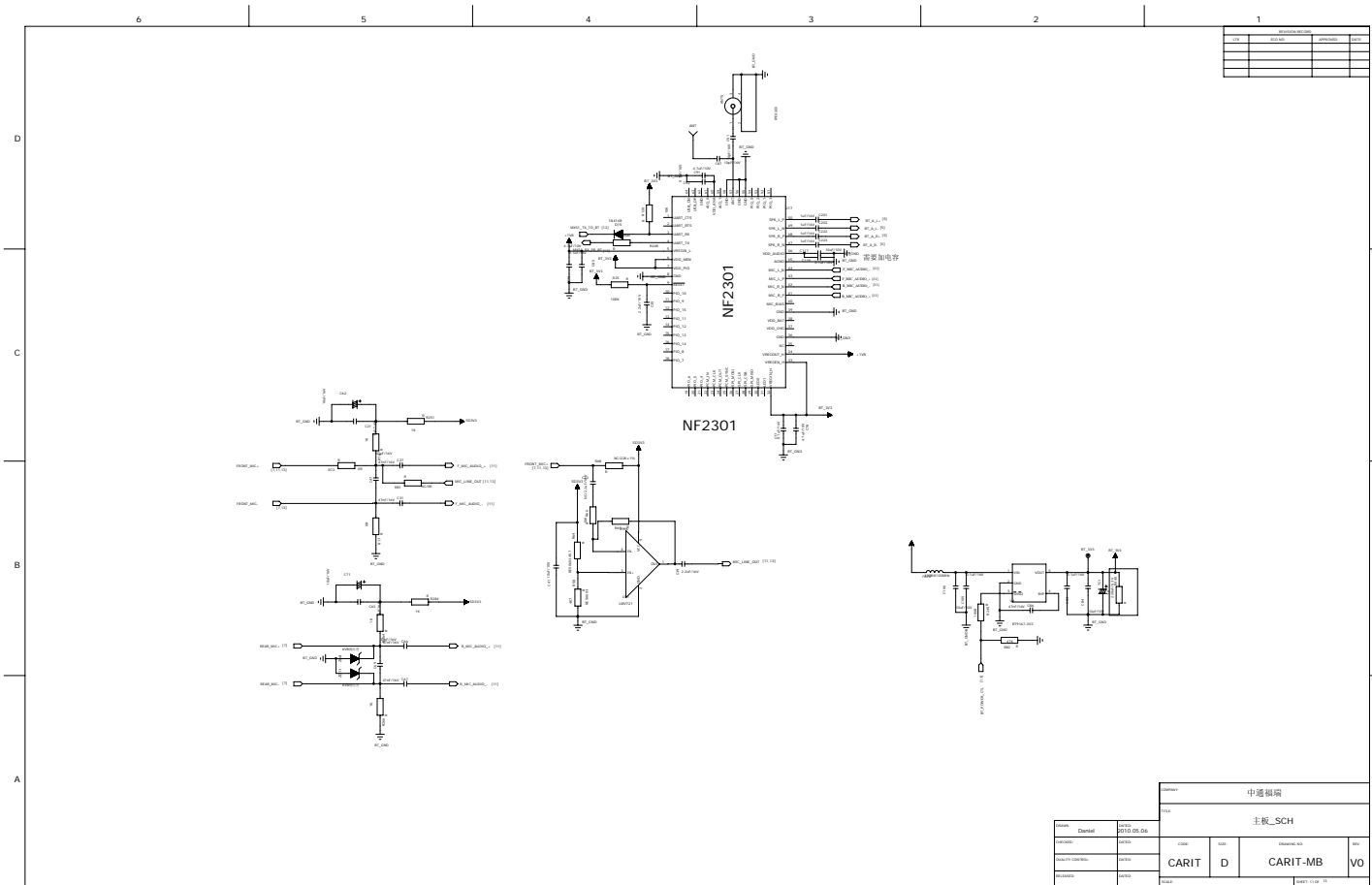
Component List			
Ref	Value	Package	Notes

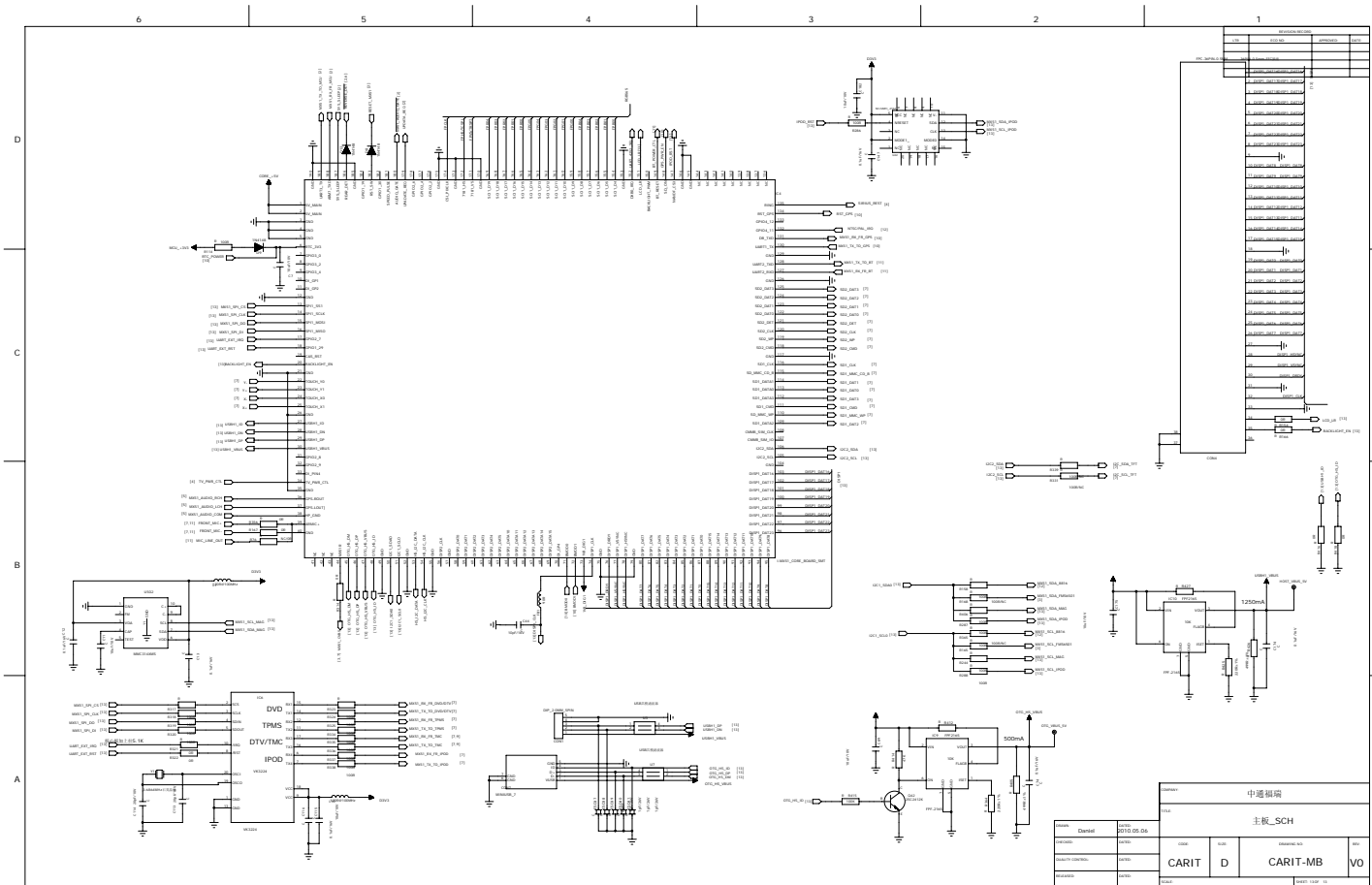
Test Points			
Ref	Value	Package	Notes

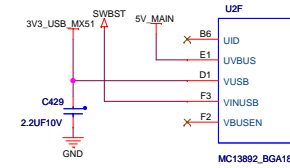
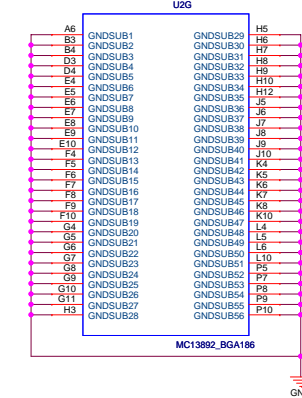
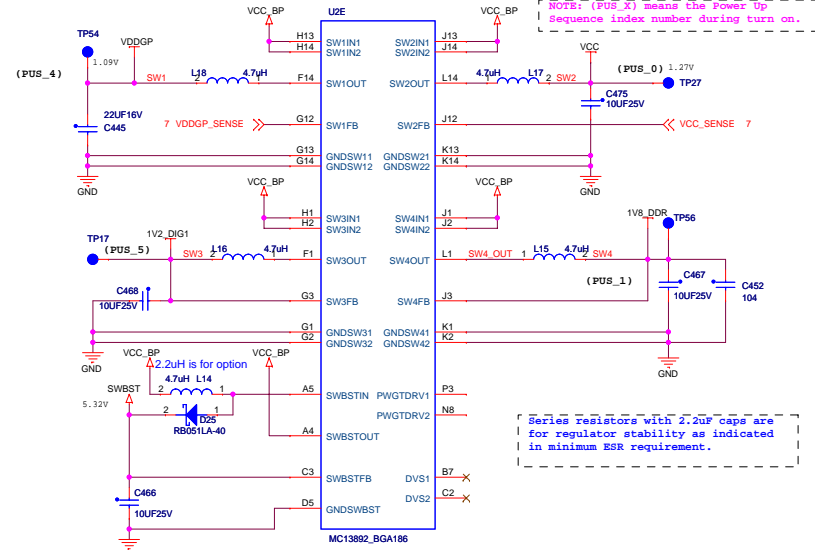
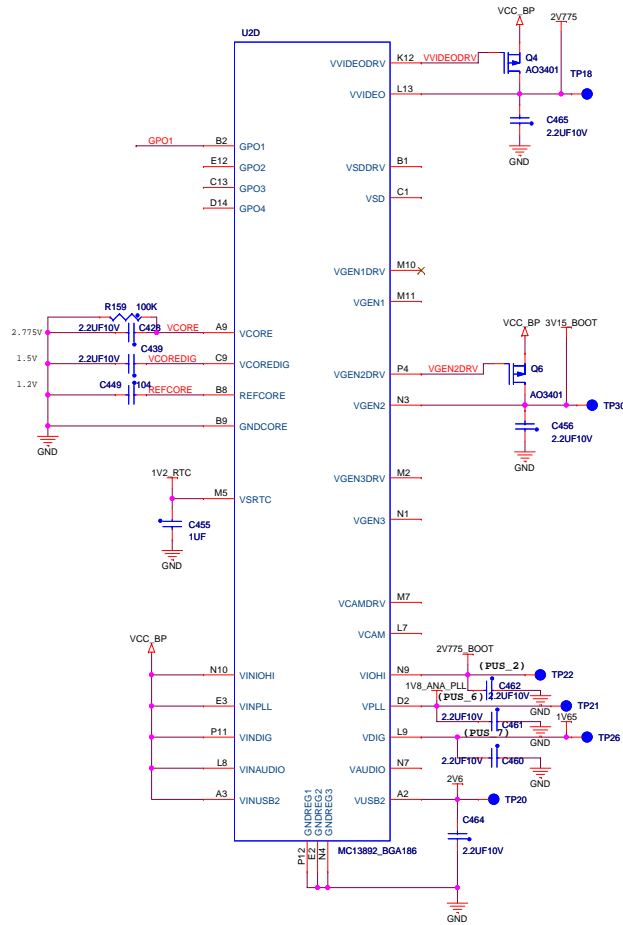
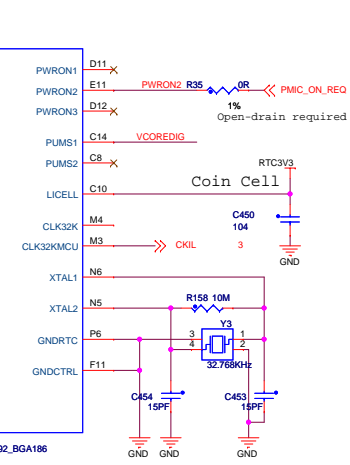
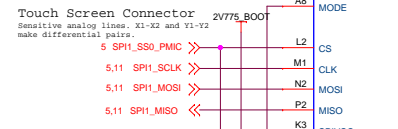
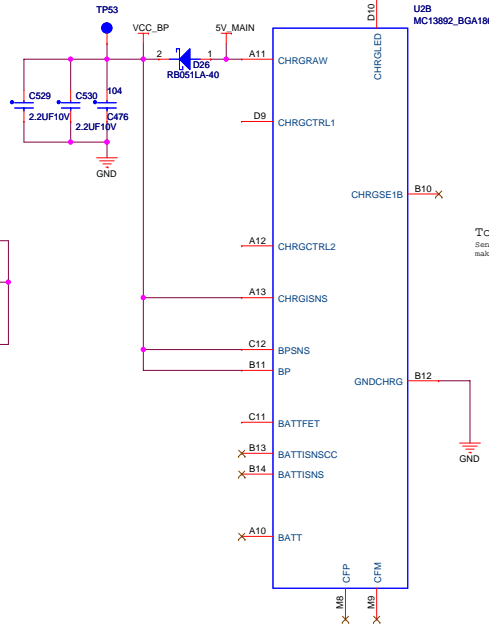
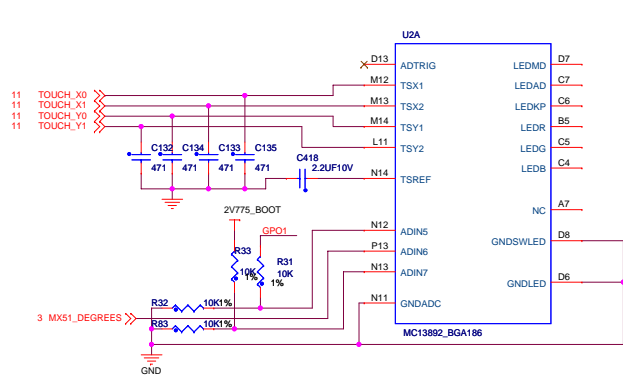
Test Points			
Ref	Value	Package	Notes



中環板端			
主板_SCH			
NAME	DATE	VERSION	REV
CARIT	D	CARIT-MB	V0
DESIGNER	DATE	CHECKED	







File	MC13892		
Size	Document Number	IMX51 MAIN BOARD	Rev 1.0
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NOTE:
To avoid bus contention, SPI1_SS2_EXP_HDR_EN_B and
SPI1_SS2_DISP_HDR_EN_B should NEVER be set to "0" at the same time.

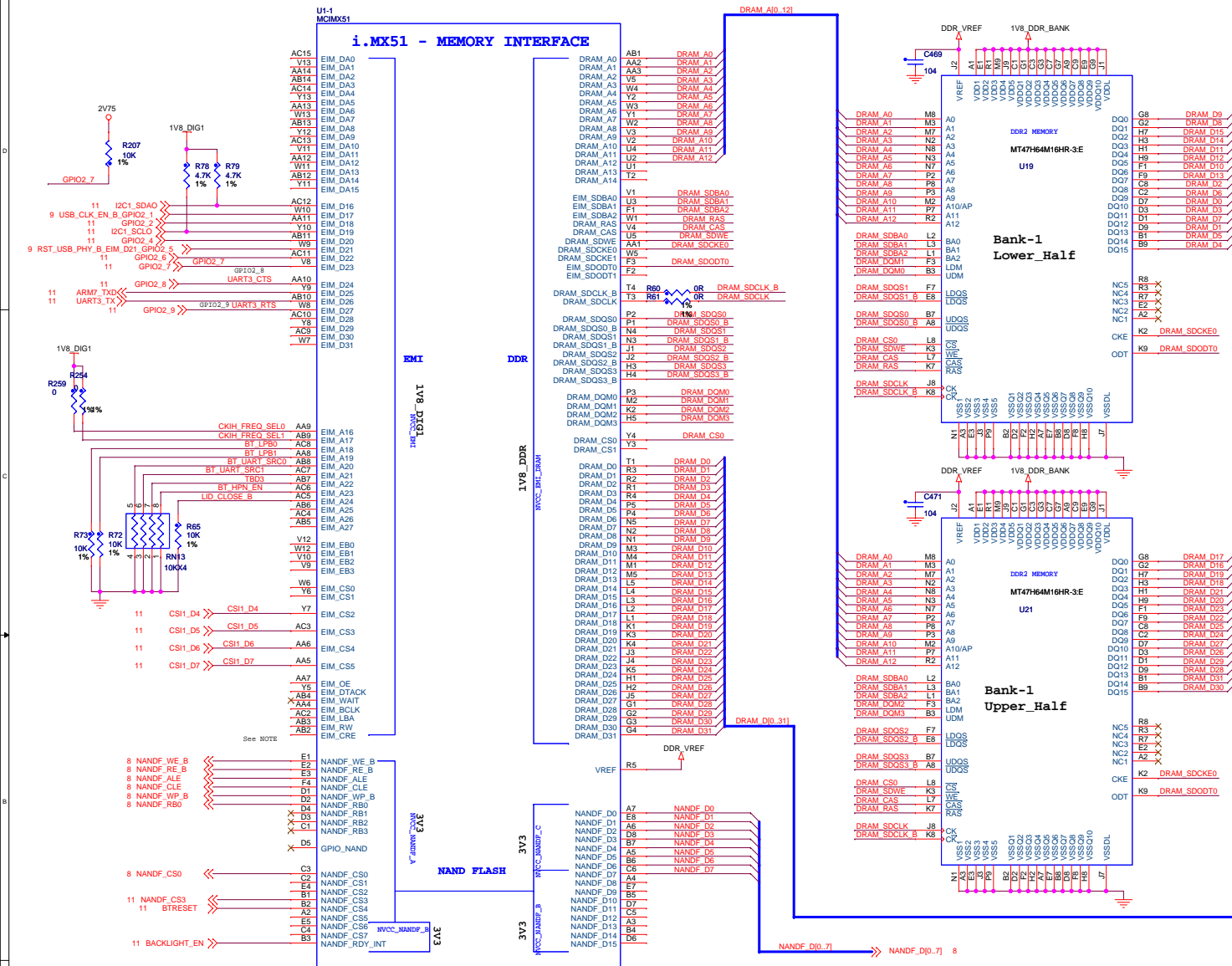
NOTE: DO NOT use EIM_CKE as GPIO as D11_PIN13 has the same GPIO assignment

DISP_CONTRAST_CTL is a PWM output and may be
used for LCD backlight brightness control.

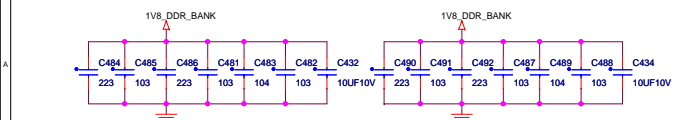
All Display signals are referenced to 2V775_BOOT

I2C-port-2 serves: DVI, USB, CODEC

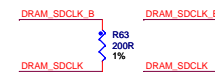
i.MX51 - MEMORY INTERFACE



NOTE: DO NOT use EIM_CRE as GPIO as DI1_PIN13 has the same GPIO assignment



```
| NOTE:                                     When swapping bytes 0 or 1
| DDR data pins can be                    into 2 or 3, must then use
| swapped for improved                    32 bit access.  Cannot use
| routing according to the                16-bit access.
| following rules:
|
| 1) Data pins can be swapped
| within each byte
| 2) Data bytes can be
| swapped
| 3) DQMx and DQSx must
| follow each byte
```



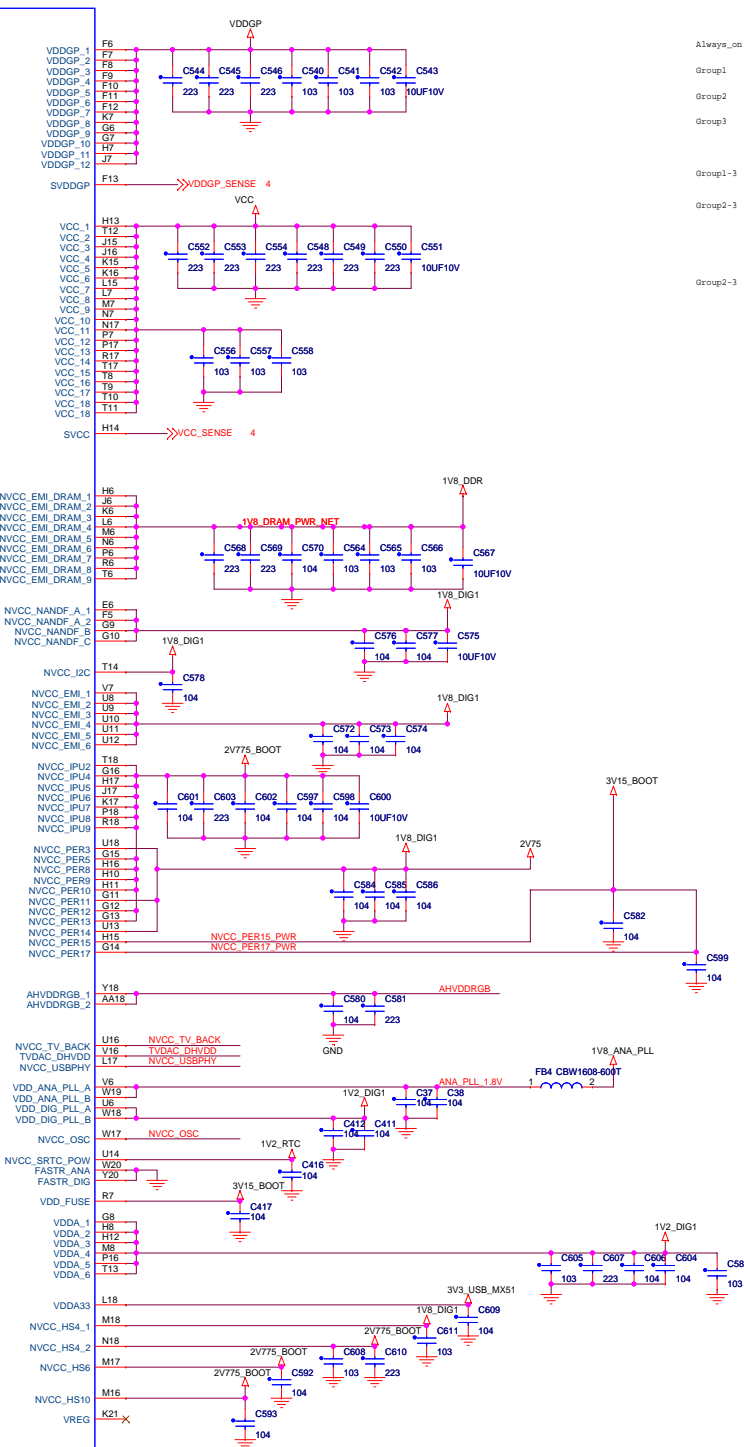
File				
MX51 MEMORY INTERFACE				
Size C	Document Number IMX51 MAIN BOARD			Rev 1.0
Date	Wednesday, January 05, 2011	Sheet	6	of 11

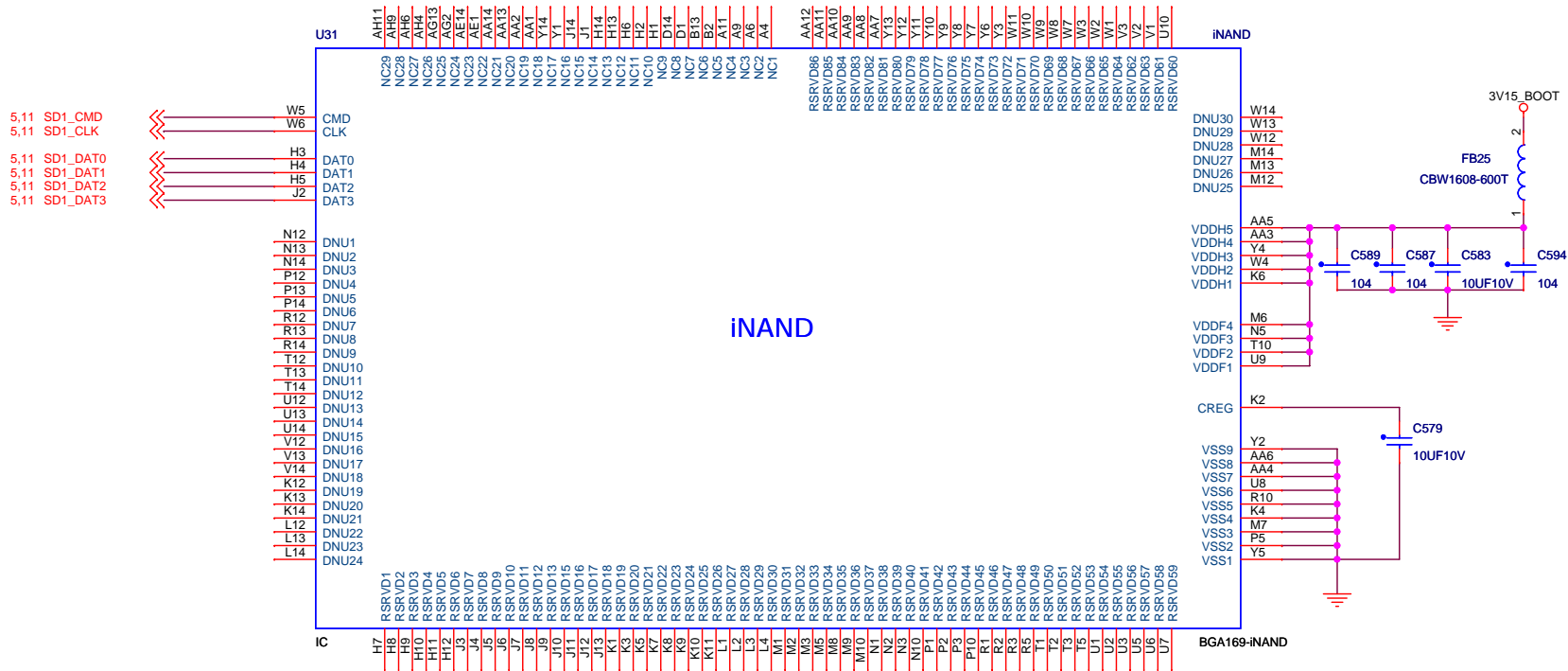
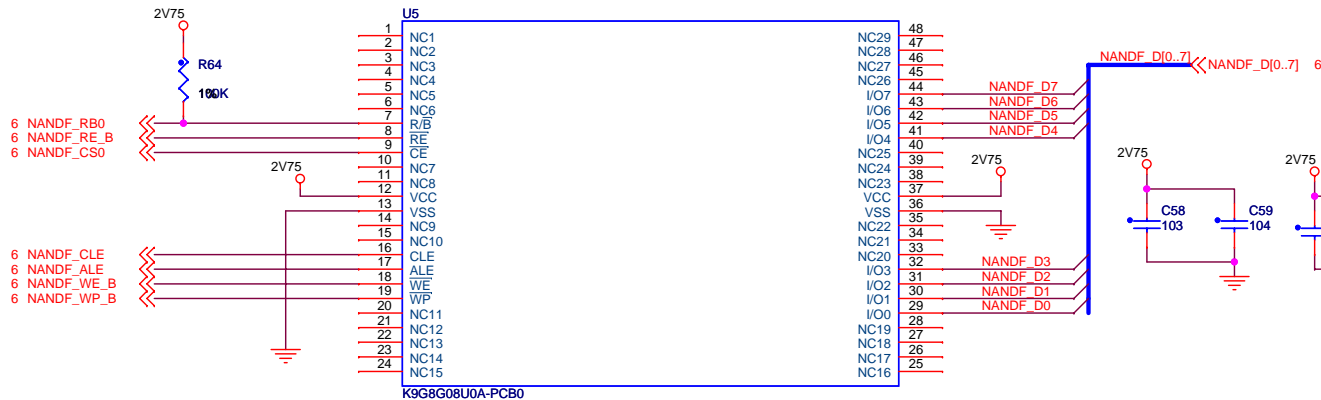
U1-5
MCIMX51

i.MX51 - POWER TO2.0 (19X19)

A1
A23
H8
GND.3
G5
GND.4
AC1
GND.5
GND.6
GND.7
GND.8
GND.9
GND.10
GND.11
GND.12
GND.13
GND.14
GND.15
K8
GND.16
GND.17
K10
GND.18
K12
GND.19
K13
GND.20
K14
GND.21
L8
GND.22
L9
GND.23
L11
GND.24
L12
GND.25
L13
GND.26
L14
GND.27
M8
GND.28
M10
GND.29
M11
GND.30
M12
GND.31
M13
GND.32
M14
GND.33
M15
GND.34
N8
GND.35
N9
GND.36
N10
GND.37
N11
GND.38
N12
GND.39
N13
GND.40
N14
GND.41
N15
GND.42
N16
GND.43
P8
GND.44
P9
GND.45
P10
GND.46
P11
GND.47
P12
GND.48
P13
GND.49
P14
GND.50
P15
GND.51
P16
GND.52
R8
GND.53
R10
GND.54
R11
GND.55
R12
GND.56
R13
GND.57
R14
GND.58
R15
GND.59
R16
GND.60
R17
GND.61
R18
GND.62
R19
GND.63

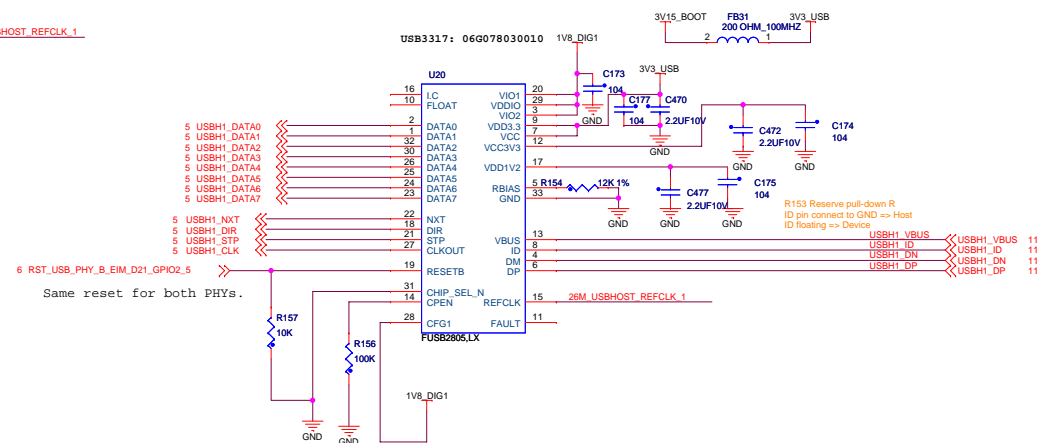
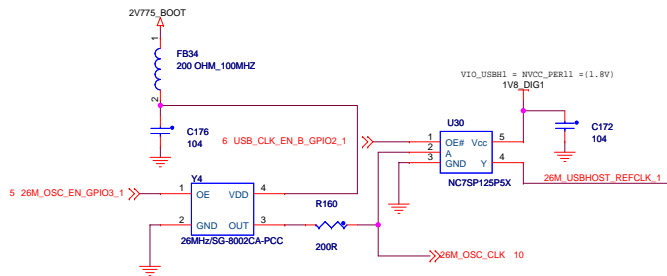
Y19
AA19
GND
T15
NGND_TV_BACK
L16
NGND_USBPHY
U7
U17
U18
V17
V18
V19
V20
V21
V22
V23
V24
V25
V26
V27
V28
V29
V30
V31
V32
V33
V34
V35
V36
V37
V38
V39
V40
V41
V42
V43
V44
V45
V46
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V48
V49
V50
V51
V52
V53
V54
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V56
V57
V58
V59
V60
V61
V62
V63
V64
V65
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V67
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V71
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V80
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V82
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V84
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V98
V99
V100



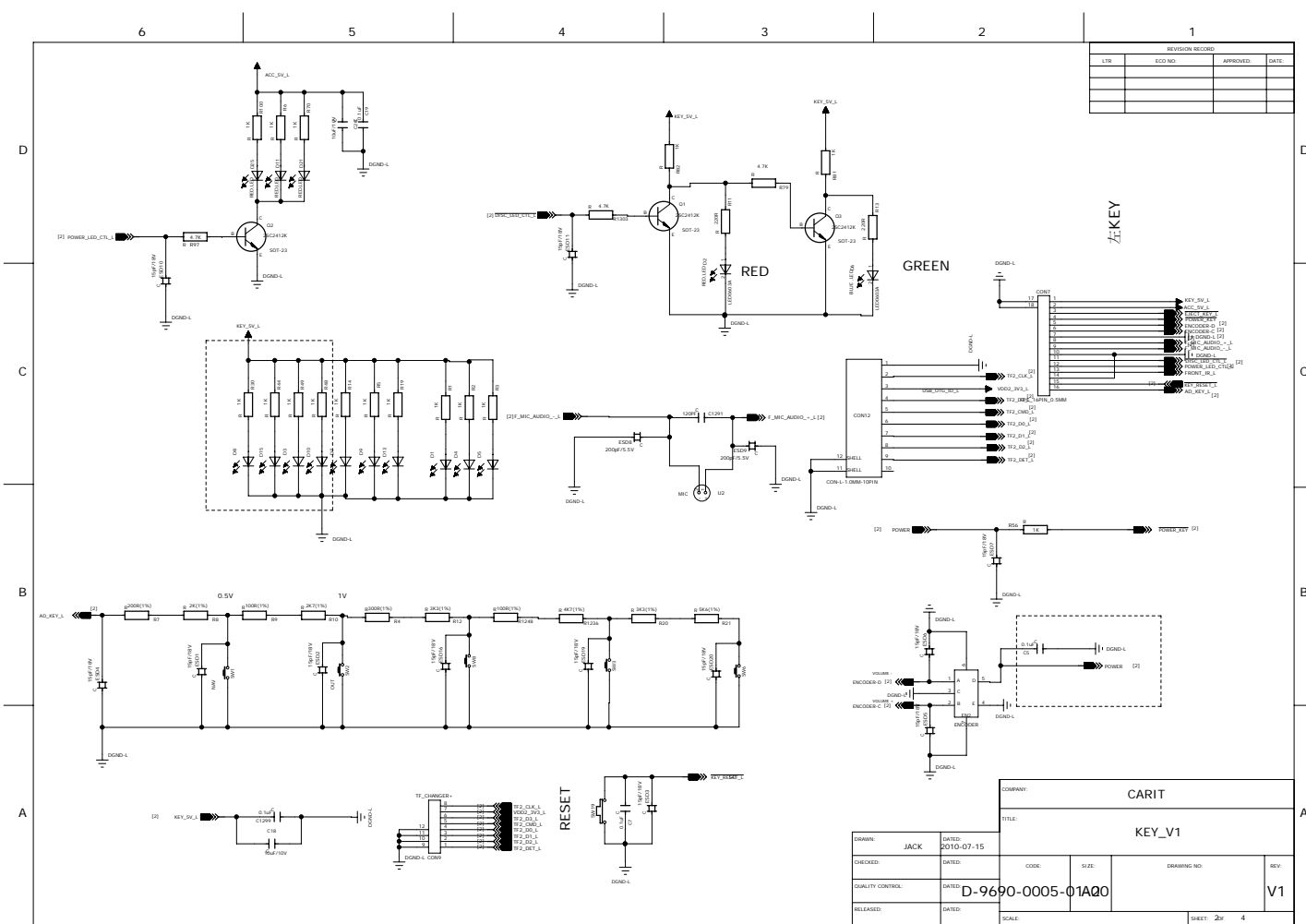


Title			
FLASH MEMORY			
Size B	Document Number	IMX51 MAIN BOARD	Rev 1.0
Date:	Tuesday, January 04, 2011	Sheet 8 of 11	

USB OTG PORTS

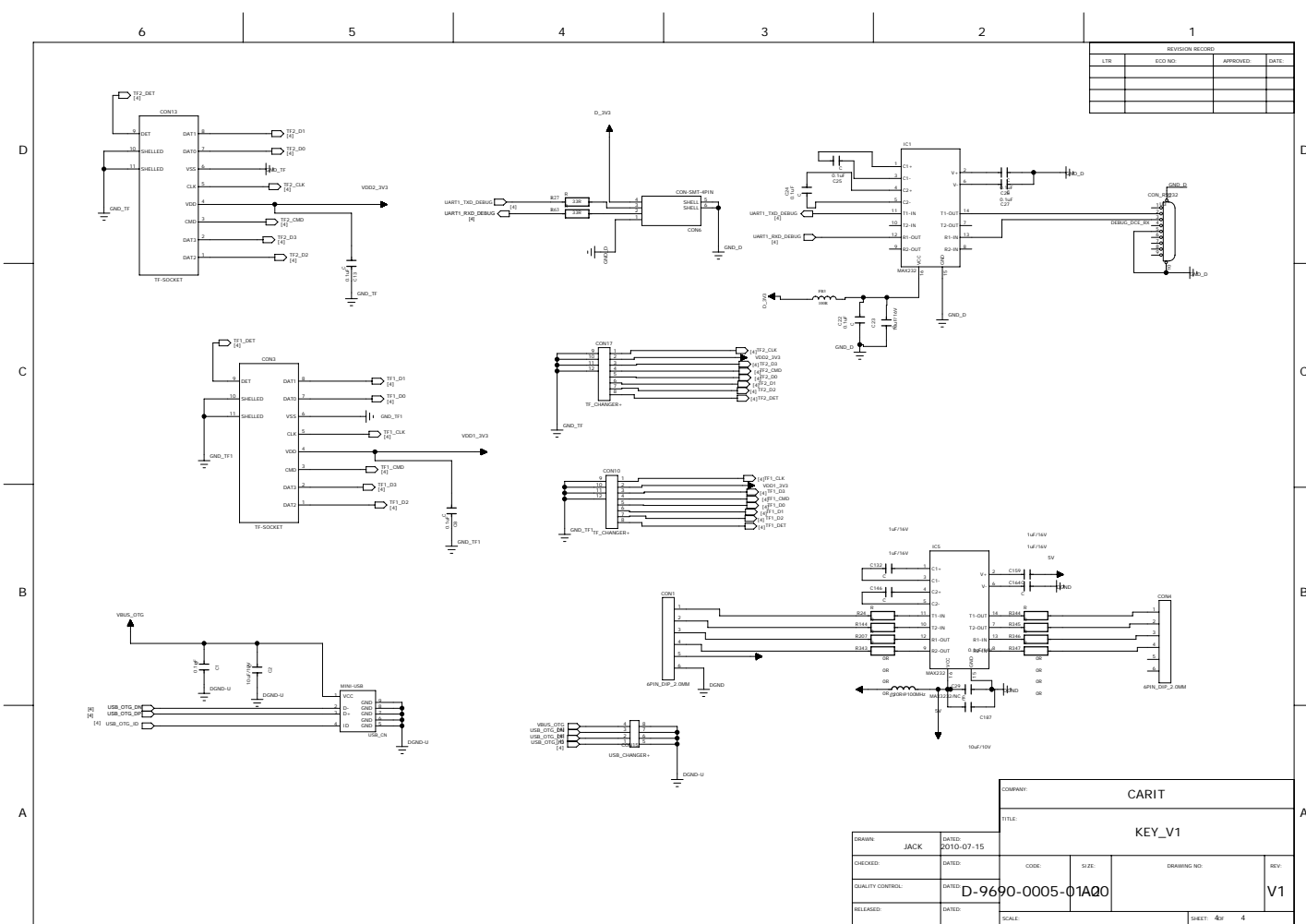


Title			
USB OTG			
Size	Document Number		Rev
C	IMX51 MAIN BOARD		1.0
Date:	Tuesday, January 04, 2011	Sheet	9 of 11



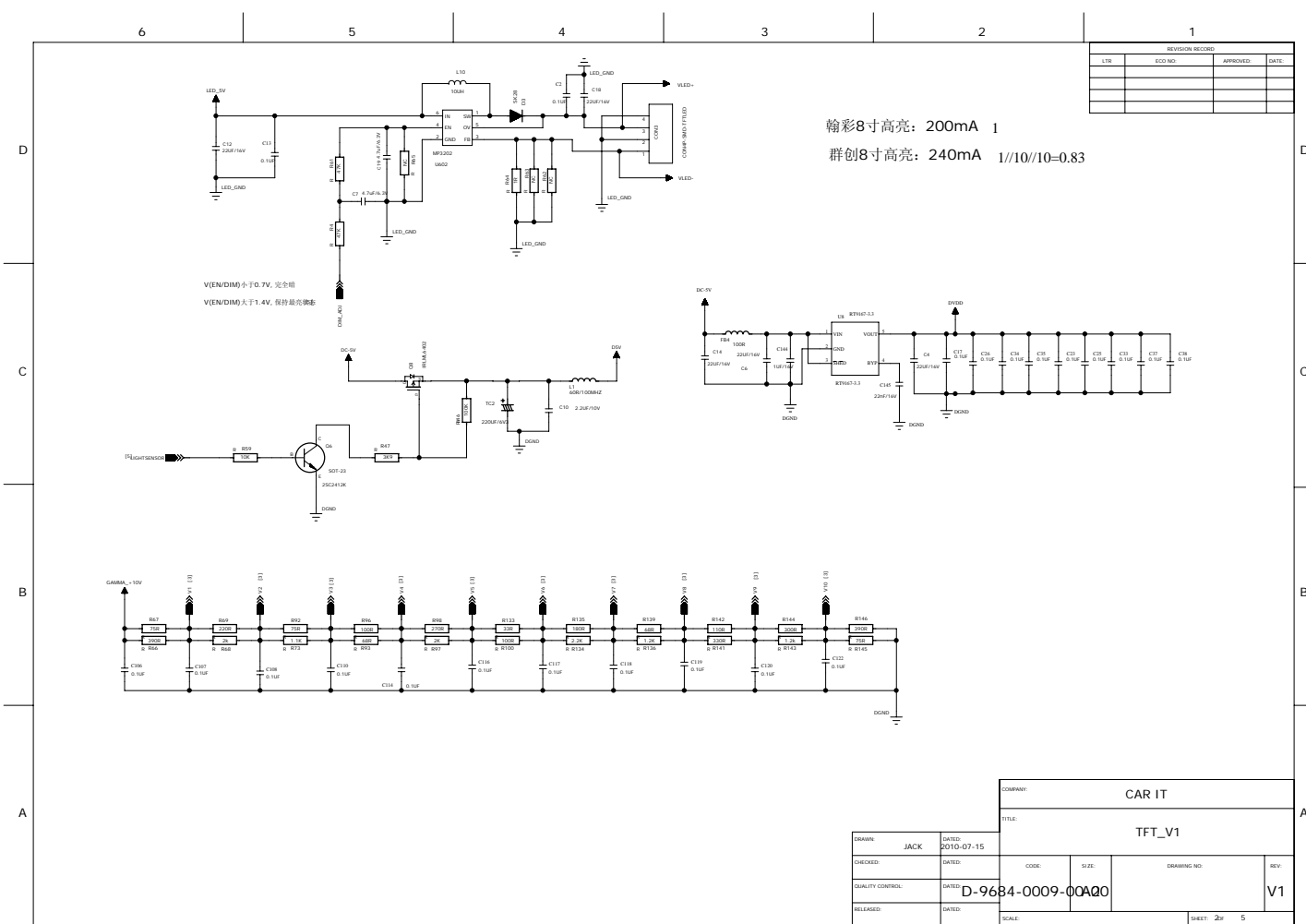
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QUALITY CONTROL:		DATED:		SCALE:		SHEET: 2x 4	
RELEASED:		DATED:		SCALE:		SHEET: 2x 4	

COMPANY: CARIT	
TITLE: KEY_V1	



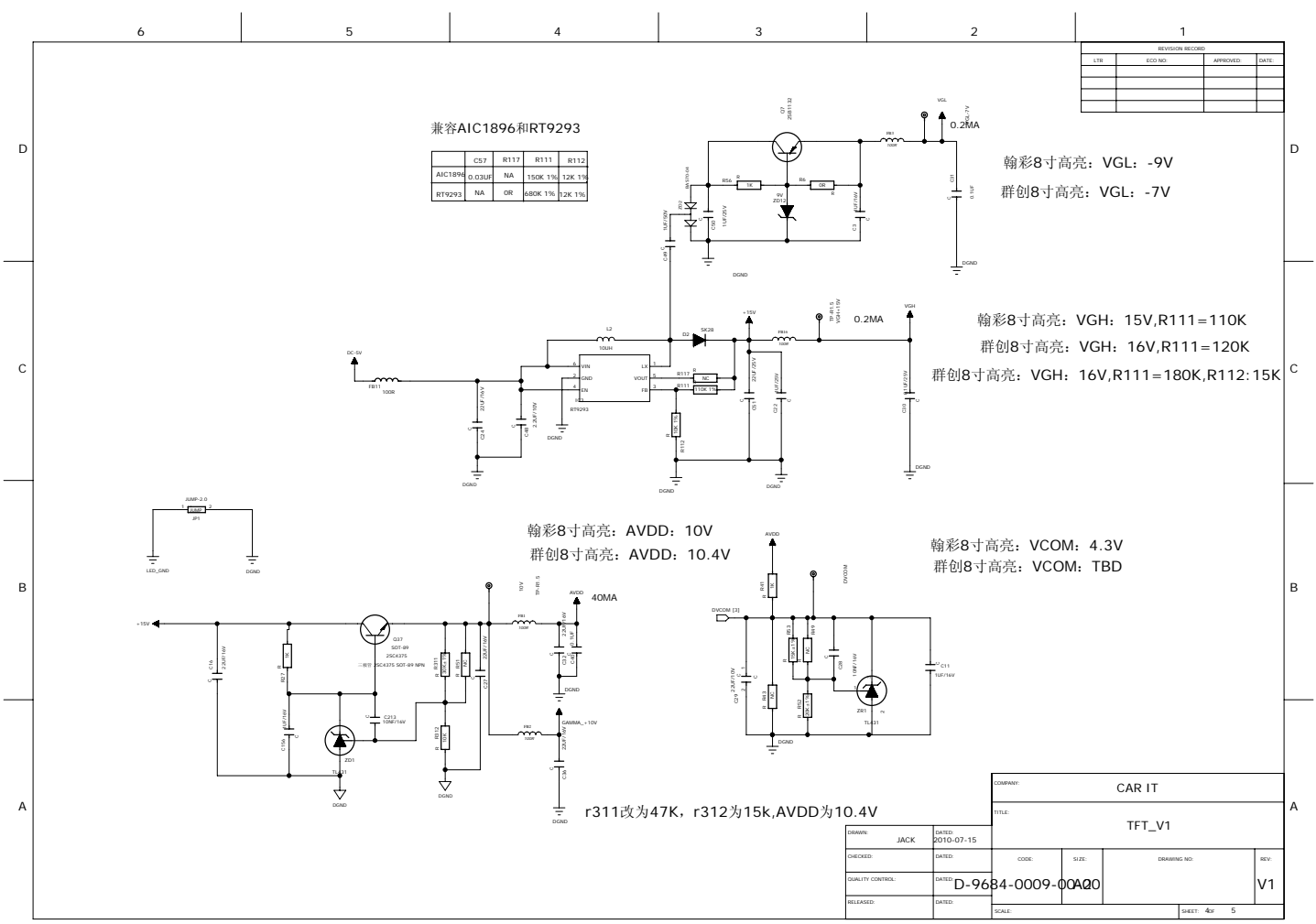
REVISION RECORD			
LT#	ECO NO.	APPROVED	DATE

COMPANY: CARIT			
TITLE: KEY_V1			
DRAWN: JACK	DATED: 2010-07-15	CODE:	SIZE:
CHECKED:	DATED:	DESIGNING NO.	REV: V1
QUALITY CONTROL:	DATED: D-9690-0005-01A20		
RELEASED:	DATED:	SCALE:	SHEET: 4x 4



翰彩8寸高亮: 200mA 1
群创8寸高亮: 240mA 1/10/10=0.83

DRAWN: JACK		DATED: 2010-07-15		COMPANY: CAR IT			
CHECKED:		DATED:		TITLE: TFT_V1			
QUALITY CONTROL:		DATED: D-9684-0009-00A20		CODE:	SIZE:	DRAWING NO:	REV: V1
RELEASED:		DATED:		SCALE: SHEET: 2/5			



REVISION RECORD			
LT#	ECO NO.	APPROVED	DATE

COMPANY: CAR IT			
TITLE: TFT_V1			
DRAWN: JACK	DATE: 2010-07-15	CODE	SIZE
CHECKED:	DATE:	DRAWING NO.	
QUALITY CONTROL:	DATE:	D-9684-0009-00A20	
RELEASED:	DATE:	V1	
SCALE		SHEET: 4/5	

