DBB

MEMORY

CAMERA

RF DIGITAL IO

JTAG

IIS

SPI0

SPI1

UART

TOUCH PANEL

GPIO

SYSTEM CONTROL

KEYPAD

[5] EMD[0:15]

EMD0	A18	EMD[0]
EMD1	A17	EMD[1]
EMD2	C17	EMD[2]
EMD3	D18	EMD[3]
EMD4	E17	EMD[4]
EMD5	E18	EMD[5]
EMD6	H17	EMD[6]
EMD7	G18	EMD[7]
EMD8	B17	EMD[8]
EMD9	B18	EMD[9]
EMD10	C18	EMD[10]
EMD11	D17	EMD[11]
EMD12	F17	EMD[12]
EMD13	G17	EMD[13]
EMD14	F18	EMD[14]
EMD15	H18	EMD[15]

[5] EMA[0:22]

EMA0 ... EMA22 / EMCEN4

[5] EMABL — M16 — EMLBN
[5] EMABH — L16 — EMUBN
[5,7] EMAOEN — K16 — EMOEN
[5,7] EMAWEN — P16 — EMWEN
[5] FLASH_CSN0 — G16 — EMCEN[0]
[5] SRAM_CSN1 — H16 — EMCEN[1]
[7] LCM_CSN — H13 — EMCEN[2]/GPIO_21
— J12 — EMCEN[3]/GPIO_22
[5] EM_ADVN — G12 — EMADVN/GPIO_45
[5] EMWAIT — F12 — EMWAIT/GPIO_55
— P13 — EMCRE
[5] FLASH_RSTN — N16 — EMRSTN/GPIO_23
[5] EMCLK — V15 — EMCLK/GPIO_44
[7] LCM_RSTN — U15 — LCMRSTN/GPIO_20

TP101

[8] CAM_MCLK — A12 — CCIRMCK/CLK32K
[8] CAM_PWDN — C12 — CAM_FD/GPIO_26
[8] CAM_RST — B12 — CAM_RSTN/GPIO_27
[8,13] CAM_PCLK — A16 — CCIRCK
[8,13] CAM_HS — B16 — CCIRHS
[8,13] CAM_VSYNC — A15 — CCIRVS
[8,13] CAM_D0 — B15 — CCIRD[0]
[8,13] CAM_D1 — C15 — CCIRD[1]
[8,13] CAM_D2 — C14 — CCIRD[2]
[8,13] CAM_D3 — B14 — CCIRD[3]
[8,13] CAM_D4 — A14 — CCIRD[4]
[8,13] CAM_D5 — A13 — CCIRD[5]
[8,13] CAM_D6 — B13 — CCIRD[6]
[8,13] CAM_D7 — C13 — CCIRD[7]
[8,12,13] SDA — D16 — SDA/GPIO_25
[8,12,13] SCL — E16 — SCL/GPIO_24

CAUTION: I2C has been pulled up by SC6600L, don't pull up it again

U100-A
ASIC:SC6600L1-224

GPIO64 no interrupt function

RF DIGITAL IO:
B11 — XTLEN/GPIO_29
B10 — RFSDA/GPIO_29 — RFSDA [4]
B9 — RFSCK/GPIO_30 — RFSCK [4]
A10 — RFSEN/GPIO_31 — RFSEN [4]
G9 — RF_PAEN/GPIO_37 — PA_EN [4]
G10 — RF_PAMODE/GPIO_36 — LED_R_EN [10]
P8 — RF_PABAND/GPIO_32 — LED_G_EN [10]
P9 — RF_VBS3/GPIO_35 — VBS3 [4]
F9 — RF_VBS2/GPIO_34 — VBS2 [4]
F7 — RF_VBS1/GPIO_33 — VBS1 [4]

TP108 TP106 TP107 TP105 TP109

JTAG:
M9 — MTMS/GPIO_38 — EXSP_SEL [9]
M10 — MTDI/GPIO_39 — R101 — CAM2_PWDN [8] — 3.14Ω0%0
N10 — MTCK/GPIO_40 — EXSP_DET [9]
N11 — MTDO/GPIO_66
M12 — MTRSTN/GPIO_67 — TORCH_EN2[9]

IIS:
V8 — IISDO — BT_PCM_DO [12]
U8 — IISDI — BT_PCM_DI [12]
U9 — IISCLK — BT_PCM_CLK [12]
U9 — IISLRCK — BT_PCM_SYNC [12]
V7 — IISMCK/CLK32K — FM_32K [12]

SPI0:
V11 — SPI0_DO/U1TXD — SPI0_DO [11]
V10 — SPI0_DI/U1RXD — SPI0_DI [11]
U10 — SPI0_CLK/GPIO_43 — SPI0_CLK [11]
U11 — SPI0_CS0 — SPI0_CSN [11]
T9 — SPI0_CS1/GPIO_50 — BT_RST [12]

SPI1:
M7 — SPI1_DO/U1TXD
N7 — SPI1_DI/U1RXD
N9 — SPI1_CLK/GPIO_46 — CAM_FLASH [11]
N8 — SPI1_CS0/GPIO_47

TP104 TP100

UART:
V5 — U0TXD — U0TXD [12]
U6 — U0RXD — U0RXD [12]
U7 — UCCTS/U1RXD/GPIO_48 — U1RXD [9]
V6 — U0RTS/U1TXD/GPIO_49 — U1TXD [9]

UOCTSN is used as U1RXD
UORTSN is used as U1TXD and NBOOT

TOUCH PANEL:
C10 — TP_XL/GPIO_51 — TP_XL [7]
C9 — TP_XR/GPIO_52 — TP_XR [7]
B7 — TP_YU/GPIO_53 — TP_YU [7]
B8 — TP_YD/GPIO_54 — TP_YD [7]

GPIO:
V3 — FM_RST/GPIO_16 — T_CARD_EN [11]
T4 — FM_EN/GPIO_17 — BT_LDO_EN [12]
V4 — PA_AUDIO_EN/GPIO_41 — PA_AUDIO_EN [6]
U4 — SD_DET/GFL_0/PWM0 — ASEN_INT2 [11]
T5 — FLIP_ON/GFL_1 — ASEN_INT1 [11]
U5 — GPIO_18 — TV_RST [13]
T6 — BT_XTL_REQ/GPIO_70 — BT_CLK_REQUEST [12]

SYSTEM CONTROL:
U3 — LDOMEMB — LDOMEMB [5]
U2 — LDOIOB — LDOIOB [5]
T3 — PRODTEST
T2 — PBINT — PBINT [10]
T10 — PWM/CLKAUX/GPIO_65 — TVPWR_EN [13]
N12 — CLK_AUX/CLKDSP/GPIO_68 — TORCH_EN[9]

KEYPAD:
U12 — KEYOUT[6]/GPIO_64 — S_SCL [11]
U13 — KEYOUT[5]/GPIO_19 — KEYOUT5 [10]
T14 — KEYOUT[4] — KEYOUT4
V14 — KEYOUT[3] — KEYOUT3
U14 — KEYOUT[2] — KEYOUT2
U13 — KEYOUT[1] — KEYOUT1
V13 — KEYOUT[0] — KEYOUT0
T15 — KEYIN[0] — KEYIN0
T16 — KEYIN[1] — KEYIN1
R16 — KEYIN[2] — KEYIN2
N13 — KEYIN[3] — KEYIN3
M13 — KEYIN[4]/GPIO_62 — KEYIN4
L13 — KEYIN[5]/GPIO_60 — S_SDA [11]

WHTLED

RF

AUDIO

ADC

CLK

SIM0

SIM1

RTC

USB

U100-B
ASIC-SC6600L1-224

[7] LED4- WHTLED_IR3
[7] LED3- WHTLED_IR2
[7] LED2- WHTLED_IR1
[7] LED1- WHTLED_IR0
WHTLED_RSET
R201 6.2K

MICBIAS

VCOM
MICBIAS
[6,9] HEAD_MIC HEADMIC_IN
R231 1K
AUXMICP
[6] AUXMICN
[6,13] MICP
[6,13] MICN
[12,13] AIL1
[12,13] AIR1
[6] HEADPHL
AGM
HEADPHR
[6] HEAD_P_L
[6] AUXSPP
[6] AUXSPN
[6] RECN
[6] RECP
HEAD_P_R
AUXSPP
AUXSPN
EARN
EARP

10uF C211
1uF C205
C200 100nf

TXQN RX/TXQN [4]
TXQP RX/TXQP [4]
TXIP RX/TXIP [4]
TXIN RX/TXIN [4]
PAGSM RF_RAMP [4]
TXREF C209 470pF
ADCI0
ADCI1
MCLK C202 1nF CLK_26M [4,12]

SIMRSTO SIMRST [11]
SIMCLKO SIMCLK [11]
SIMDAO SIMDA [11]
VSIM0

VSIM1
SIMDA1 SIM2DA [11]
SIMCLK1 SIM2CLK [11]
SIMRST1 SIM2RST [11]

RTC32KI 32K_IN [2]
RTC32KO 32K_OUT [2]
VDDRTC
VBATBK

VSIM
VSIM2
C208 1uF C206 1uF
RTCVDD
VBACKUP
10nF C201
R206 10K
4.7uF C210

[9] USB_DP R202 33 PADP_USB
[9] USB_DM R203 33 PADM_USB
R208 1.5K VDDUSB
VDD_USB
1uF C207

X100 32.768KHZ
22PF C204
22PF C203

32K_IN
32K_OUT

CHARGE

Q301

TRANSISTOR-PWR_MOS-TPCP8801

[3] ISENSE

VCHG

should afford 1A current

[3] CHG_DRV

100nf
C330

R300
0.3Ω

VBAT

should afford 1A current

All trace connectted to the MOSFET should be as wide as possible

BAT CON.

J301

VBAT

VB300
C311
C312
22uF
47pF

TVS-ESD625.0T1G
TVS301

VBAT

C303 10uF
C300 10nF

AVDDVBO

C307
2.2uF

AVDDBB

C308
2.2uF

VBAT

VDDRF

C304
2.2uF

VCAM_CORE

VCAM_ANA

C301
1uF

C302
1uF

VCHG

[3] ISENSE

[3] CHG_DRV

[11] VIBR_CTRL

[10] KPLED_CTRL

AVDD36_1

B2 | AVDD36_1
C1 | AVDDVBO
J1 | AVDDVB
H3 | AGNDVB
J2 | VDDAO
J3 | VSSAO

AVDD36_2

A1 | AVDD36_2
A3 | AVDDBB
C4 | AGNDBB
A2 | AVDDRF

AVDD36_3

C3 | AVDD36_3
F11 | AVDDAUX1
F10 | AVDDAUX2

CHARGE

K3 | ISENSE
K2 | VCHG
L3 | VDRV

L6 | VIBR_OUT
M6 | VIBR_VSS
N6 | KPLED_OUT

U100-C
ASIC-SC6600LI-224

VBAT

R3 | VBAT
K13 | VBAT
A5 | VBAT
B4 | VBAT

T7 | VDD
T12 | VDD
F10 | VDD
A6 | VDD

J13 | VMEM
A4 | VMEM

T9 | VDDIO
A9 | VDDIO

A11 | VCAM

V1 | NC

VBAT

VDD_CORE

C309 2.2uF

VMEM

C305 2.2uF

VDDIO

C310 2.2uF

VCAM_ANA

GND

K12 | VSSIO
V2 | VSSIO
C16 | VSSIO
C6 | VSSIO
U1 | VSS
T11 | VSS
G13 | VSS
C11 | VSS
C7 | VSS
G7 | VSS
J9 | VSS
J10 | VSS
K9 | VSS
K10 | VSS

Dual band NF part:

U401£¬U403£¬C432£¬C433£¬C424£¬C451
C416£¬C417£¬C412£¬C454,L402,L407

GSM ANT: GSM850: 824-849(Tx) 869-894(Rx)

PCS1900: 1850-1910(Tx) 1930-1990(Rx)

DUAL band U402 will be RF7168,R411
QUAD band U402 will be RF7163,R412

VDDRF
C420 1uF
C406 100pF
VDDIO

[2,12] CLK_26M

CLK_IN
REF_26/13B
REF_OUT
VDD_DCXO
REF_IN+
REF_IN-
DCXO_REG
TEMP_OUT
VDD_IO
LE
GND

U404

CLK
DATA
NC
VDD_SYN
VDD_SYN_REG
VDD_LO
VDD_RF
PA_VCTRL
PA_VCTRL
USGSM_TX

[1] RFSEN
[1] RFSCK
[1] RFSDA

VDDRF

TP402 TP401 TP401

C449 NF
C421 1uF
C422 1uF

100pf
C400

VDDRF
RX_CAP_I-
RX_CAP_I+
RX_CAP_Q-
RX_CAP_Q+
VDD_BB2
VDD_BB3

USGSM_RX+
USGSM_RX-
EGSM_RX+
EGSM_RX-
DCS_RX+
DCS_RX-
PCS_RX+
PCS_RX-
VDD_RF2
DCS/PCS_TX

L402 10nH
C432 2.2PF
C433 2.2PF

OUTPUT1 U401 INPUT
OUTPUT2 GND

RF-SAW-SAFEB881MFL0P00

NF C441
33PF C424

L403 10nH
C405 2.2PF
C404 2.2PF

EGSM_OUT EGSM_IN
EGSM_OUT
DCS_OUT
DCS_OUT DCS_IN
U405
GND GND

L406 6.2nH
C414 1.9PF
C415 1.9PF

RF-SAW-SAFEH1Q96FA0P00

L407 6.2nH
C416 1.9PF
C417 1.9PF

OUTPUT1 U400 INPUT
OUTPUT2 GND

33PF C410
33PF C411

0.5PF C453

C419
C418

0.5PF C481
0.5PF C482

NF C442
33PF C412

0.5PF C454

R421
E401 ANTENNA

RF-IN J403
GND1 GND2
GND3 ANT

NF

27pF C451
50PF C419
3P
L408
L401 10nH

ANT400
E402 ANTENNA

LC-RF-SW-PA-RF7163

GND
GND
GND
GND
GND
GND
GND
GND
GND
GND

RX1
RX2
RX3
RX4

ANT

VBATT

VBATT
GSM_IN
DCS/PCS_IN

C413 100pF
C436 10pF
C437 10uF

C455 56pF

L405
6.2nH
C425 3.3PF
C426 3.5PF

RFTX_LB [4]

L404
2.4nH
C429 3.3PF
C424 3.3PF

RFTX_HB [4]

22PF C456

VBAT

VDDRF
L400
10nH

100pF C407
4.7PF C418
10PF C401
1uF C423

100pF
C409

RFTX_HB [4]
RFTX_LB [4]

VDDRF R411 0
[3,4]

VBS3
VBS1
[1]
[1] VBS2

R412
0

GATCTRL2
GATCTRL1
GATCTRL0
TX_EN
VRAMP

PA_EN [1]

C430 NF

C434 NF
C408 NF
NF C427

RF_RAMP RAG1 R461
[2] 15R

C435 47pF

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NOR+pSRAM

when stl36l0t7050 R521 0R,R522 NC
else R521 NC,R522 0R

Strapping List

R522
0
VMEM [3,5,7]

R521
10F
VDD_CORE [3]

[5] VDDF

100nF
C502

100nf
C501
VMEM [3,5,7]

EMA22
[1,5]
emc_admux_en: 0:ADM
                1:ADP(Default)

R502
0R

EMD[0:15]

U501
IC-TV00578002AA8D

EMA[0:22] [1]

VBAT

R503
0R

LDOMEMB
[1]

R505
0

LDOMEMB: 0:VDDMEM 3V
                1:VDDMEM 1.8V

VBAT

R504
0R

LDOIOB
[1]

R506
0

LDOIOB: 0:VDDIO 2.8V
                1:VDDIO 1.8V

EM_ADVN VDDF
[1]
[5]

VMEM [3,5,7]

R513
0R

VMEM [3,5,7]

VMEM [3,5,7]

R511
10K

EMWAIT [1]

R512
0
VMEM [3,5,7]

VDDF [5]
VMEM [3,5,7]

R510
0R

EMA22 [1,5]

VMEM [3,5,7]

CAUTION: MCP signals have higher speed than before, layout carefully

MAIN MIC

EAR-MIC

RECEIVER

HEADPHONE

Class AB

MICBIAS

R606
510

C612
NF

R604
1.5K

C816
NF

[2,1] MICP

C802
22pF

B600
SZ1005G750T

MIC601

C609
100pF

[2,1] MICN

C803
22pF

C817
NF

R605
510

R603
1.5K

R603
1.5K

VB801

VB802

MIC-FPC-7110

pF12
R602

CLASS-AB Amplifier

VDDIO

R601
1K

R615
1K

10uF
C624

C600
100nf

C814
NF

[2] AUXMICP

C804
22pF

C605
100pF

R614
2.2K

[2] AUXMICN

C606
22pF

C615
NF

HEAD_MIC [2,9]

C618
NF

VB800

[2] RBCP

B602
SZ1005G750T

RCV601

C801
100pF

B603
SZ1005G750T

RCV602

C808
NF

VB803

If use combined speaker and receiver ,then Analog switch are needed
to prevent reverse current.Pls refer to <<SC6600H Speaker&Receiver Application Notes>>

[2] HEADPHR

C825
10nF

R604
100

C822
47pF

HEAD_R [9]

[2] HEADPHL

C823
10nF

R600
100

C819
47pF

HEAD_L [9]

Class AB

TP600

FB600.601,default to smt 0R

SPEAKER_EN Must select GPIO default 0

PA_AUDIO_EN

VBAT

SPK- [9]

SPK+ [9]

U601

SPM601

[1] C662
NF

R621
10

C1 SHDN VO1 A2
C3 BYPS VDD B1
A3 IN+ GND B3
A1 IN- VO2 C2

SPM-

SPM602

[2] AUXSPP

R622
30K

IC-AUDIO_PA-LM4990BGA9

SPK+

C604
22uF

[9]

[2] AUXSPN

C661
22uF

10nF
C663

R624
150K

C607
100pF

NF
C610

NF
C611

VB804

VB805

TP

LCM_2.2-QVGA"//QCIF"

[1] TP_XL' R702 TP_XL [7]
[1] TP_YU' R703 TP_YU [7]
[1] TP_XR' R704 TP_XR [7]
[1] TP_YD' R705 TP_YD [7]

C721 NF C702 NF C723 NF C724 NF

VR701
NZQA6V8XV571G

LCM_CSN LCM_CSN [7]
EMA16 EMA16' [7]
EMAWEN EMAWEN' [7]
EMAOEN EMAOEN' [7]
RCN705

EMA0 EMA0' [7]
EMA1 EMA1' [7]
EMA2 EMA2' [7]
EMA3 EMA3' [7]
RCN704

EMA4 EMA4' [7]
EMA5 EMA5' [7]
EMA6 EMA6' [7]
EMA7 EMA7' [7]
RCN703

EMA8 EMA8' [7]
EMA9 EMA9' [7]
EMA10 EMA10' [7]
EMA11 EMA11' [7]
RCN701

EMA12 EMA12' [7]
EMA13 EMA13' [7]
EMA14 EMA14' [7]
EMA15 EMA15' [7]
RCN702

VMEM [3.5]
LCM_RSTN [1]
C700 100nf

VBAT
LED1-
LED2-
LED3-
LED4-

ACC-TFT-TM-35PIN-711G-1

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CAMERA

CAMERA1

CAMERA2

camera

INTERFACE

MK1
MK2

Boot select:

U0RSTN = low --> boot from USB

U0RSTN = high --> boot from NOR or UART
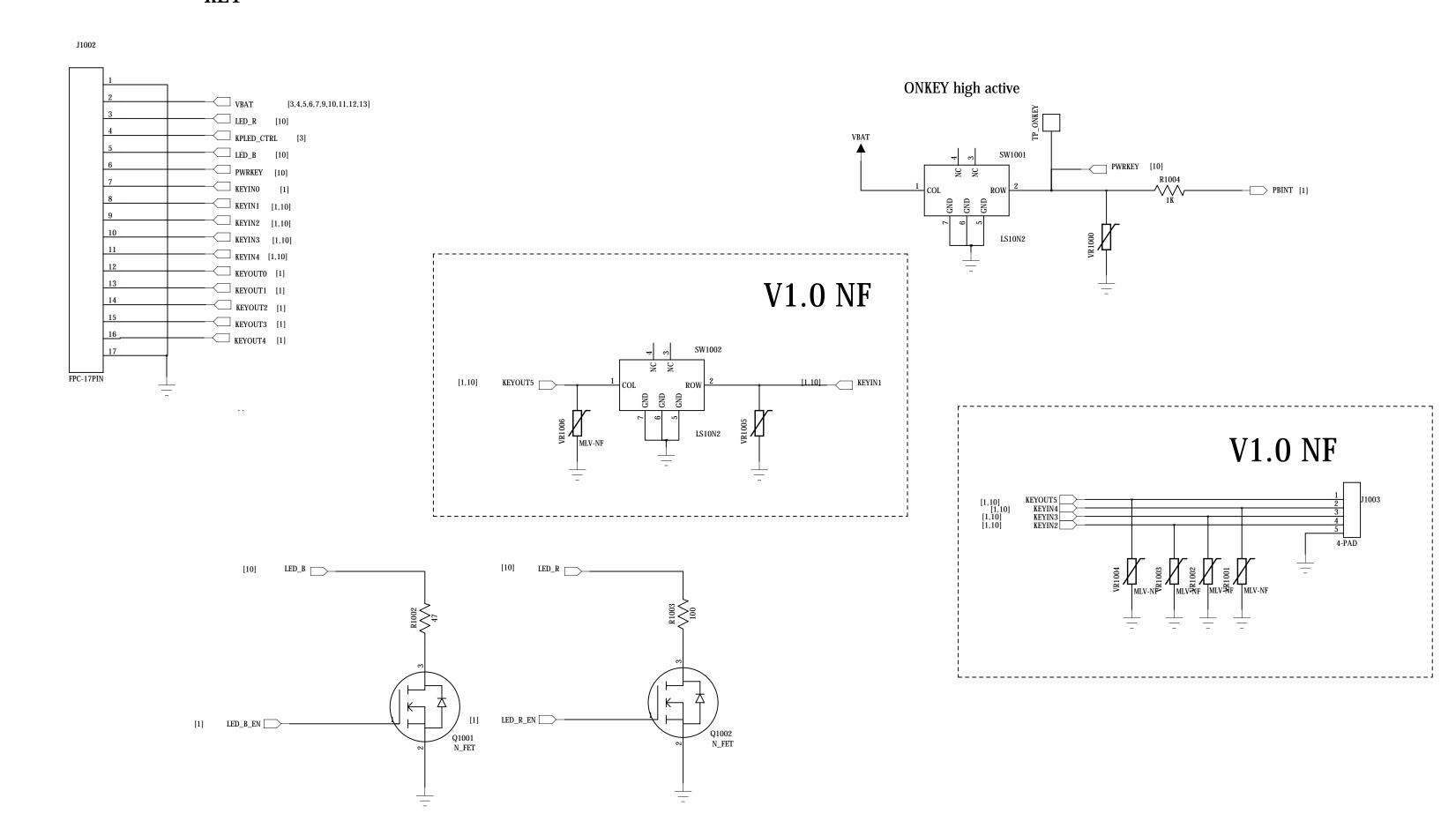
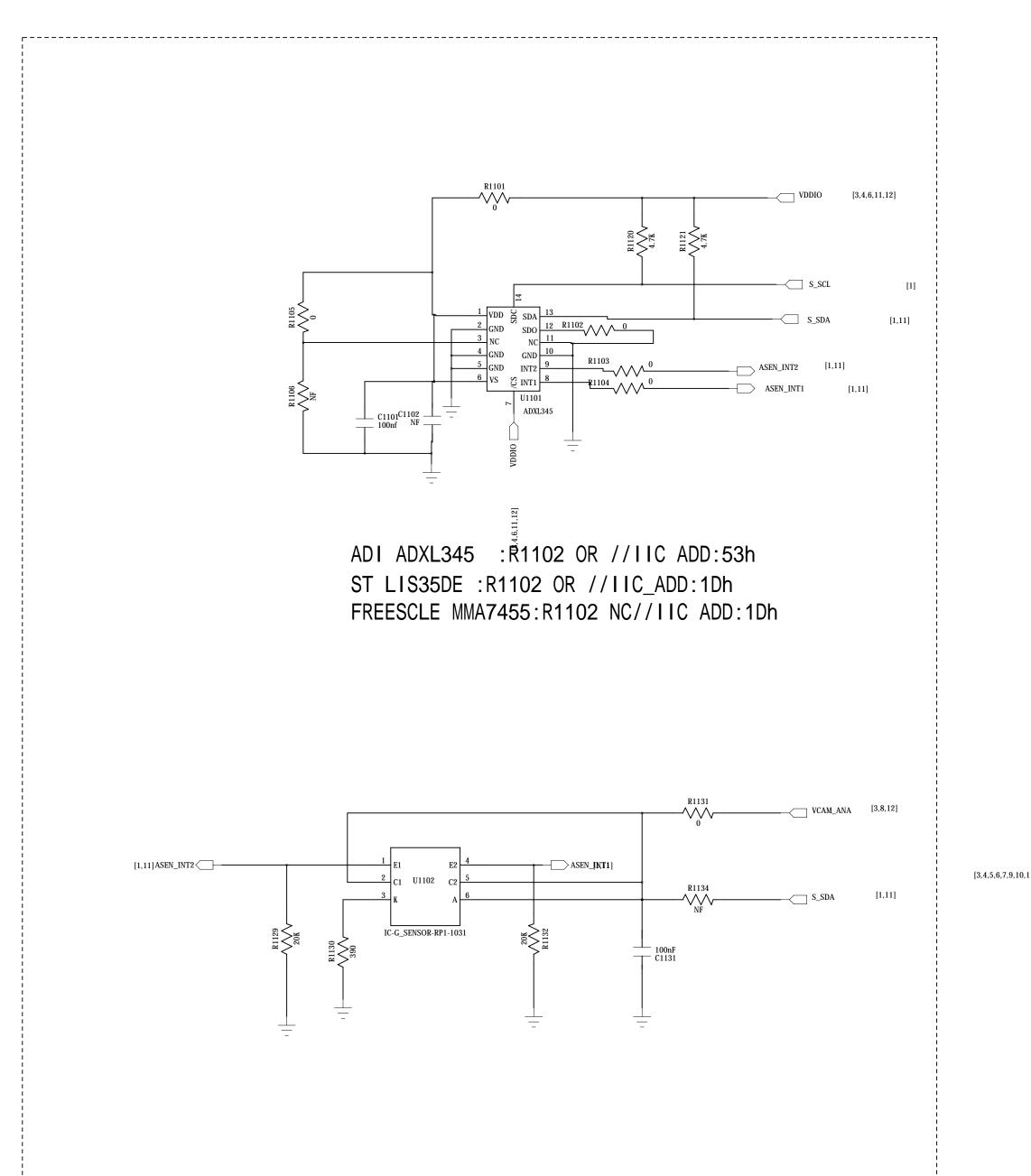NOTE: keyin0 & keyout0 pressed when power up will also lead to USB boot

H_MIC [9]
MPSL [9]
MP3R [9]

R901 1000
R902 1000
R903 1000

HEAD_MIC [2,6]
HEAD_L [6]
HEAD_R [6]
FM_ANT [9,12,13]

SPKE- [9]
SPKE+ [9]

TX [9]
RX [9]

VCHG [3,9]

H_MIC [9]

R943
R944

R941 1K
R942 1K

U1RXD [1]
U1TXD [1]
USB_DM [2]

USB_DP [2]
MP3R [9]
MPSL [9]

NF
R945
EXSP_DET [1]

NF
R946

R947 NF
FM_ANT
FM_ANT [9,12,13]

BEAD-BLM18HE102SN1-GHZ-600MA-0603

J902
CON-USB-USBIN-01015-TP01

TP_TXD
TP_RXD
TP_GND
TP_VBAT

TX [9]
RX [9]

VBAT

NF

VCHG

POWER
GND
ACC_POWER_JACK

100nf
C905

[3,4,5,6,7,9,10,11,12,13] VBAT
C915 NF
TP9
TP9

TORCH_EN1 [1]
Q903
DTC114TKA

[3,4,5,6,7,9,10,11,12,13] VBAT
TP9
TP9

TORCH_EN2 [1]
Q901
DTC114TKA

SPKE+ [9]
SPK0 [6]

U901
L906

EXSP_SEL [1,9]
VBAT
SPK+ [6] [3,4,5,6,7,9,10,11,12,13]

NF
R948

R951 NF
R953 NF

SPKE- [9]
SPK1 [6]

U902
L907

EXSP_SEL [1,9]
VBAT
SPK- [6] [3,4,5,6,7,9,10,11,12,13]

NF
C901
NF
R954

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KEYPAD

KEY

J1002

2	VBAT	[3,4,5,6,7,9,10,11,12,13]
3	LED_R	[10]
4	KPLED_CTRL	[3]
5	LED_B	[10]
6	PWRKEY	[10]
7	KEYIN0	[1]
8	KEYIN1	[1,10]
9	KEYIN2	[1,10]
10	KEYIN3	[1,10]
11	KEYIN4	[1,10]
12	KEYOUT0	[1]
13	KEYOUT1	[1]
14	KEYOUT2	[1]
15	KEYOUT3	[1]
16	KEYOUT4	[1]
17		

FPC-17PIN

ONKEY high active

VBAT

SW1001

COL   ROW

GND GND GND GND

LS10N2

TP_ONKEY

PWRKEY   [10]

R1004
1K

VR1000

PRINT   [1]

V1.0 NF

[1,10]   KEYOUT5

SW1002
COL   ROW
NC   NC
GND GND

LS10N2

[1,10]   KEYIN1

VR1006
MLV-NF

VR1005
MLV-NF

V1.0 NF

[1,10]   KEYOUT5
[1,10]   KEYIN4
[1,10]   KEYIN3
[1,10]   KEYIN2

J1003

4-PAD

VR1004   VR1003   VR1002   VR1001
MLV-NF   MLV-NF   MLV-NF   MLV-NF

[10]   LED_B

R1002
47

[1]   LED_B_EN

Q1001
N_FET

[10]   LED_R

R1003
100

[1]   LED_R_EN

Q1002
N_FET

should afford 100mA current

ADI ADXL345 : R1102 OR //IIC ADD:53h
ST LIS35DE : R1102 OR //IIC_ADD: 1Dh
FREESCLE MMA7455: R1102 NC//IIC ADD: 1Dh

VBAT

VDDIO [3,4,6,11,12]
S_SCL [1]
S_SDA [1,11]
ASEN_INT2 [1,11]
ASEN_INT1 [1,11]

R1101 0
R1105 0
R1106 NF
C1101 100nf
C1102 NF
U1101 ADXL345

VDDTF
Q1101
FET-P-SI1305DL-T1-E3
VDDIO
C1134 1uF
T_CARD_EN [1]

[0] V18_CTRL

FLASH_LED- [11]
R1114
CAM_FLASH [1]
Q1102 N_FET
R1116

VCAM_ANA [3,8,12]
R1131 0
ASEN_INT1
R1134 NF
S_SDA [1,11]
[1,11]ASEN_INT2
U1102
IC-G_SENSOR-RP1-1031
R1129 20K
R1130 39K
R1132
C1131 100pF

VDDTF [11]
SP10_DI [1]
SP10_CSN [1]
SP10_DIO [1]
SP10_CLK [1]
FLASH_LED- [11]
VBAT

CON1101
R1113
R1111 47k
R1112 47k

VSIM [2]
VSIM2
SIMRST [2]
SIMRST [2]
SIMDA [2]
SIMDA [2]
SIMCLK [2]
SIMCLK [2]

[3,4,5,6,7,9,10,11,12,13]
24 5602 0240506a59
C1121 27pF
C1122 27pF
C1140 NF
C1112 NF

BT_32K line to clk_aux.
BT_CLK_REQUEST must line to bb gpio input pin.
BT_LDO_EN must hold low level when bb rst and power on
Confirm rda5868+ iic address.default is 2AH/2CH

[1] U3RXD
[1] U3TXD

UART_RTS
UART_CTS

[1] BT_PCM_DO   PCM_DOUT
[1] BT_PCM_DI   PCM_DIN
[1] BT_PCM_CLK   PCM_CLK
[1] BT_PCM_SYNC PCM_SYNC
              DVDDO1

[1] BT_CLK_REQUEST   USB_DP
              USB_DN

VBAT

[1,12] FM_32K

SDA [1,8,12,13]
SCL [1,8,12,13]

U1204
IC-BT-SAW-BP1608

ANT1

BT_RST[1]
BT_LDO_EN[1]

VBAT
CLK_26M   [2,4]

U1203
IC-RDA5802E

[9,13] FM_ANT

AIL1 [2,13]
AIR1 [2,13]

[1,12] FM_32K

SCL [1,8,12,13]
SDA [1,8,12,13]

FM

NOTES: If the camera and FM functions both adopted in the system,
they should use the same IO power supply and I2C pullup resistances.

Bluetooth ANT: 2400-2483.5MHz

U1301
IC-ATV-TUNER-TLG1120

TV_2.8V

AMCK [13]
ASCK [13]
TV_RST [1]

CAM_D[0:7]

CAM_PCLK [1,8]
CAM_VSYNC [1,8]
CAM_HS [1,8]

TV_1.2V [13]
TV_2.8V [13]

SDA [1,8,12]
SCL [1,8,12]

ANT_TV2
NC ANTENNA
ANT_TV1
NC ANTENNA

FM_ANT
[9,12]

TV PWR

VBAT
[3,4,5,6,7,9,10,11,12,13]
U1302
TV_2.8V [13]

VBAT
U1303
TV_1.2V [13]
SZ1005G750T

TVPWR_EN [1]

AUDIO ANALOG OUT

ASCK
TV_AUDIO_L [13]
AIL1 [2,12]

AMCK
AIR1 [2,12]

RECORD

TV_AUDIO_L [13]
MICP [2,6]
MICN [2,6]

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Title£º	<title>	Check by£º <check>
REV£º	<REV>	Approve£º <appr>
File NO.£º	<file no>	sheet: 13/ 13