Routing table

Time slot	T1
Role	(H/M)
Next hop	Next hop Ip

Sample frequency control:

S1

Sample frequency

Sensor enable map

Time slot

Sensor

On/Off

Downstream data structure

T1

S2

T2

T1

S3

(H/M)

Next hop Ip

T3

S4

T4

S5

T5

S6

Tn

(H/M)

Next hop Ip

T6

S7

T7

S8