outing table								
Time slot	T1		T2				Tn	
Role	(H/M)		(H/M)				(H/M)	
Next hop	Next hop Ip		Next hop Ip		•••		Next hop Ip	
Sample frequency control:								
Time slot		T1	T1	Т3	T4	T5	T6	T7
Sample frequency								
Sensor enable map								
Sensor	S1	S2	S3	S4	S5	S6	S7	S8
On/Off								
	Time slot Role Next hop ample frequ Time slot Sample freq ensor enable	Role (H/M) Next hop Next h  ample frequency co Time slot Sample frequency ensor enable map Sensor S1	Time slot T1  Role (H/M)  Next hop Ip  imple frequency control:  Time slot T1  Sample frequency  ensor enable map  Sensor S1 S2	Time slot T1 T2  Role (H/M) (H/M)  Next hop Next hop Ip Next ho  ample frequency control:  Time slot T1 T1  Sample frequency  ensor enable map  Sensor S1 S2 S3	Time slot T1 T2  Role (H/M) (H/M)  Next hop Ip Next hop Ip  imple frequency control:  Time slot T1 T1 T3  Sample frequency  ensor enable map  Sensor S1 S2 S3 S4	Time slot T1 T2  Role (H/M) (H/M)  Next hop Next hop Ip Next hop Ip  imple frequency control:  Time slot T1 T1 T3 T4  Sample frequency  ensor enable map  Sensor S1 S2 S3 S4 S5	Time slot	Time slot         T1         T2          Tn           Role         (H/M)         (H/M)          (H/M)           Next hop         Next hop Ip          Next ho           sample frequency control:         T1         T1         T3         T4         T5         T6           Sample frequency         Sample frequency         S1         S2         S3         S4         S5         S6         S7