

Monte Carlo Integration: The VEGAS Algorithm for Graphics Processing Units

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Abstract: This work presents a study and modification of the VEGAS integrating algorithm in graphics processing units. The changes made to a reviewed program allow for better performance and use of memory to incorporate more evaluation points, which was an important restriction for the original code on high dimension spaces.

Keywords: GPU, Monte Carlo methods, VEGAS

2000 AMS Subject Classification: 65C05 - 68M20

1 INTRODUCTION

Monte Carlo methods are a wide and well known class of computational algorithms that rely on repeated random samplings to get numerical results. One of their main applications is the numerical integration of functions in problems ranging from particle physics[1] to cosmology[2]. The usual working pattern for these algorithms is:

- Definition of a possible input domain.
- Generation of random inputs from a *probability distribution* over the domain.
- *Deterministic* computations over the inputs.
- Results gathering.

Since its creation, lots of efforts have been made in order to boost Monte Carlo methods performance, especially concerning the minimization of numerical errors and the algorithm's speed, trying to adapt them to every new coming computing architecture, including graphics processing units (GPUs), by creating different alternatives for them[3].

One of those alternatives is the VEGAS algorithm, created by G.P. Lepage[4]. It is based in the concept of *importance sampling*: It samples points from the probability distribution described by the function $\|f\|$ to be integrated, so that the sampling points are concentrated in the regions that make the largest contribution to the integral. VEGAS is based on an iterative and adaptive Monte Carlo scheme: each axis of variable is divided into grids, dividing the integration space into hypercubes. Monte Carlo integrations are performed on each hypercube and the variances from hypercubes are used to adapt the shape of the grids, which will be used in the next iteration, reducing the variance of the total integral at each step.

This work concentrates on the study and improvement of a multi-dimensional VEGAS algorithm, which uses GPUs, created by J. Kanzaki in 2011[5].

2 THE INITIAL PROGRAM

Kanzaki's code can be found at <http://madgraph.kek.jp/KEK/GPU/gVEGAS/example/>. It is written in CUDA language, adapted from FORTRAN code written by Lepage[6]. There is a single and double precision version available in the webpage. The steps followed by the program in each iteration are:

1. Generation of the integration space, separating it in hypercubes.
2. That data is sent to the GPU, where the function evaluations are performed and the information per hypercube is gathered.

3. These results are sent back to the CPU, where weighted averages, approximation errors and the reallocation of the grid are performed.
4. If the amount of iterations is reached or a minimum value of error is obtained, the program stops.

Although this code has a great performance relative to the first stages of VEGAS (evaluating a function in lots of different points in space on GPUs is an embarrassingly parallel task), there are still some issues that are addressed in this work.

There is a considerable amount of data moved between GPU and CPU. The code generates an array of evaluations (one for each point) and also needs to store the hypercube the point is occupying for every dimension in the integration space. So, for every evaluation point, the program needs $(dim+1) * 4$ bytes (in single precision) of RAM, where dim is the dimension of the integration space. When working on GPUs, memory management is an important issue and it is not properly addressed in this code.

The complete weighted averages are computed in the CPU, but it can be also done in GPU, because the *reduction* operation is easy to parallelize. The grid evaluation can also work in parallel.

3 MODIFICATIONS

There is a kernel in the original CUDA implementation that is in charge of generating random points, evaluating the function on them and creating the location arrays for each dimension. The modified kernel now performs the values (and their squares) reduction, aided by the *atomicAdd* instruction[7] and also takes the data to a histogram array, which is passed to the CPU to obtain the grid rearrangement for the next iteration. This array has a fixed size, so there aren't any memory problems.

An important note is that the original code passes single precision numbers to double when it computes the weighted averages for the integrals. The new code uses only single precision since it works with the *atomicAdd* instruction and it is only available for single precision in the Maxwell architecture, which was the one available for this work.

The entire code is hosted at <https://github.com/lbiedma/gVegascp>.

4 NUMERICAL RESULTS

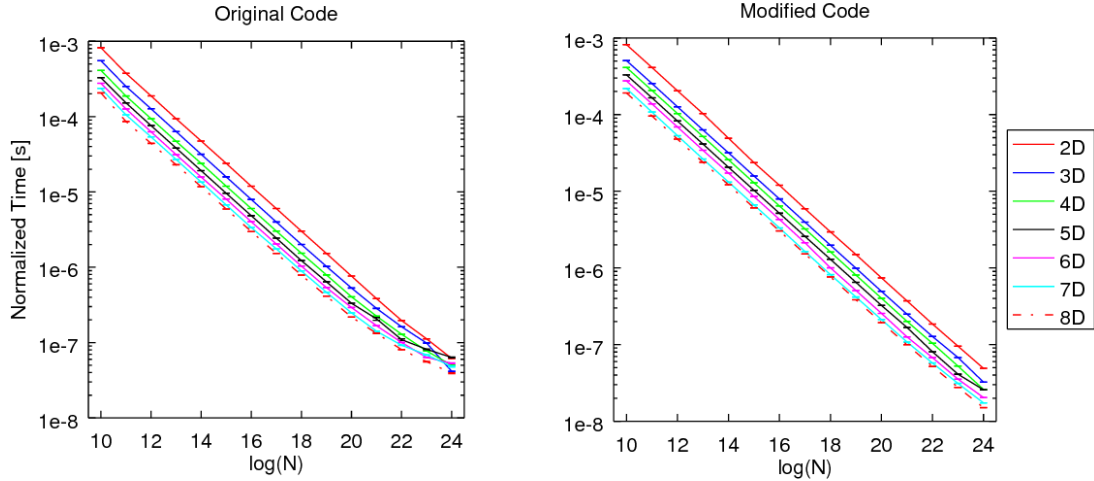
SPEED COMPARISON

The hardware specifications for the experiments were:

CPU	2x Intel(R) Xeon(R) CPU E5-2620 (12 cores at 2.40 GHz)
Memory	126 GB
GPU	NVIDIA GeForce GTX Titan X (12 GB RAM)

The termination conditions were set at 1×10^{-6} for the relative error between estimated integrals per step and 10 as the max amount of iterations. The space dimension d was limited to a maximum of 8 and the amount of points used was increased on each execution from 2^{10} to 2^{24} (the memory limitations on the original code made it impossible to reach higher dimension numbers). Only for speed comparison purposes, the multi-dimensional paraboloid ($\|x\|^2$) in the $[-1, 1]^d$ was chosen.

The following graph shows the performance of the original and the modified programs, with time normalized by the number of points used and the amount of iterations.



It can be observed that both programs perform at almost the same speed at first, but the new program continues to maintain a good performance as the amount of function evaluations increases. More points couldn't be evaluated because of memory restrictions for the original code.

BENCHMARKING

5 SUMMARY AND OUTLOOKS

The modifications applied to the original program allowed it to perform at a higher speed for big problems and manage memory in a better way, which is an important issue when working with GPUs, since RAM is highly limited.

NVIDIA's new GPU architecture (Pascal) incorporates the *atomicAdd* instruction for double precision numbers[8], this will be really useful to achieve high performance in double real and complex precision computations, and may be the subject of future work.

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