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TC118 (Document No.: S&CIC1060)

Single channel DC motor driver

1. Features

- ➤ Single channel built-in power MOS full bridge driver
- ➤ Drive forward, backward, stop and brake functions
- > Built-in hysteresis thermal effect overcurrent protection function
- ightharpoonup Low on-resistance (1.6 Ω)

- ➤ The maximum continuous output current can reach 1.8A, the peak value is 2.5A
- > No need for external filter capacitor
- ➤ Adopt DIP-8, SOP-8 package form

2. Product application

> Toy motor drive

Three, pin diagram and pin description

			Pin	diagran	n				Serial numberymbol		I/O	Function Description	
									1	NC	_	Hang in the air	
									2	INA	I	Join INB to determine state	
NC	1	8	OUTA	NC	1	8	ΟU	JTA	3	INB	I	Join INA to determine state	
INA	2	7	PGND	INA	2	7	PG	ND					
INB	3	6	AGND	INB	3	6	AG	GND	4	VDD	P	Power positive	
VDD	4	5	OUTB	VDD	4	5	ου	ТВ	5	OUTB	O Fu	ll-bridge output terminal B	
									6	AGND	G gr	ound	
	DIP-8					SOP-8			7	DCMD	0	1	
	TC118D					TC118S			7	PGND	G gr	ound	
									8	OUTA	O Fu	ll bridge output terminal A	

4. Absolute maximum ratings

parameter		symbol	Rated value	unit
voltage		VCC	7.2	V
n d	Pd	DIP-8	1	W
Power consumption	Pa	SOP-8	0.96	W
TI 1 ' 4	θЈА	DIP-8	125	°C/W
Thermal resistance	θJA	SOP-8	130	°C/W
Operating temperature		Topr	-20~85	°C
Junction temperature		Tj	150	$^{\circ}\mathrm{C}$
storage temperature		Tstg	-55~150	°C
Manual welding temperature			350~370	°C
Peak output current		Iop	2.5	A
Maximum continuous output current		Ioc	1.8	A

Note: The maximum continuous output current depends on the heat dissipation conditions.

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5. Recommended working conditions (Ta=25°C)

parameter	symbol	Parameter value	unit
voltage	VCC	2.4~7.2	V
Control input voltage	VIN	0~VCC	V
Positive and reverse output current	Iout	-1500~1500	mA

6. Electrical characteristics (Ta=25°C, VCC=3V, RL=15Ω, except for special instructions.)

	parameter	symbol	Test condition	Minimum	typical max	imum	unit
Overall route							
Circuit shutdow	n current	ICCST	INA=INB=1	_	0	10	uA
			INA=H, INB=L or				
Working curren	t	ICC	INA=L, INB=H or	_	0.3	1	mA
			INA=H, INB=H				
Control input							
High level input	t voltage	VINH		2.0	_	_	V
Low-level input	voltage	VINL		_	_	0.8	V
High level input	current	IINH	VIN=3V	_	5	20	uA
Low-level input	current	IINL	VIN=0V	-1	0	_	uA
Pull-down resist	tor	RIN		_	1.5	_	$M\Omega$
drive							
Output on-resist	ance	RON	Io=±200mA	_	1	1.6	Ω

Seven, input / output logic table

Way	Output	Οι	nter	er
, ray	OUTB	OUTA	INB	INA
Standby state	Hi-Z	Hi-Z	L	L
go ahead	L	Н	L	Н
Back	Н	L	Н	L

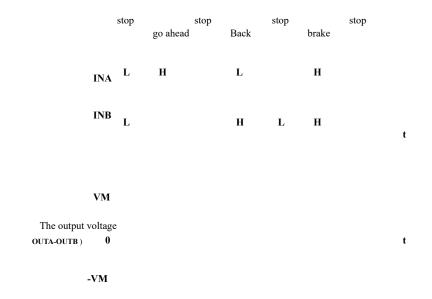
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8. Input / output waveform



Nine, application reference circuit diagram

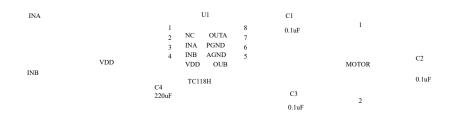


Figure 1: Typical application scheme application circuit diagram

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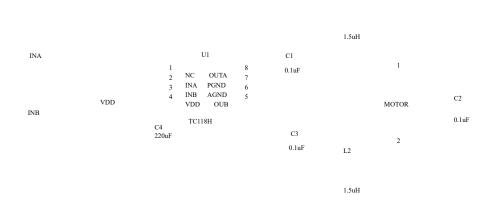
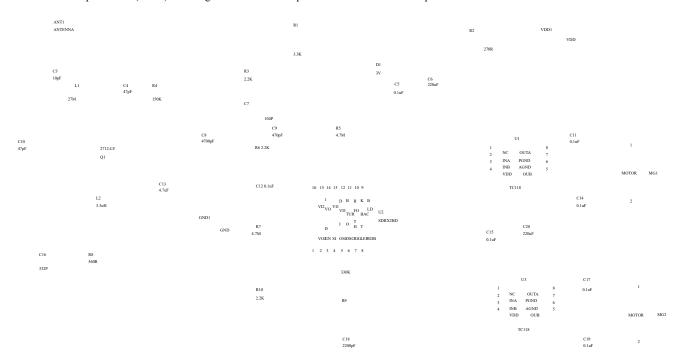


Figure 3: Application circuit diagram of strong interference scheme

Note: 1. The capacitor C1 (0.1uF) in the figure is connected in parallel to the motor and not placed on the PCB.



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11. Package size drawing

➤ DIP-8