# SYSC 5001W: Project deliverable 2

## Qiguang Chu 300042722

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# 1 Model Verification and Validation

The goal of the validation is to produce a model that represents true system behavior closely enough for the model to be used as a substitute for the actual system.

### 1.1 Verification of Simulation Models

Verification is concerned with building the model correctly. It proceeds by the comparison of the conceptual model to the computer representation what implements that conception.

#### 1.1.1 Flow Diagram

I made a flow diagram that includes each logically possible action a system can take when an event occurs.