***Ejercicio Nº 4:***

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| (1) mov R6, #xxxx  (2) mov R6, R  (3) mov R6, (R)  (4) mov R6, xxxx  (5) mov R6, (xxxx)  (6) mov R6, (R2)xxxx  (7) mov R6, @300(R) | #xxxx Inmediato  R Registro  (R) Registro indirecto  xxxx Absoluto  (xxxx) Memoria indirecto  (R)xxxx Base  @xxxx(R) Pre-indexado indirecto | ***Paso*** | ***Valor*** | ***Accesos*** |
| ***1*** | ***xxxx = 100*** | ***0*** |
| ***2*** | ***R = R1*** | ***0*** |
| ***3*** | ***R = R4*** | ***1*** |
| ***4*** | ***xxxx = 400*** | ***1*** |
| ***5*** | ***xxxx = 200*** | ***2*** |
| ***6*** | ***xxxx = 200*** | ***1*** |
| ***7*** | ***R = R3*** | ***2*** |

***Ejercicio Nº 5:***

|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| ***Programa*** | ***Inciso A: Ensamblado*** | |  | | | | | | |
| Programa A:  LDA R0, FFh  LOAD R1, 0(R0)  ADD R2, RF, RF  JZ R1, lbl2  lbl1: ADD R2, R2, R1  DEC R1  JG R1, lbl1  lbl2: STORE R2, 0(R0)  HLT  Pseudocódigo 1  if (R1 <= 4) R2++;  else R2--;  Pseudocódigo 2  R3 = 0;  for (R4 = 0; R4 < 10; R4++)  R3 += R4; | 00:  02:  04:  06:  08:  0A:  0C: 0E:  10:  12:  14:  16:  18:  1A:  1C:  1E:  20:  22: |  |  | | | | | | |
| ***Inciso B:*** | | | | | | |
| ***R0*** | ***R1*** | ***R2*** | ***PC*** |  |  |  |
| -- | -- | -- | **00** |  |  |  |
| **FF** | -- | -- | **02** |  |  |  |
| FF | **04** | -- | **04** |  |  |  |
| FF | 04 | **00** | **06** |  |  |  |
| FF | 04 | 00 | **08** |  |  |  |
| FF | 04 | **04** | **0A** |  |  |  |
| FF | 03 | 04 | **0C** |  |  |  |
| FF | 03 | 04 | **0E-08** |  |  |  |
| FF | 03 | 07 | **10** |  |  |  |
| FF | 04 |  | **12-1A** |  |  |  |
| FF | 04 |  | **1C** |  |  |  |
| FF | 04 |  | **1E** |  |  |  |
| FF |  |  | **20-1A** |  |  |  |
| FF |  |  | **1C** |  |  |  |
| FF |  |  | **1E** |  |  |  |
| FF |  |  | **20** |  |  |  |
| FF |  |  | **22** |  |  |  |
| FF |  |  | **--** |  |  |  |
|  | | | | | | |
| ***Inciso C:*** | | | | | | |
| LDA R4, 04 ; R4 = 4  SUB R3, R1, R4 ; R3 = R3 – R4  JG R3, false ; si (R3-4)>0 🡪 R3>4  INC R2  JMP fin  false: DEC R2  fin: HLT | | | | | | |
| ***Inciso D:*** | | | | | | |
| XOR R3, R3, R3 ; R3=0  LDA R4, 09 ; R4 = 09  loop: ADD R3, R3, R4 ; R3+=R4  DEC R4 ; R4--;  JG R4, loop ; Loop mientras R4>0  HLT | | | | | | |