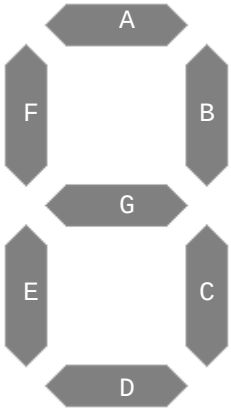
Embedded systems course
7-segments specification

The display is based on 2 hexadecimal digits using 7-segment display (http://en.wikipedia.org/wiki/Seven-segment_display)

The 7-segment Aux register is located at **0x104.** It is a write-only register.

Register specification:

Bit	Description
0-6	Each bit represents one of the LEDs in the 7-segment display. When a bit is set to 0 the LED is turned off, and when it is set to 1 the LED is turned on. Bit 0 – LED 'A' Bit 1 – LED 'B' Bit 2 – LED 'C' Bit 3 – LED 'D' Bit 4 – LED 'E' Bit 5 – LED 'F' Bit 6 – LED 'G' 
7	Chip select. 0 for Chip 0 (representing least significant byte), 1 for chip 1 (most significant byte).
8-31	Reserved to 0.

Whenever there is more than one way to draw a character, use the left most representation from http://en.wikipedia.org/wiki/Seven-segment_display_character_representations