

Embedded systems course

Input panel specification

The device has an input panel with 13 buttons.

The input panel interface registers base address is **0x1E0**.

The following registers are supported:

Address	Access Type	Symbol	Name
0x1E0	RO	PADSTAT	Input panel status register Reading "1" in a specific bit means that the corresponding button was pressed (see section 3.1.1) Reading the register sets it to all 0.
0x1E1	RW	PADIER	Input panel interrupt enable register Writing "1" to a specific bit will cause the device to assert IRQ9 whenever the corresponding bit in PADSTAT is asserted.



Buttons / bits mapping

Bit	13:31	12	11	10	9	8	7	6	5	4	3	2	1	0
Button	reserved	#	0	*	9	8	7	6	5	4	3	2	1	OK