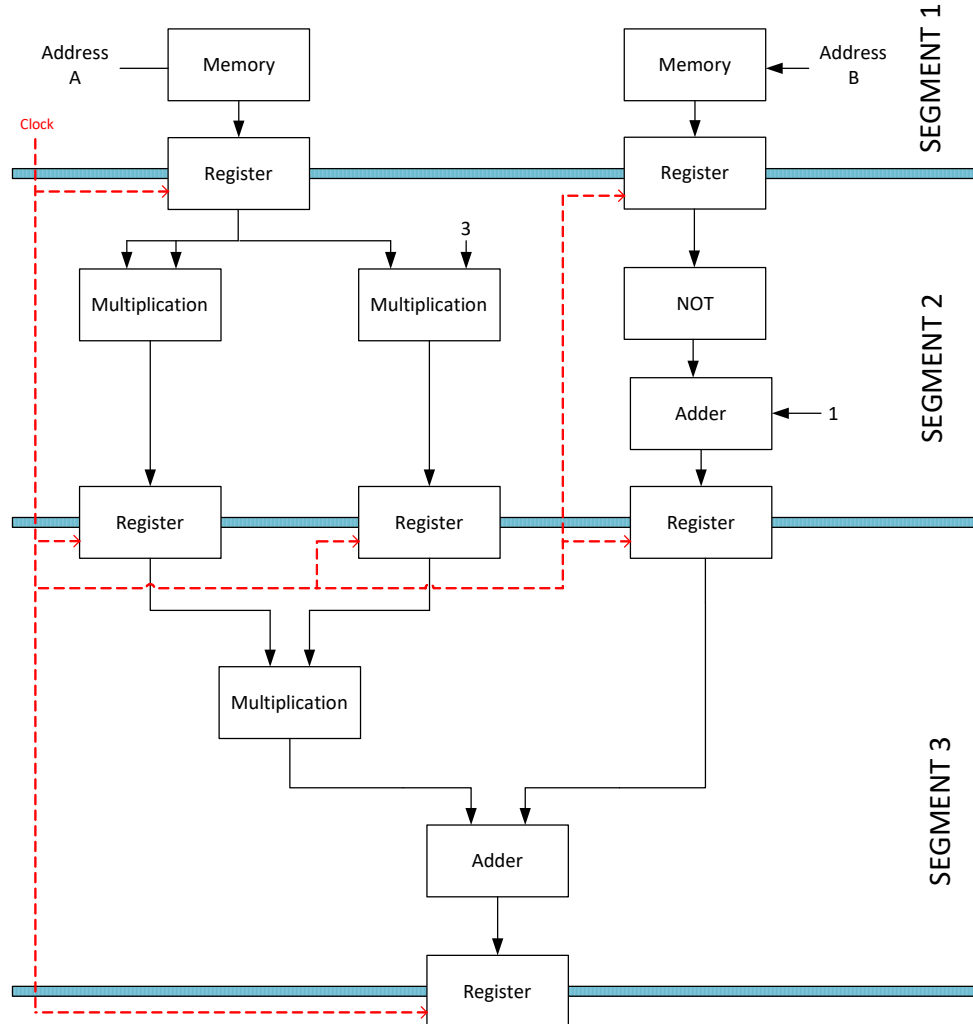


## BLG 322E – Computer Architecture Assignment 1 Solutions

1)



$$T_{\text{Segment1}} = 50 \text{ ns} + 5 \text{ ns} = 55 \text{ ns}$$

$$T_{\text{Segment2}} = 10 \text{ ns} + 20 \text{ ns} + 5 \text{ ns} = 35 \text{ ns} \text{ or } 30 \text{ ns} + 5 \text{ ns} = 35 \text{ ns}$$

$$T_{\text{Segment3}} = 30 \text{ ns} + 20 \text{ ns} + 5 \text{ ns} = 55 \text{ ns}$$

2) The maximum segment time is 55 ns. Therefore, the clock cycle must be equal to 55 ns.

$$T_n = 50 \text{ ns} + 30 \text{ ns} + 30 \text{ ns} + 20 \text{ ns} = 130 \text{ ns (Without pipeline)}$$

$$S = \frac{n * t_n}{(k + n - 1) * t_p} = \frac{5 * 130}{(3 + 5 - 1) * 55} = 1.68 \text{ (For 5 numbers)}$$

$$\lim_{n \rightarrow \infty} S = \frac{t_n}{t_p} = \frac{130}{55} = 2.36 \text{ (For infinite numbers)}$$

3) The theoretical speedup =  $k = 3$  ( $k \rightarrow$  number of segments)

