

D

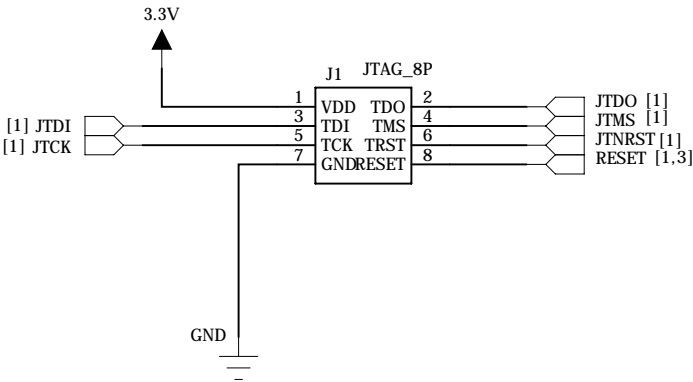
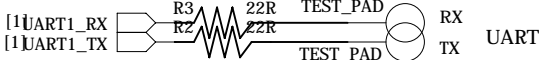
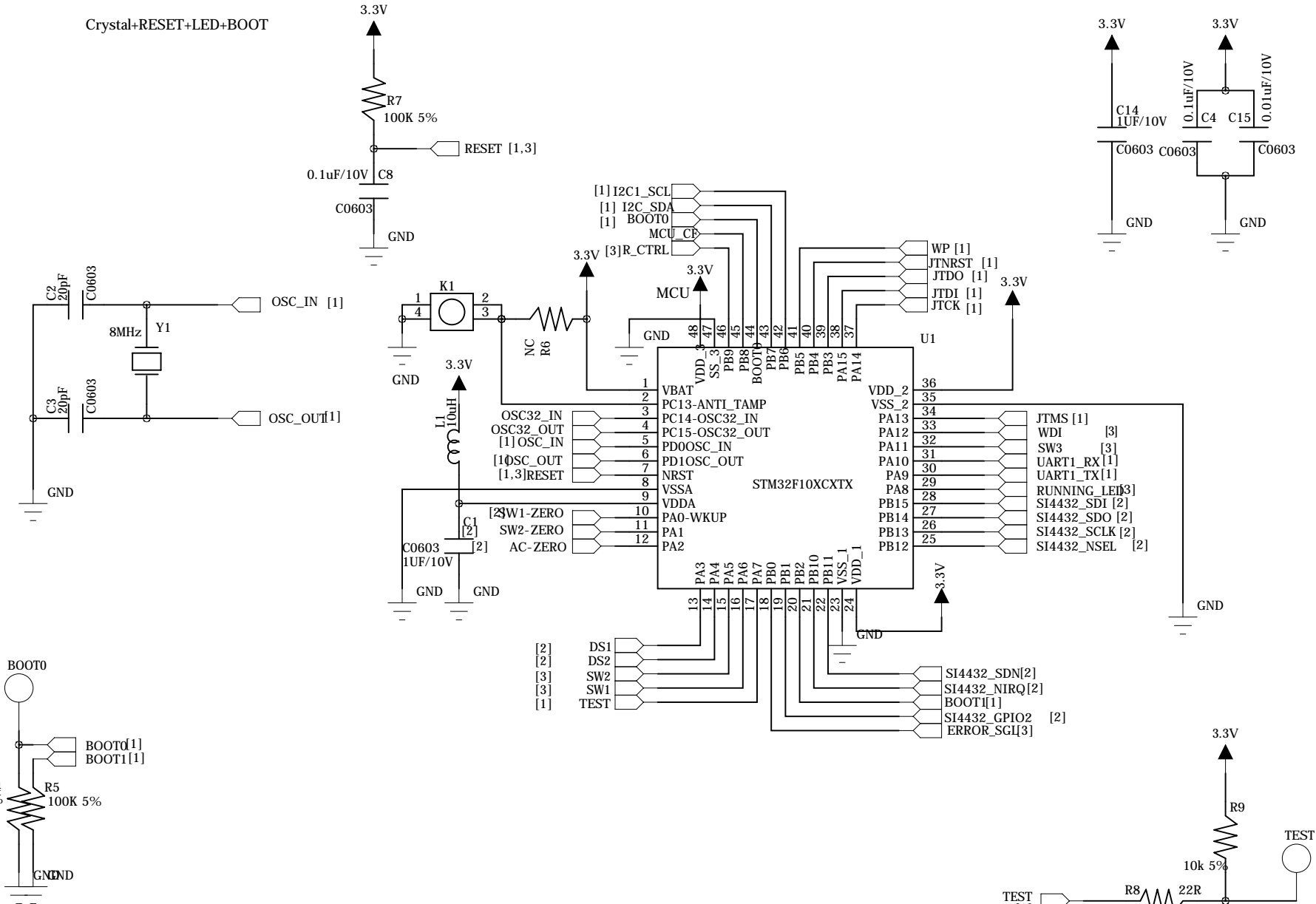
C

B

A

BOOT0	BOOT1	
0	X	Start From FLASH
1	0	Load From UART
1	1	Start From SRAM

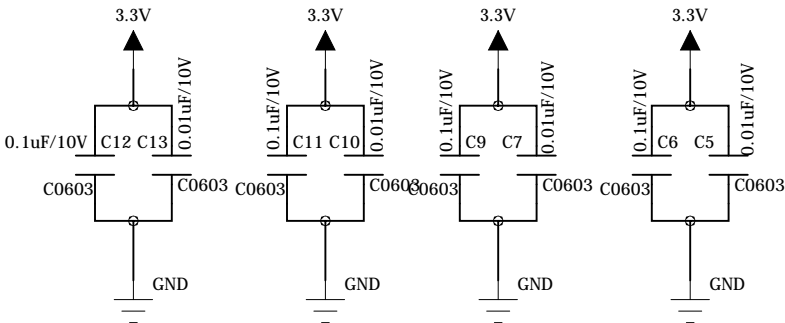
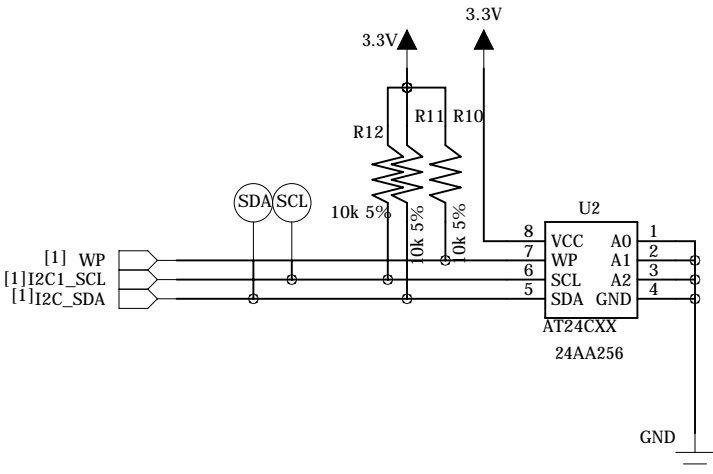
Crystal+RESET+LED+BOOT



REVISION RECORD

LTR	ECO NO:	APPROVED:	DATE:

EEPROM



COMPANY: BESTFU			
TITLE: ASP3			
CODE:	SIZE:	DRAWING NO:	REV:
<Code>	B	2	V1.0
SCALE: <Scale>			SHEET: 1 of 3

DRAWN: fuhuoyang	DATED: 2015-10-30
CHECKED:	DATED:
QUALITY CONTROL:	DATED:
RELEASED:	DATED:

D

C

B

A

6

5

4

3

2

1

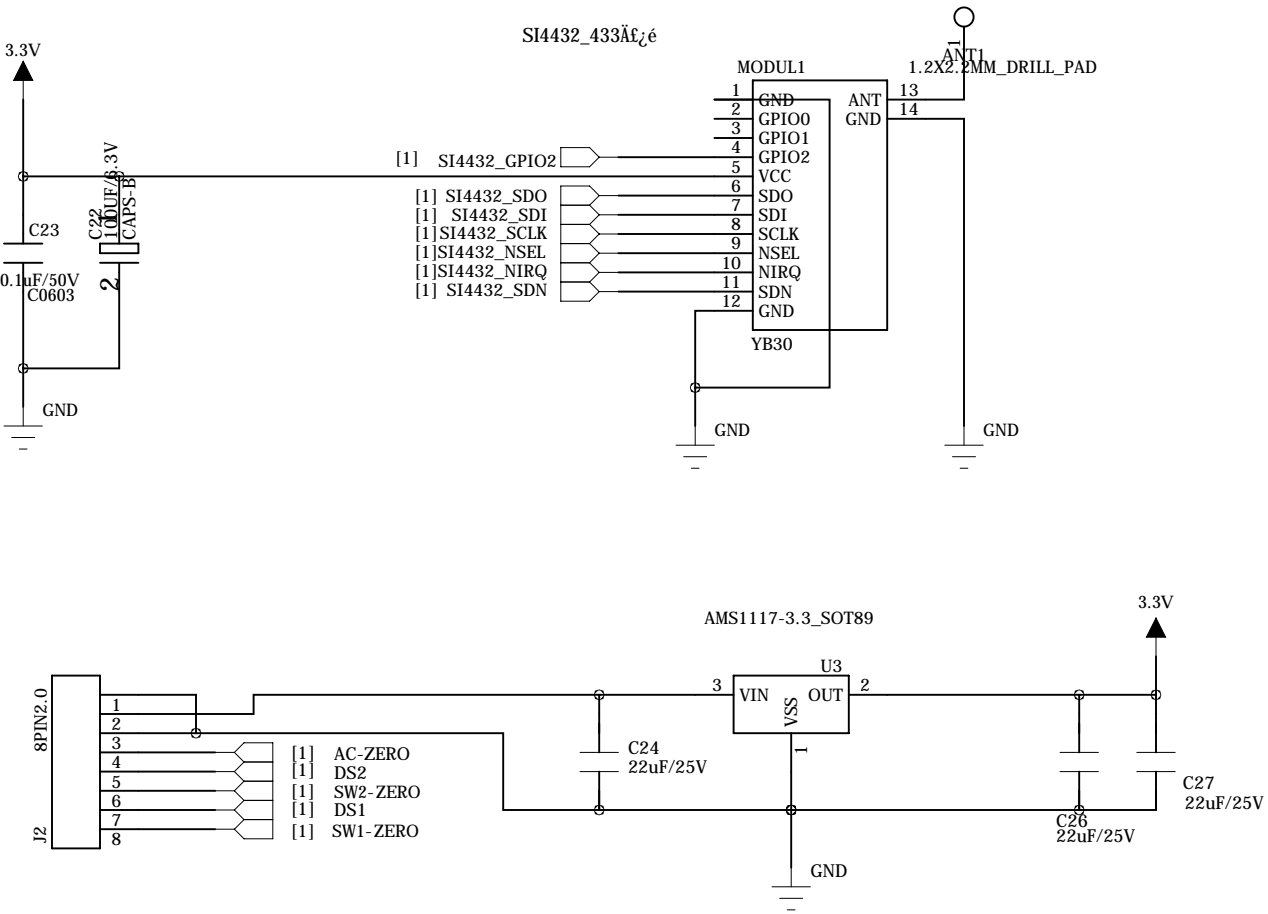
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

D

C

B

A



COMPANY:			
BESTFU			
TITLE:			
ASP3			
CODE:	SIZE:	DRAWING NO:	REV:
<Code>	B	2	V1.0
SCALE: <Scale>		SHEET: 2 OF 3	

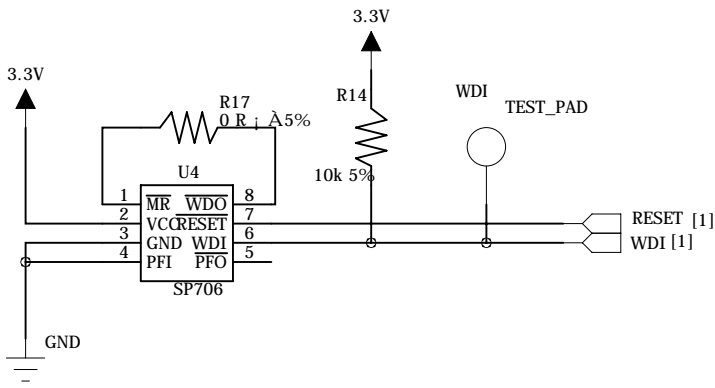
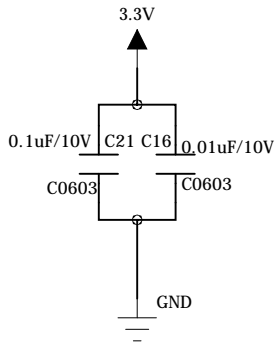
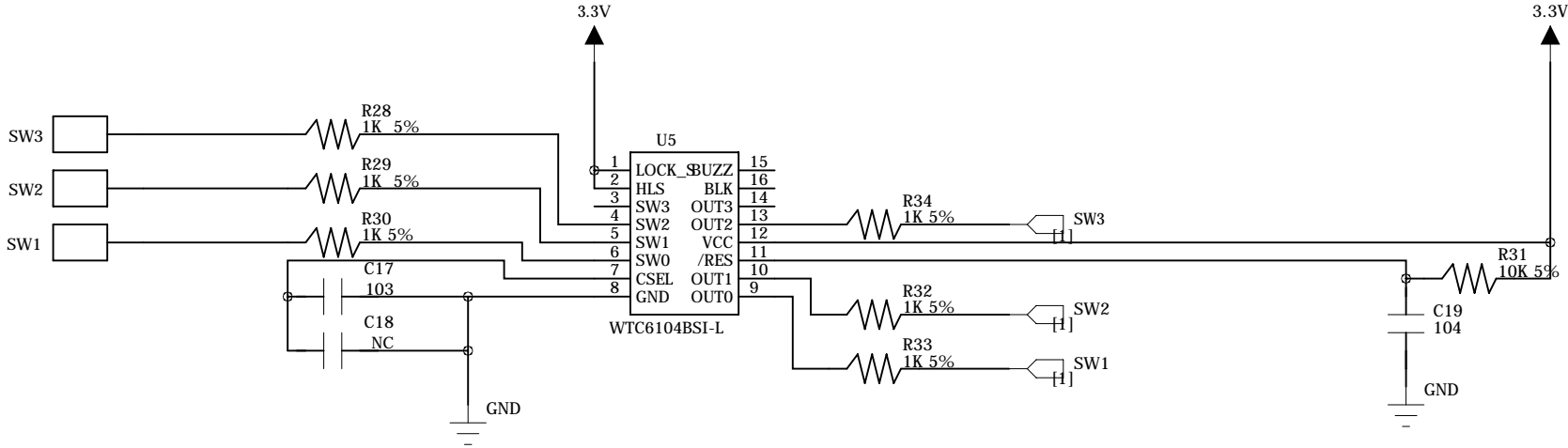
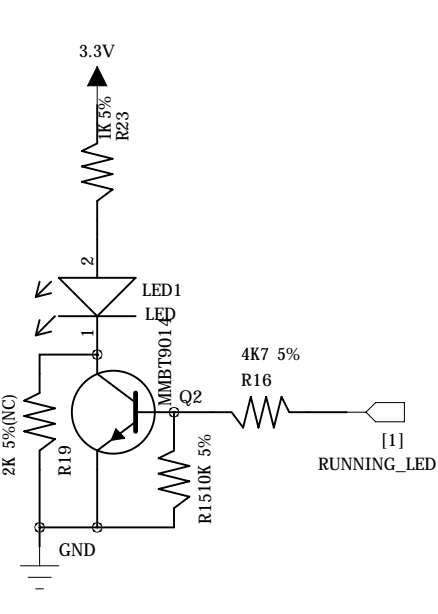
DRAWN:	DATED:
fuhuoyang	2015-10-30
CHECKED:	DATED:
QUALITY CONTROL:	DATED:
RELEASED:	DATED:

D

C

B

A



REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

D

C

B

A

DRAWN: fuhuoyang	DATED: 2015-10-30
CHECKED:	DATED:
QUALITY CONTROL:	DATED:
RELEASED:	DATED:

COMPANY: BESTFU			
TITLE: ASP3			
CODE: <Code>	SIZE: B	DRAWING NO: 2	REV: V1.0
SCALE: <Scale>			SHEET: 3 of 3