

VD 8..15 = BAT42

## Speicher + Eprom

TITLE: Z1013-128

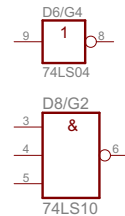
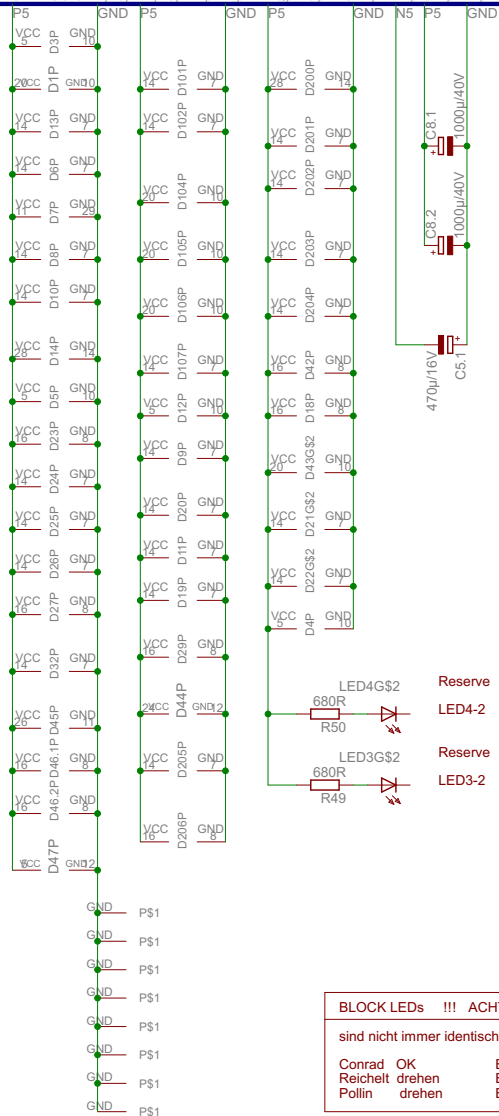
Document Number:

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Date: 21.12.2016 22:42:36

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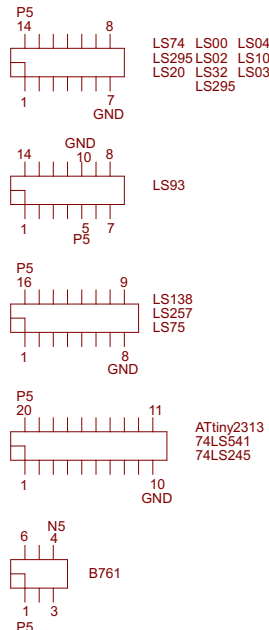
STEUERBUS:GND,P5,P12,N5,N12,/WR,/MREQ,/IEO,/RESET,TAKT,/IODI,/NMI,/WAIT,/RFSH,/PM1,/BUSAK,/RD,/MEMDI,/IORQ1,/BUSRQ,/INT,/IORQ,CASG,/HALT,PM1,/BS,/RESET-T,/DK10,/DK13,MREQ,TEST,/RESET-C,/IOSEL1,ROM-A,E,X1A3,X1C3,X1C25,X1C27,Z17,/MEMDI+



Grundposition der Jumper		
JP12	1-2	Geschlossen
JP14	1-2	Geschlossen
JP15	1-2	Geschlossen
JP1	1-2	offen
JP2	1-2	offen
JP3	1-2	offen
JP4	1-2	offen
JP5	1-2	offen
JP10	1-2	Geschlossen
JP13	1-2	Geschlossen
JP7	3-4	2MHz
JP17	2-3	2MHz
JP6	2-3	2MHz
JP8	1-2	offen
JP9	2-3	offen
JP11	1-2	offen
JP18	1-2	offen
JP19	1-2	offen
JP16	offen	offen
JP20	1-2	offen

Dieses ist eine Nachtägliche Änderung  
Nach dem 11/2016

D30 Bildspeicher	
SJ1	1-2
SJ2	1-2
SJ3	1-2



Achtung INFO	
JP7 - 1-2 = 1MHz	
JP7 - 3-4 = 2MHz	
JP7 - 5-6 = 4MHz	
JP15 -1 = 1-2	
JP15 -2 = 3-4	
JP14-1 = 1-2	
JP14-2 = 3-4	
JP14-3 = 5-6	

