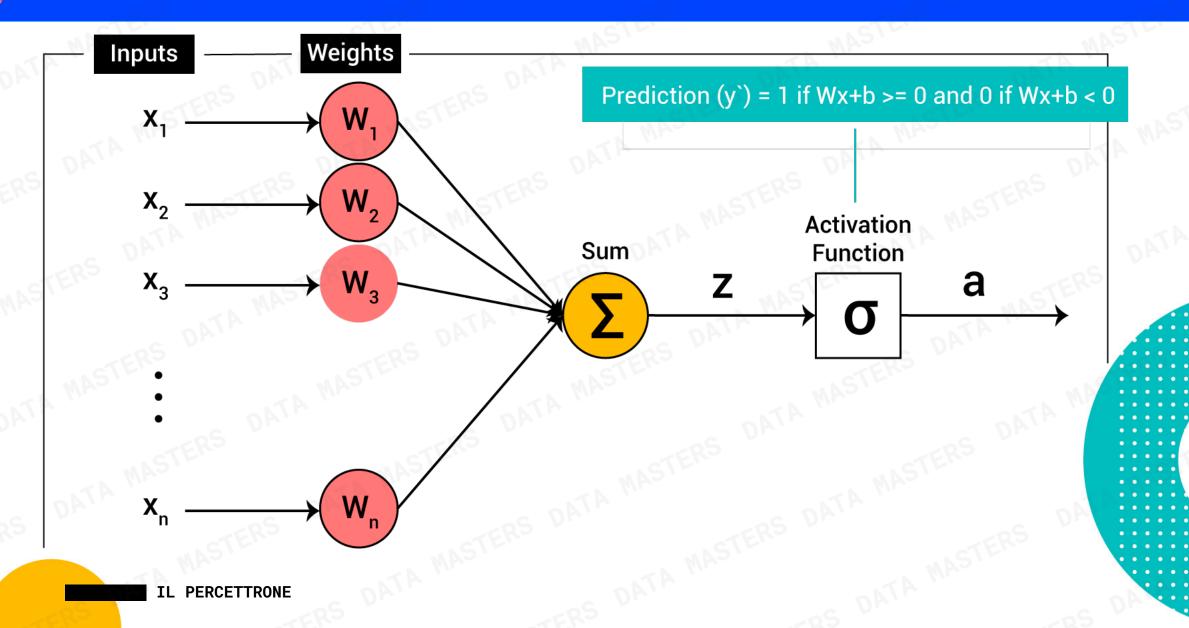
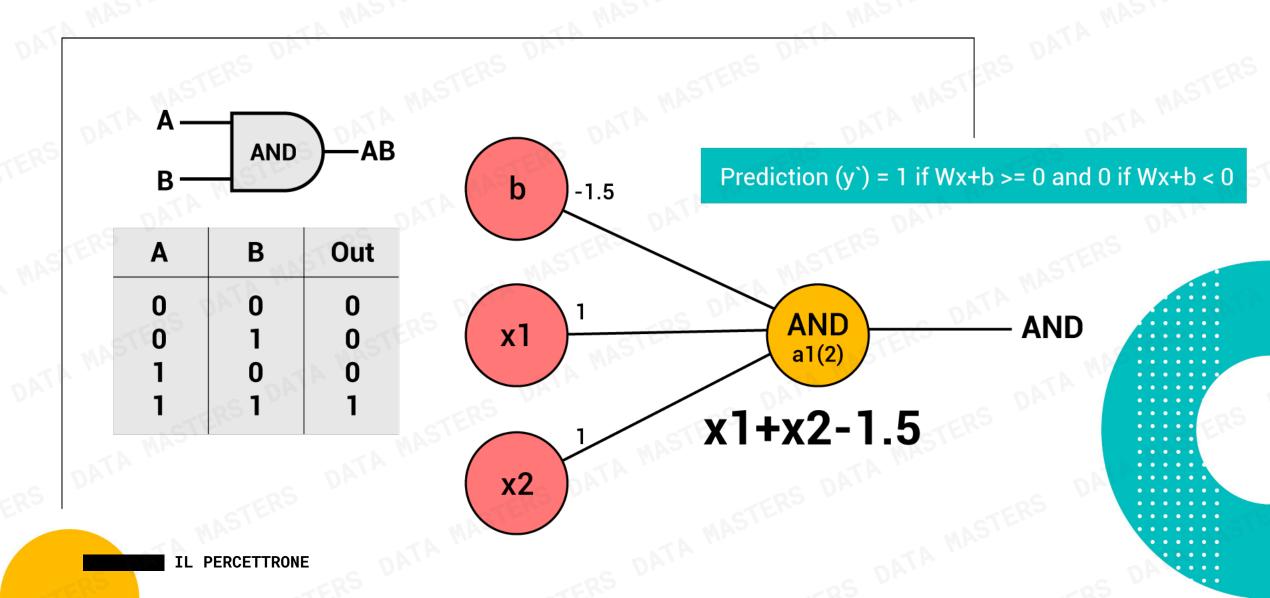
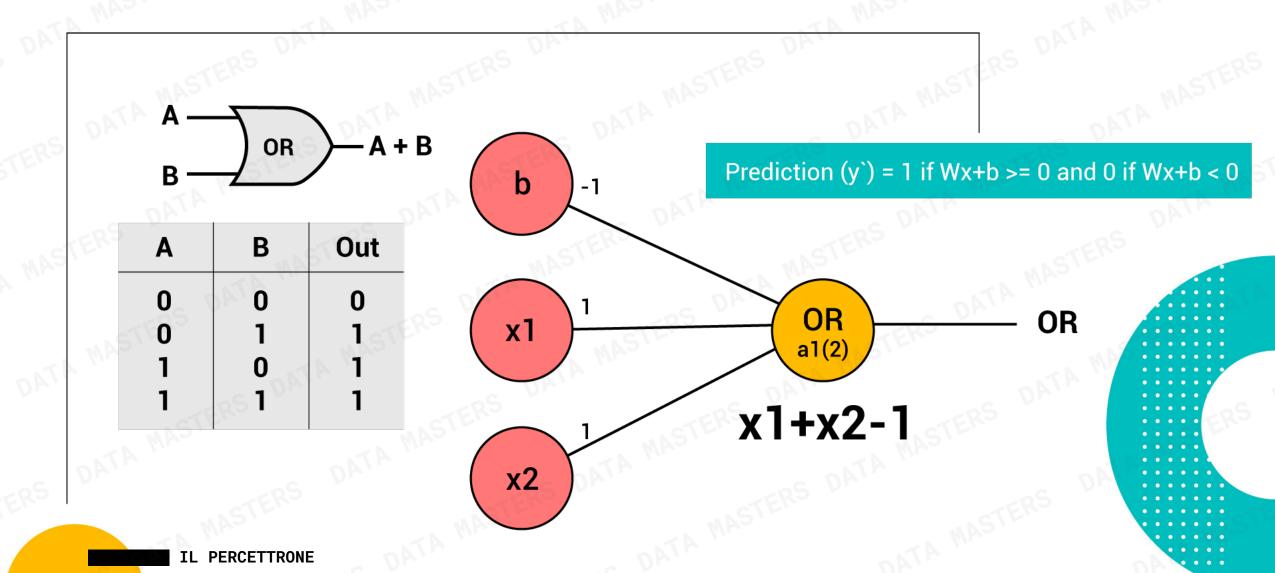
Percettrone



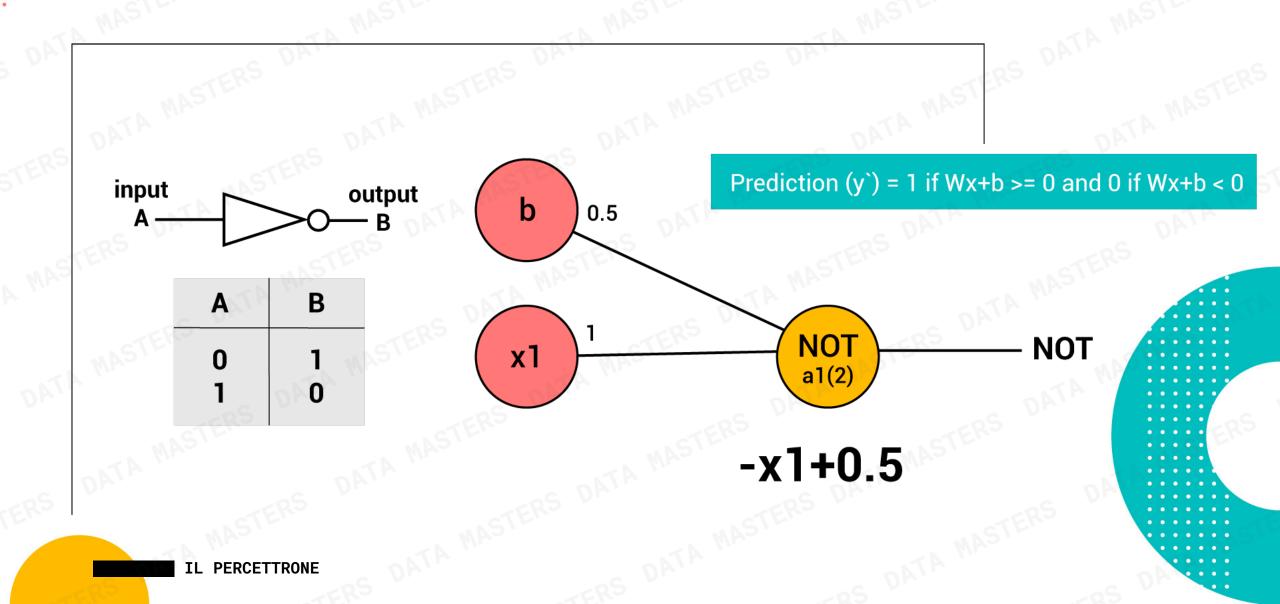
Percettrone - operatore logico AND



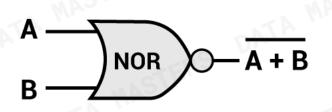
Percettrone - operatore logico OR



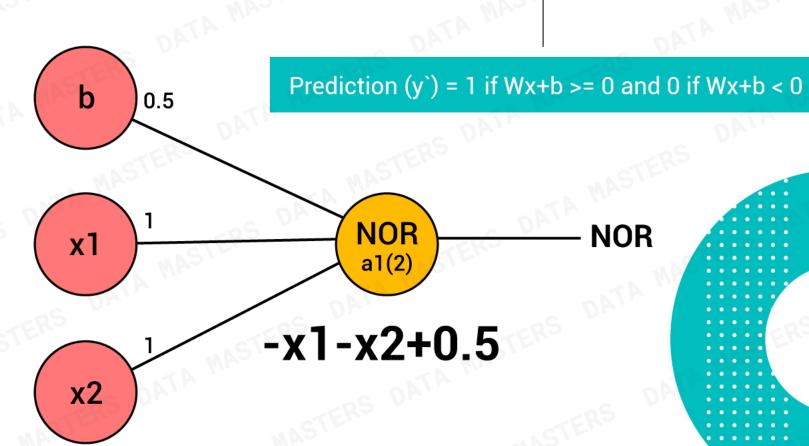
Percettrone - operatore logico NOT



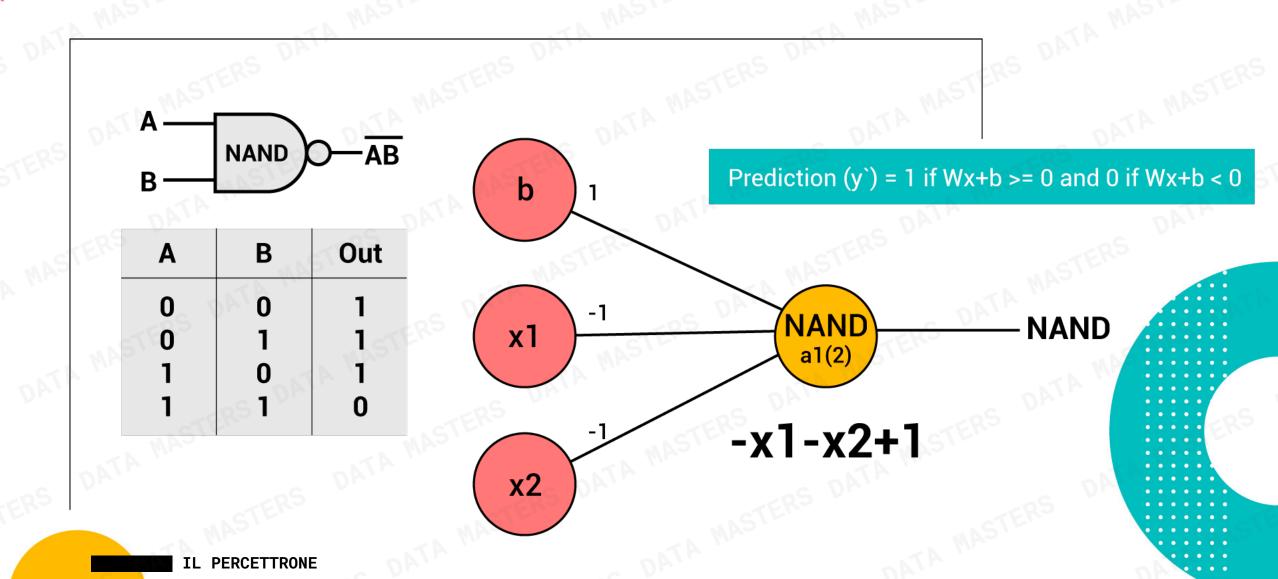
Percettrone - operatore logico NOR



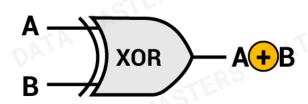
Α	В	Out
0	0	1
0	1	0
1	0	
1	RS 1 Vr	0



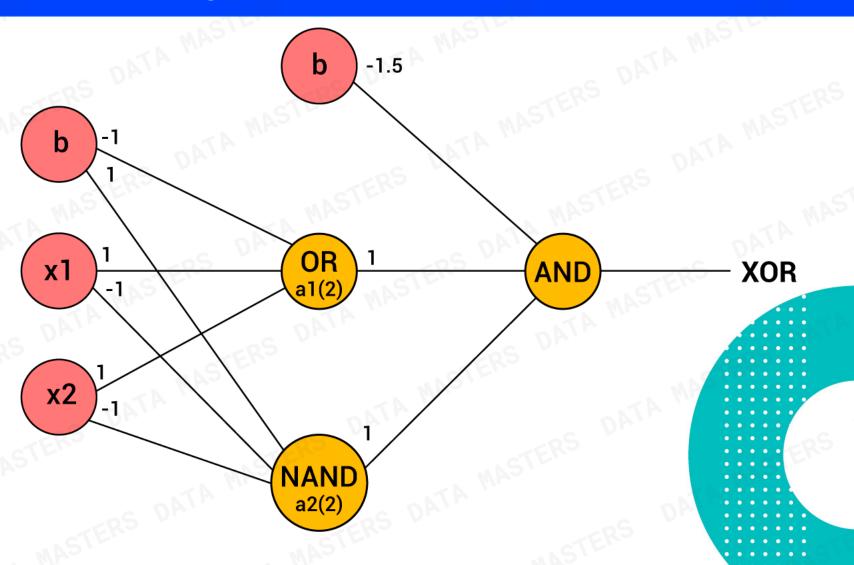
Percettrone - operatore logico NAND



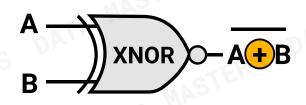
Percettrone - operatore logico XOR



RSADIA	В	Out
0	0.	0
0.00	1	1
MAST	0	TAMAS
1	1 1	0



Percettrone - operatore logico XNOR



A	В	Out
0	0	1
0	ৈ 1	0
. 11	0	0
1	1 TERS	OP1

