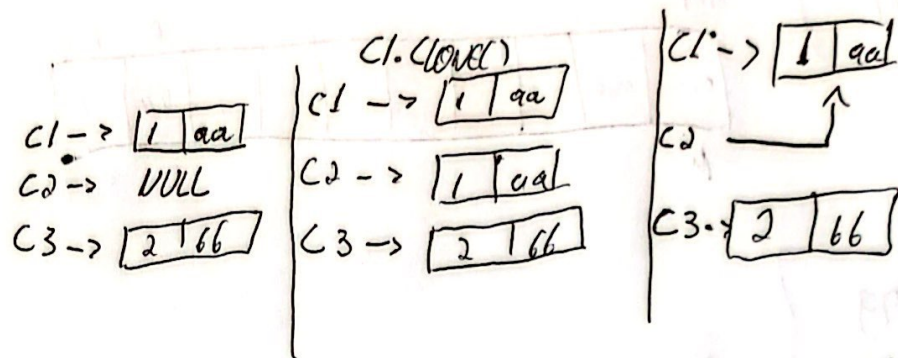


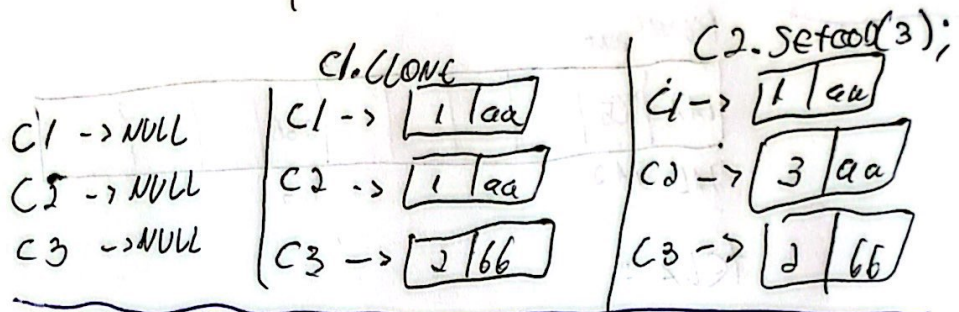
Trabalho Teórico 6 - Ponteiros e Referências

Unicelab 001°

Pg 70



Pg 83 e 84



ADDRES:

C1 → NULL
 C2 → NULL
 C3 → NULL

ADDRES:

C1 → 7Ah
 C2 → 9Ah
 C3 → A5h

ATRIBUTOS

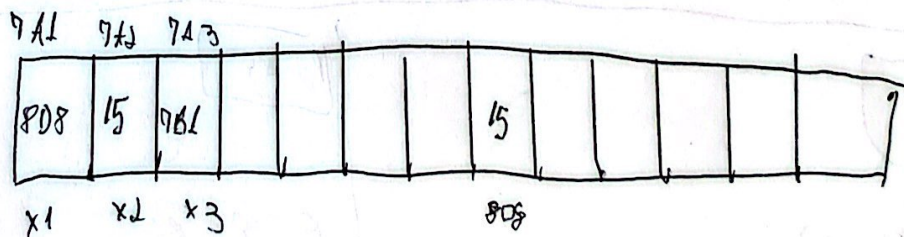
C1 → 1/aa
 C2 → 3/aa
 C3 → 2/bb

Pg 86

B - Pois o IF está comparando os endereços da memória e não os atributos

Unicelab 00 11

Pg 70

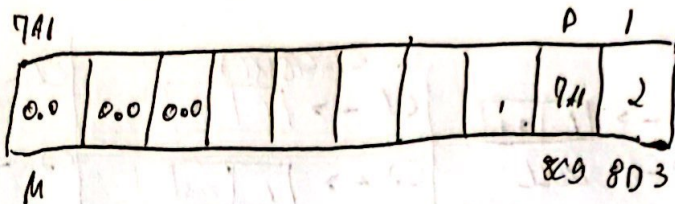


TELA:

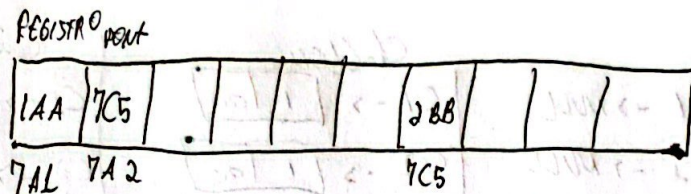
x1 (L1x0) (L1x0) (7A1) x2 (L1x0) (7B1) x3 (L1x0) (7C5)
 x1 (808) (L1x0) (7A1) x2 (L1x0) (7B1) x3 (L1x0) (L1x0) (7C5)
 x1 (808) (20) (7A1) x2 (L1x0) (7B1) x3 (L1x0) (L1x0) (7C5)
 x1 (808) (20) (7A1) x2 (20) (7B1) x3 (L1x0) (L1x0) (7C5)
 x1 (808) (20) (7A1) x2 (20) (7B1) x3 (7B1) (20) (7C5)

x1 (8D8) (20) (7A1) x2 (15) (7B1) x3 (7B1) (15) (7C5)

Pg 71



Pg 79



TELA :

FUNCIONARIO(1) = AA

FUNCIONARIO(2) = BB

Pg 81

TELA :

3.14

2.918

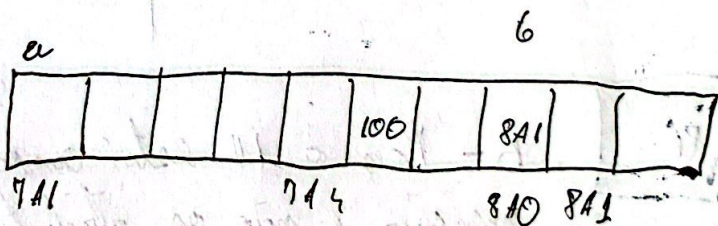
5.0

20

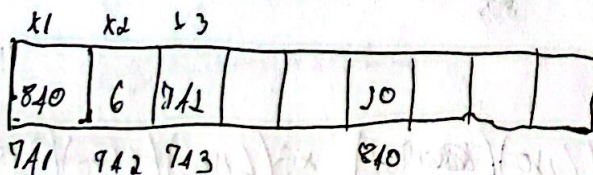
5

NULL

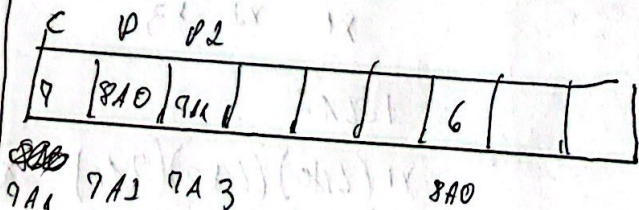
Pg 82



Pg 83



Pg 84



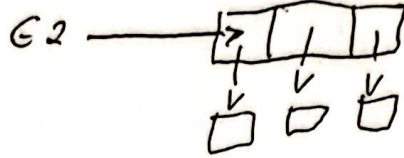
Pg 96 a. 104

Janla:

E1 → ?

E1 → ?

E2 →



Pg 105 a 121

C:

E1

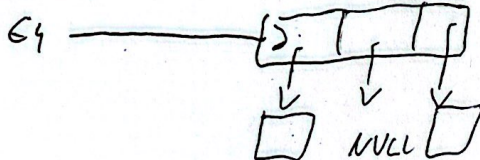
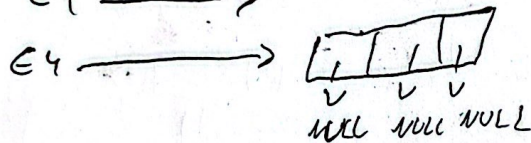
E2 →

E2 →

E2 →

E3

E4 →



Pg 122 a 138

C++

E1

E2 → NULL

E2 →

E2 →

E3

E4 → NULL

