

Low before starts

Start Bit  $\Rightarrow$  high for  $2.0 - 2.5 \text{ ms}$

Low time between bits  $\Rightarrow 0.5 \text{ ms} \pm 100 \mu\text{s}$

Zero  $\longleftrightarrow \Rightarrow 0.5 \text{ ms} - 0.75 \text{ ms}$

One  $\longleftrightarrow \Rightarrow 1.2 \text{ ms}$

CLK  $10 \text{ KHz}$

Buttons

Center  $\rightarrow$  5 0 0 1 1 | 0 0 0 0 0 0 0 1 1 0 0

Up  $\rightarrow$  5 0 0 0 1 | 1 1 1 0 0 0 0 1 1 0 1

Left  $\rightarrow$  5 0 1 0 1 | 1 1 1 0 0 0 0 1 1 0 1

Right  $\rightarrow$  5 1 1 0 1 | 1 1 1 0 0 0 0 1 1 0 1

Down  $\rightarrow$  5 1 0 0 1 | 1 1 1 0 0 0 0 1 1 0 1

V-up  $\rightarrow$  5 0 1 0 0 | 1 0 0 0 0 0 0 1 1 0 0

V-DN  $\rightarrow$  5 1 1 0 0 | 1 0 0 0 0 0 0 1 1 0 0