Static RAM

* Stack area
* Channel Pointers
* "VM" vars (i.e. system RAM)
* Scratchpad area

sram.sch

JAMMA Connector

jamma.sch

Arithmetic & Logic Unit

* 16bit ALU
* ALU Input Registers
* ALU Output Latches

alu.sch

Master Clock

master_clock.sch

Code ROM Banks

* Code ROM
* Instruction Pointer Register
* ROM Bank Register

code_banks.sch

Video Signal Output

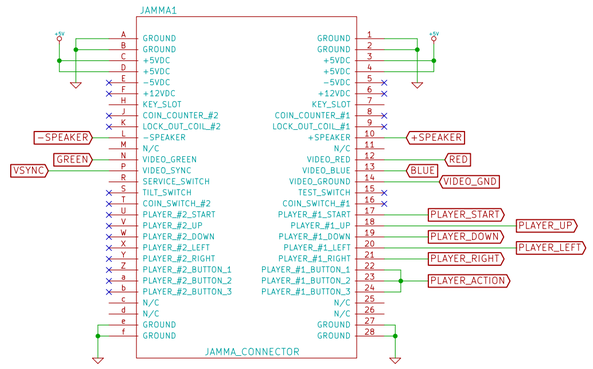
video_output.sch

Microcode Sequencer

* Microcode ROMs
* Opcode Register

microcode.sch

File: anotherworld.sch		
Sheet: /		
Title:		
Size: A2	Date: 16 aug 2016	Rev:
KiCad E.D.A.		Id: 1/8



File: jamma.sch		
Sheet: /JAMMA Connector/		
Title:		
Size: A2	Date: 16 aug 2016	Rev:
KiCad E.D.A.		Id: 2/8

TO-DO:

? MHz

MASTER_CLOCK

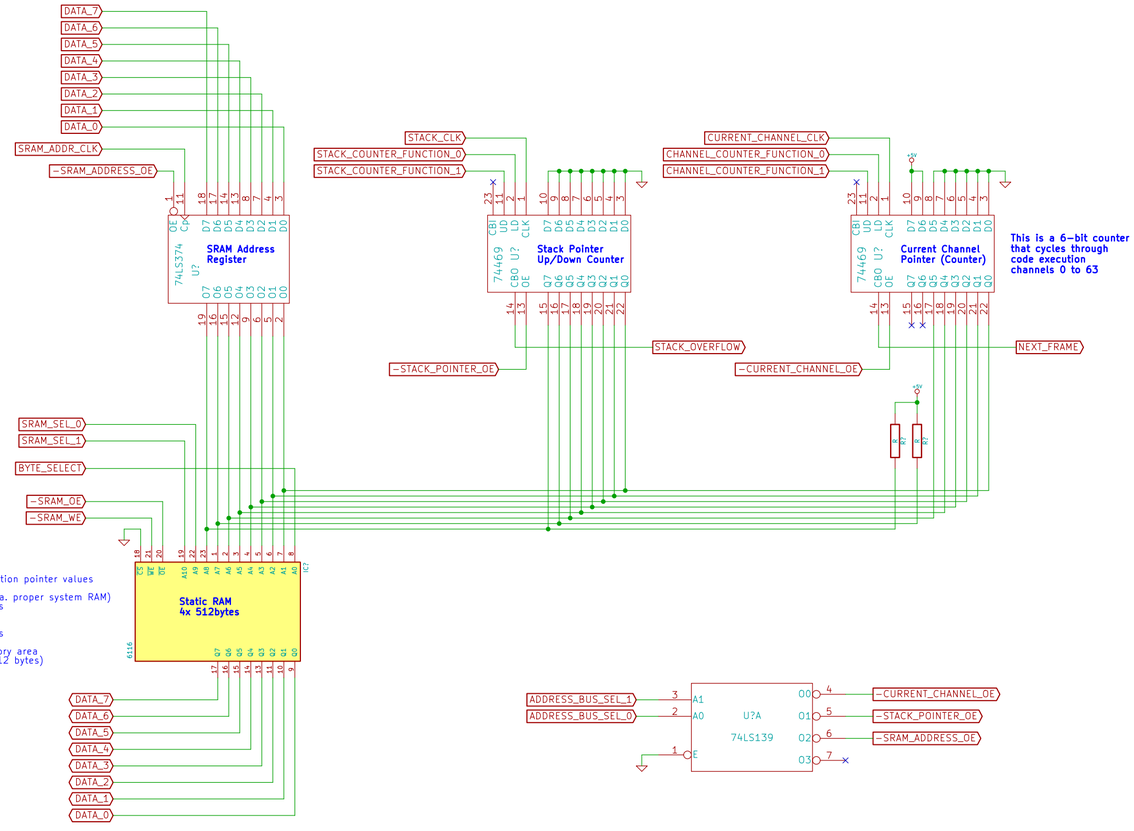
(CRYSTAL OSCILATOR)

TO-DO:

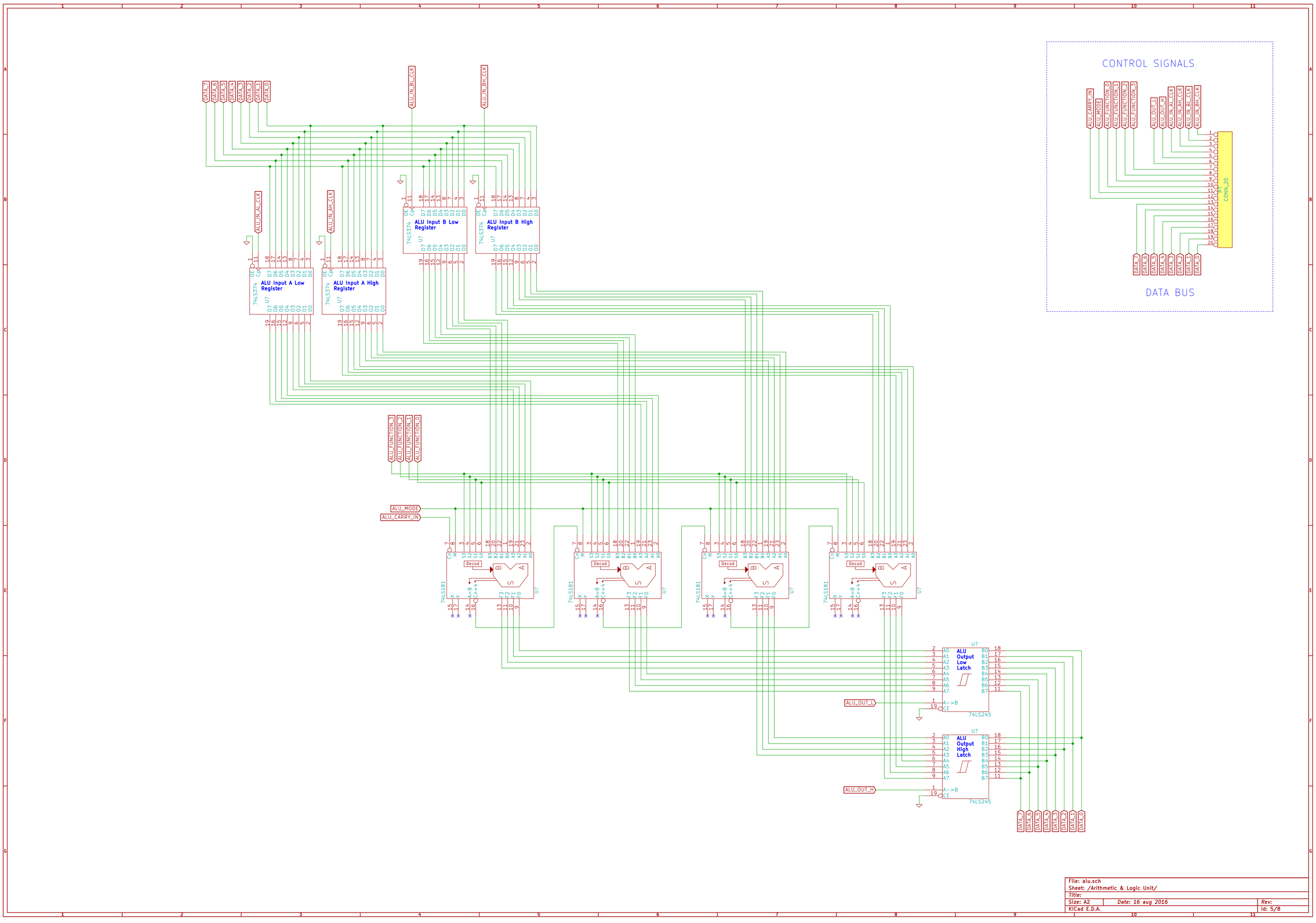
15.75MHz

PIXEL_CLOCK

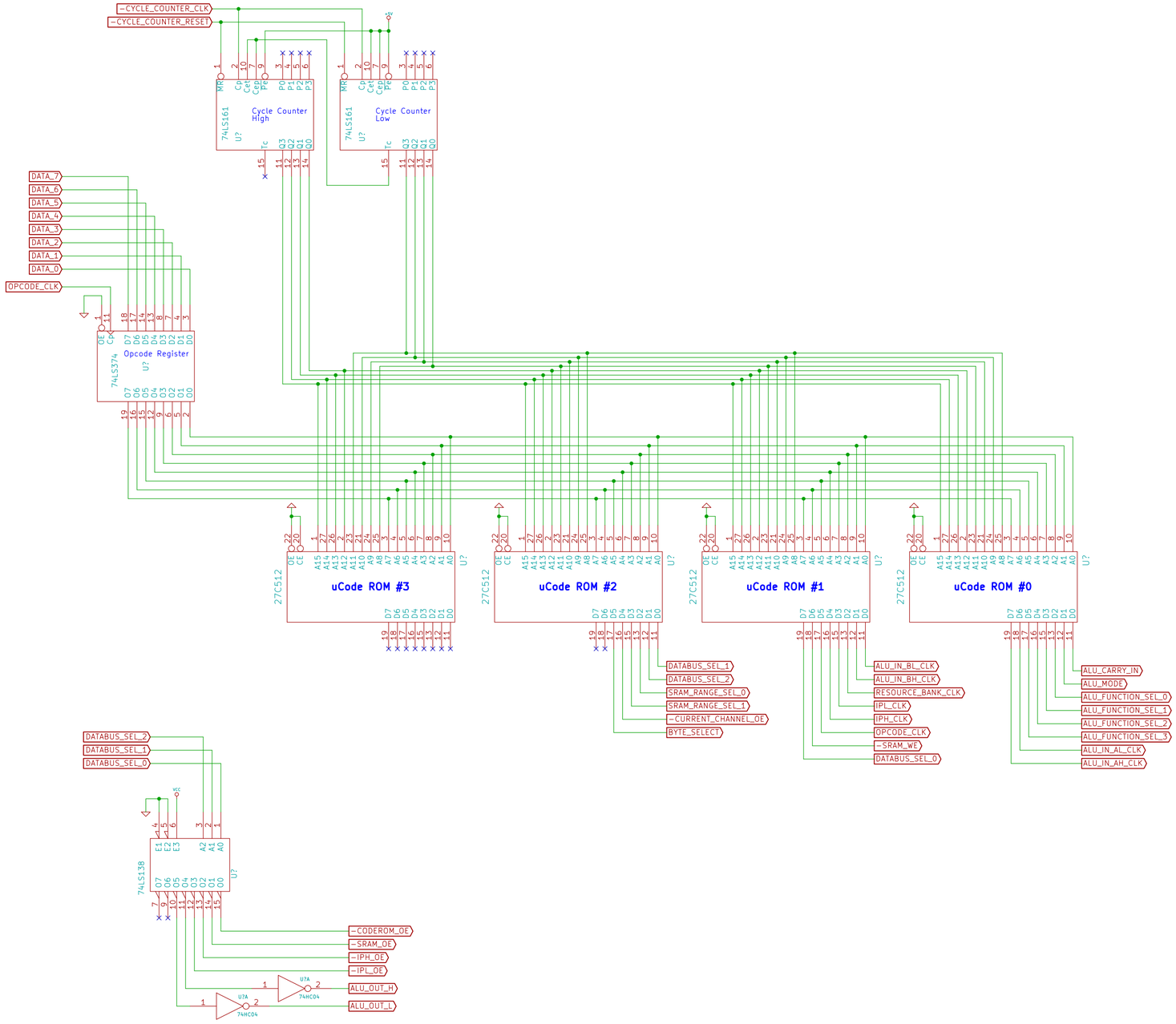
File: master_clock.sch		
Sheet: /Master Clock/		
Title:		
Size: A2	Date: 16 aug 2016	Rev:
KiCad E.D.A.		Id: 3/8



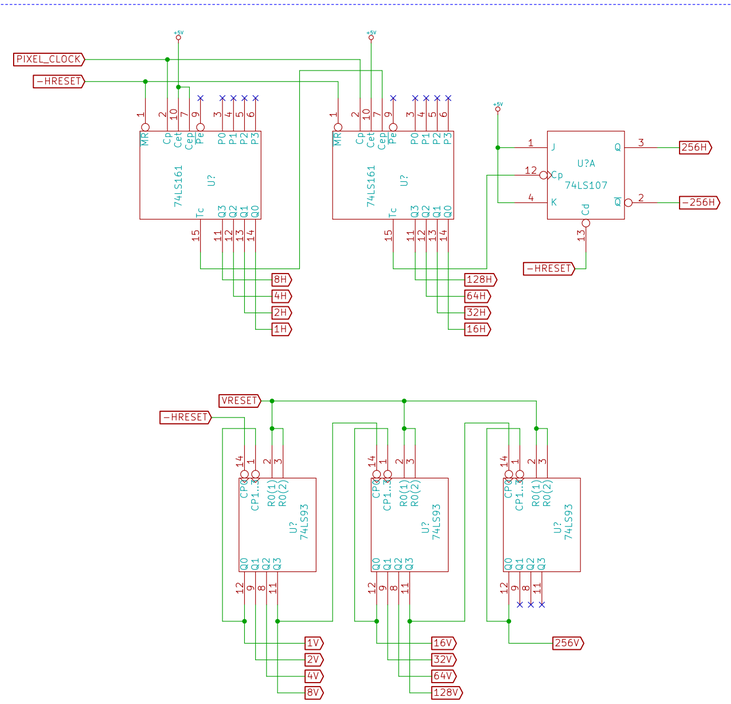
File: sram.sch		
Sheet: /Static RAM/		
Title:		
Size: A2	Date: 16 aug 2016	Rev:
KiCad E.D.A.		Id: 4/8

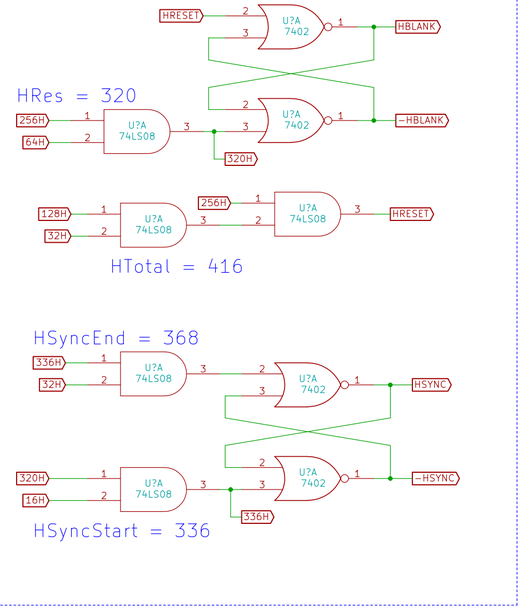


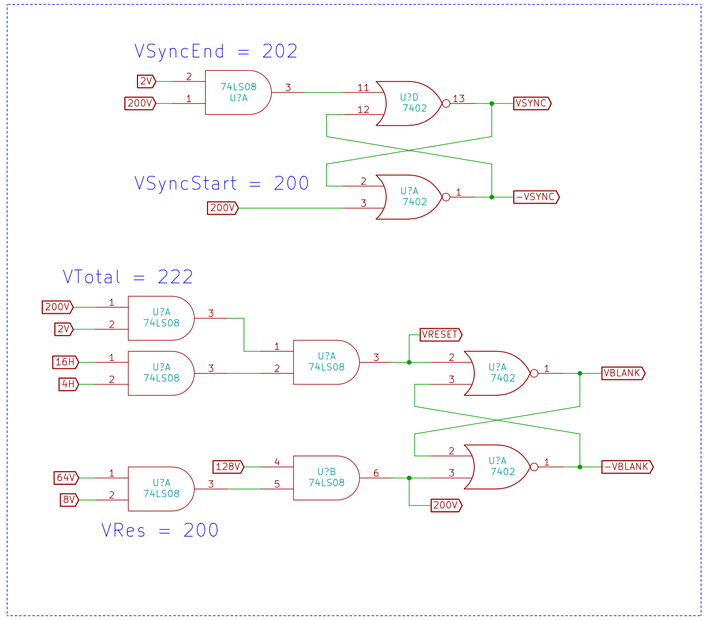
File: alu.sch		
Sheet: /Arithmetic & Logic Unit/		
Title:		
Size: A2	Date: 16 aug 2016	Rev:
KiCad E.D.A.		Id: 5/8

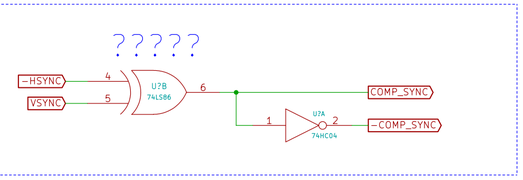


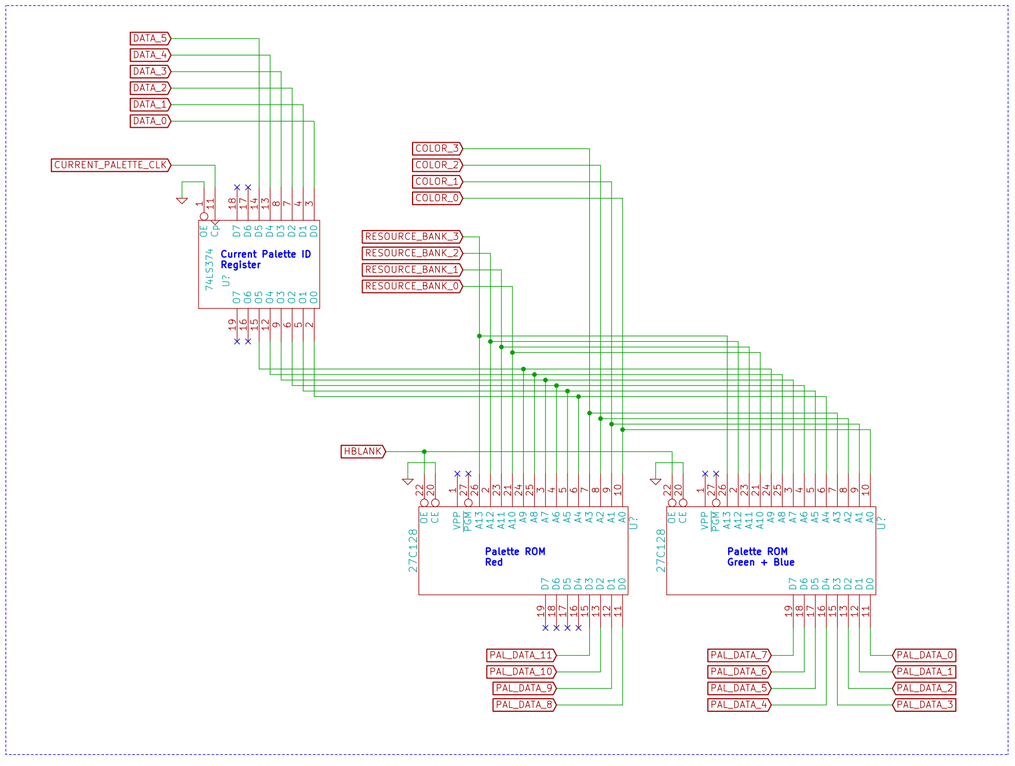
File: microcode.sch		
Sheet: /Microcode Sequencer/		
Title:		
Size: A2	Date: 16 aug 2016	Rev:
KiCad E.D.A.		Id: 6/8

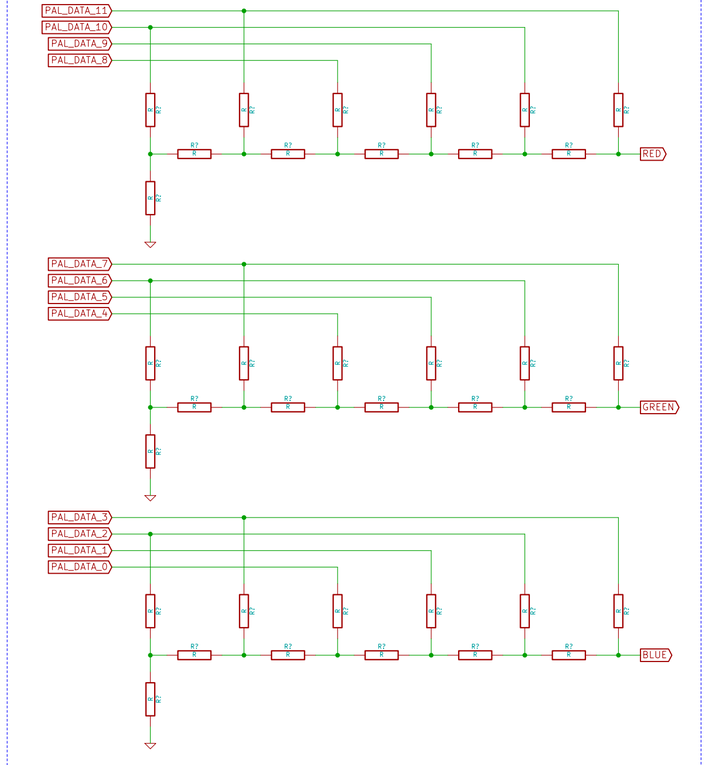








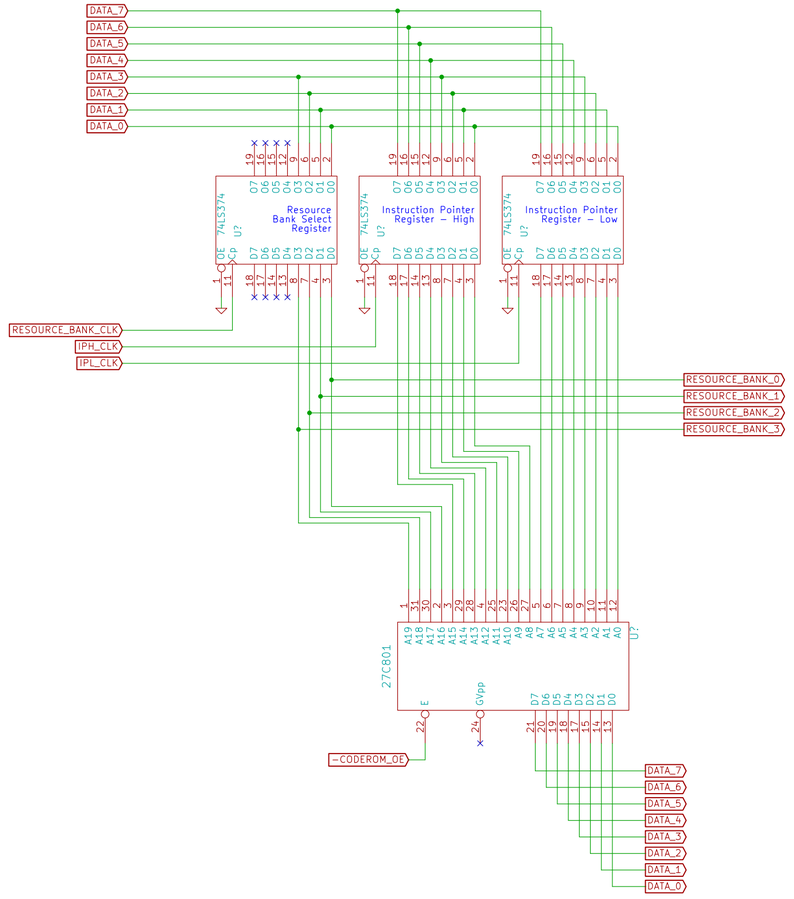




TO—DO:

VIDEO SRAM

File: video_output.sch		
Sheet: /Video Signal Output/		
Title:		
Size: A2	Date: 16 aug 2016	Rev:
KiCad E.D.A.		Id: 7/8



File: code_banks.sch		
Sheet: /Code ROM Banks/		
Title:		
Size: A2	Date: 16 aug 2016	Rev:
KiCad E.D.A.		Id: 8/8