Static RAM JAMMA Connector * Stack area * Channel Pointers * ''VM'' vars (i.e. system RAM) * Scratchpad area sram.sch jamma.sch Arithmetic & Logic Unit Master Clock * 16bit ALU * ALU Input Registers * ALU Output Latches master_clock.sch alu.sch Code ROM Banks Video Signal Output * Code ROM * Instruction Pointer Register * ROM Bank Register video_output.sch code_banks.sch Microcode Sequencer * Microcode ROMs * Opcode Register microcode.sch













