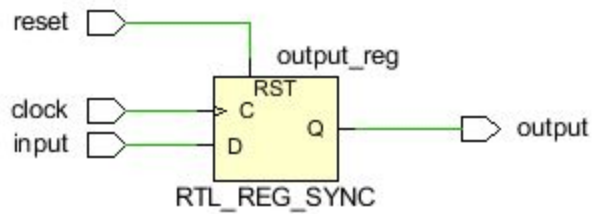
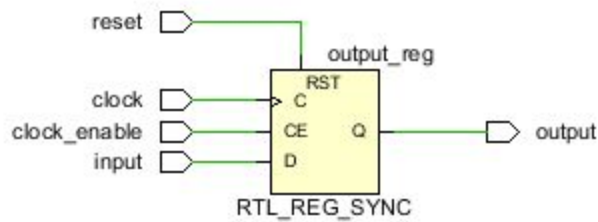


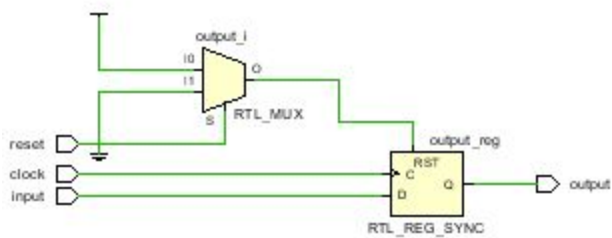
w/Synchronous Active High Reset:



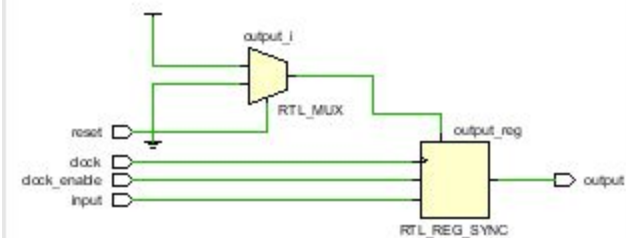
w/Synchronous Active High Reset with chip enable (CE):



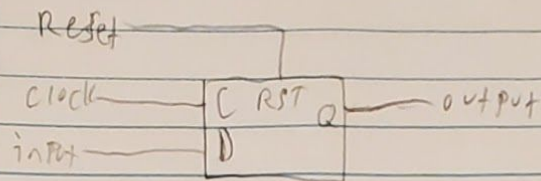
w/Synchronous Active Low Reset:



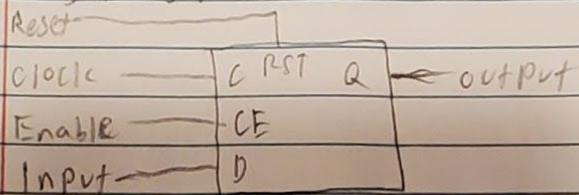
w/Synchronous Active Low Reset with chip enable (CE):



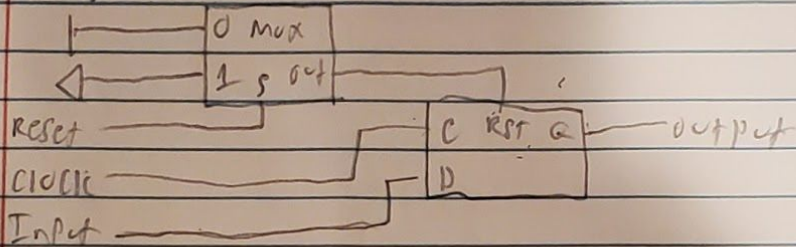
High reset:



High reset CE:



Low reset:



Low reset CE:

