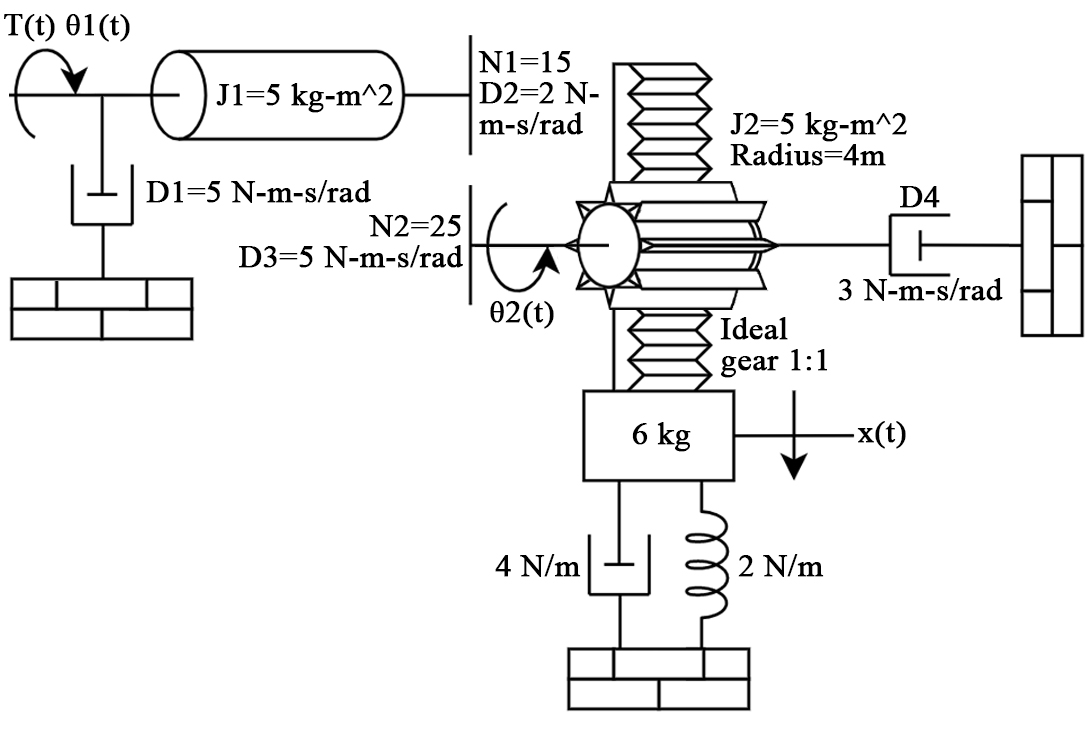
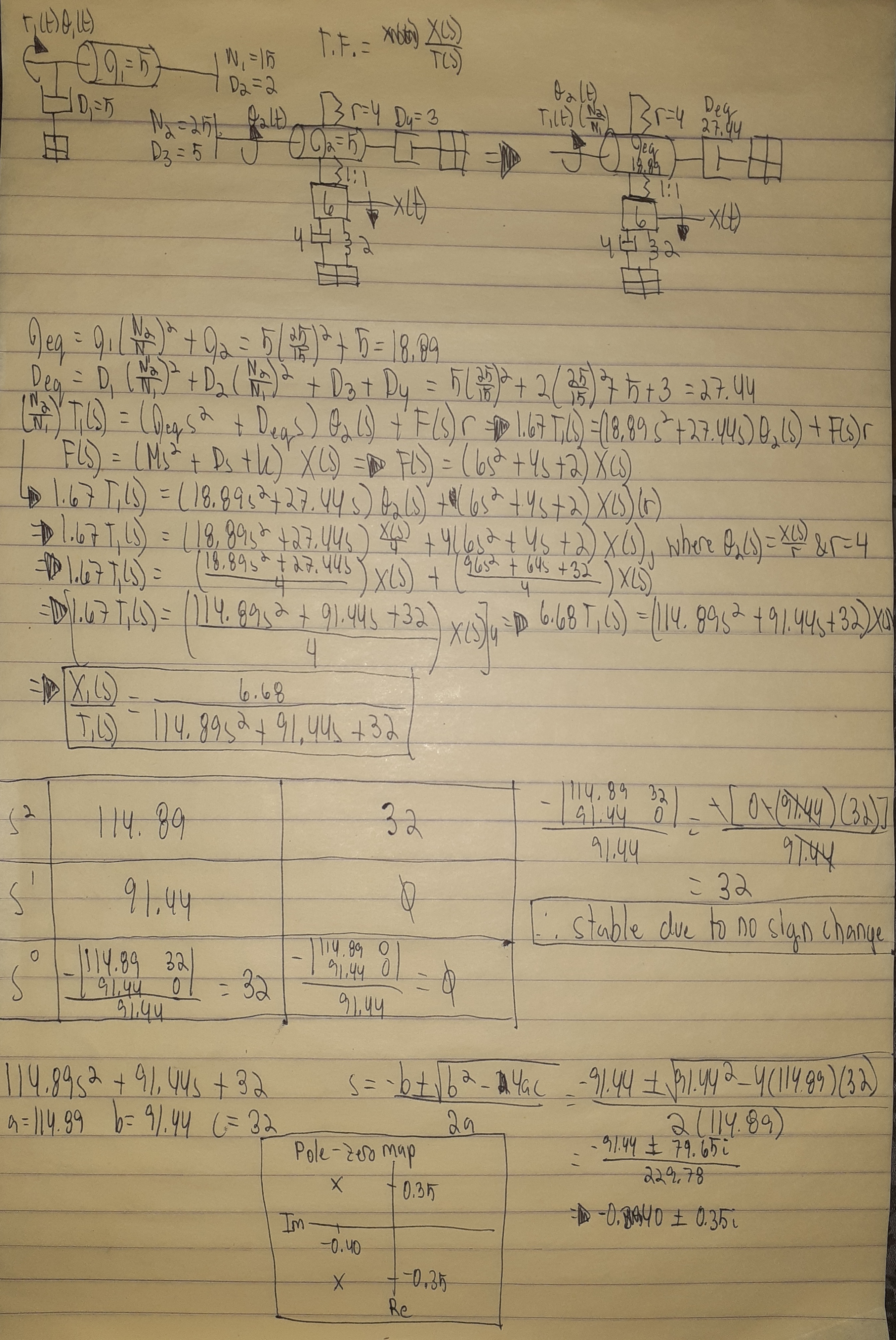
**System Design**

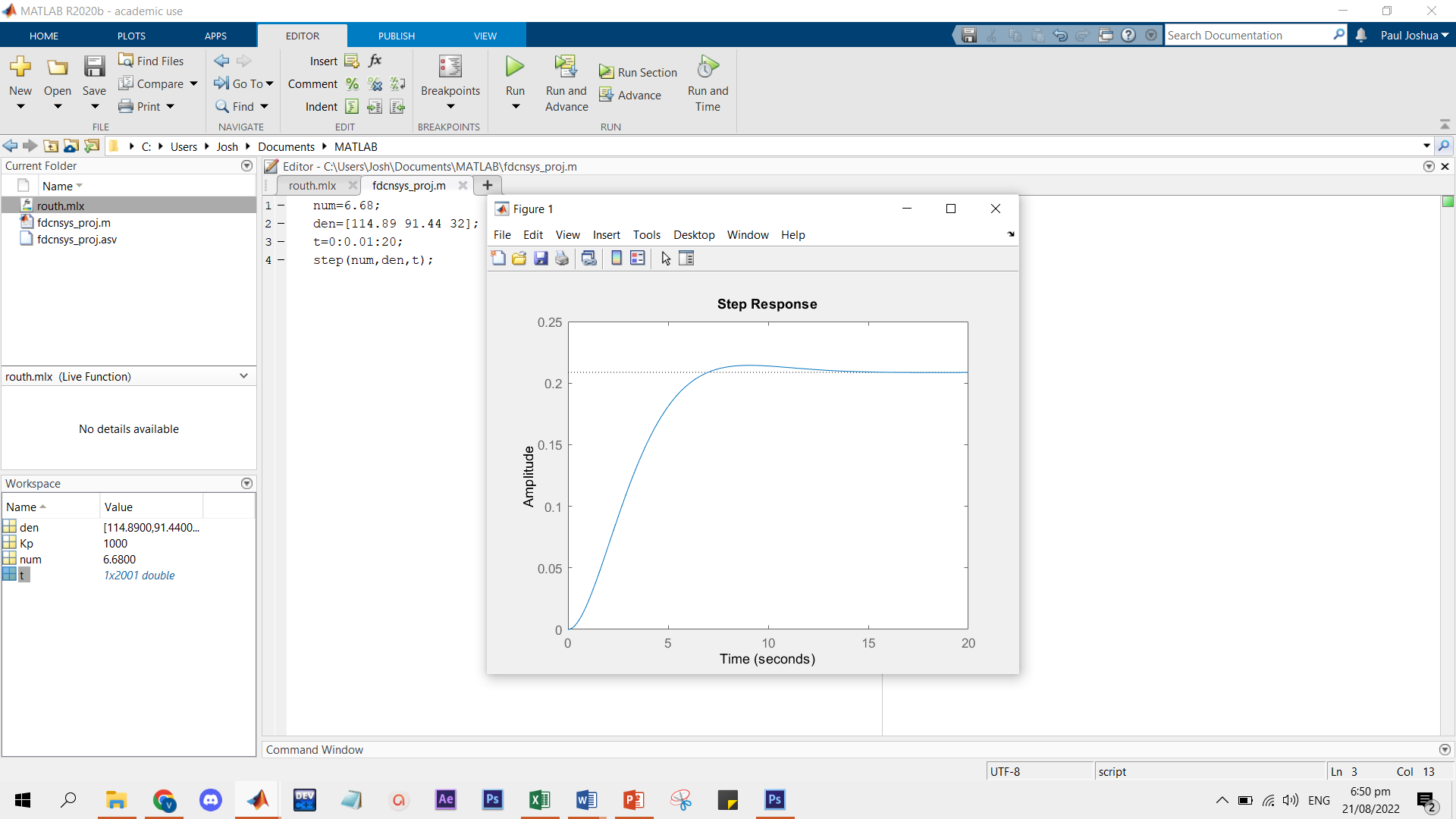


**Transfer Function**

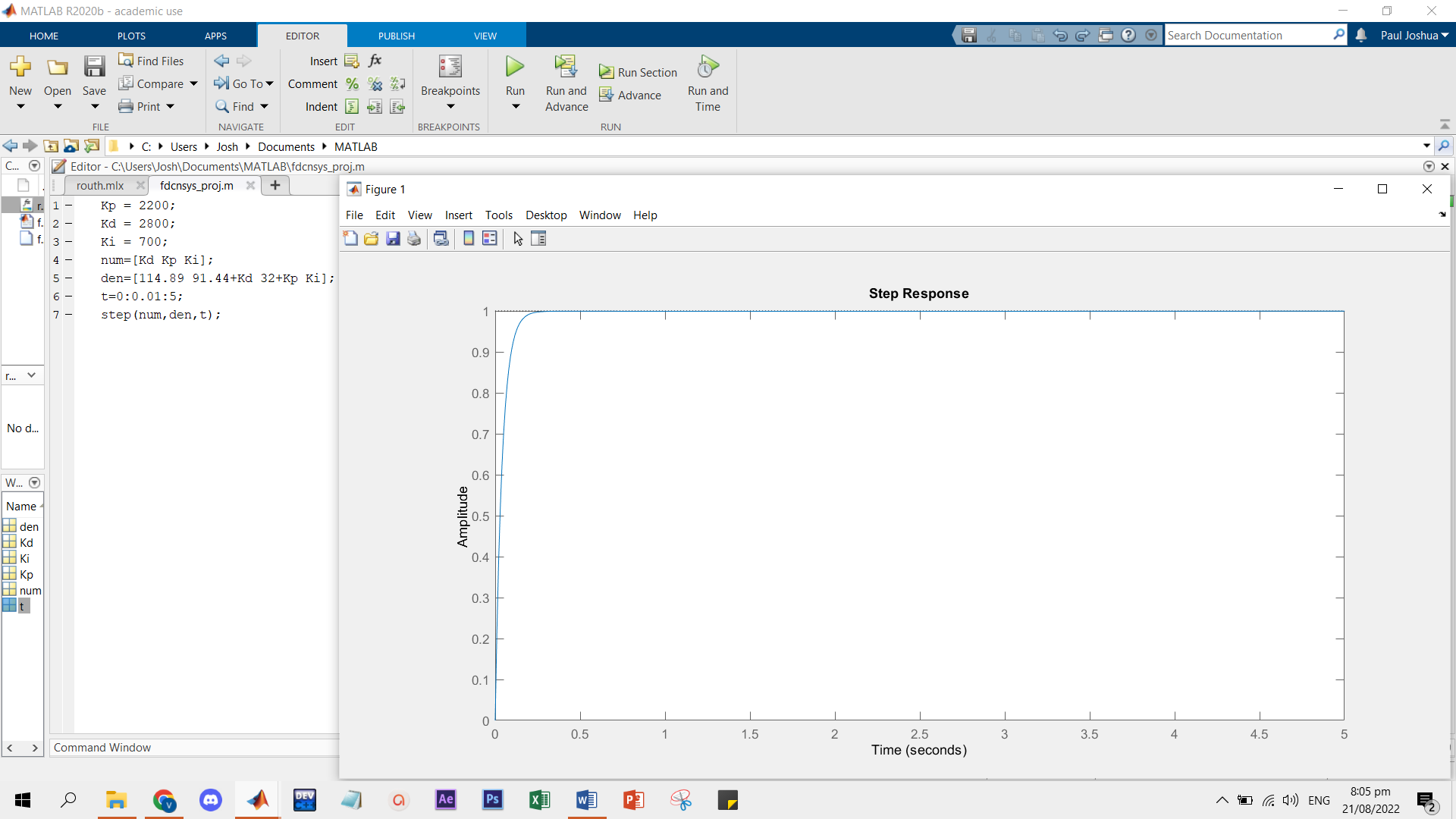


**PID Controller**

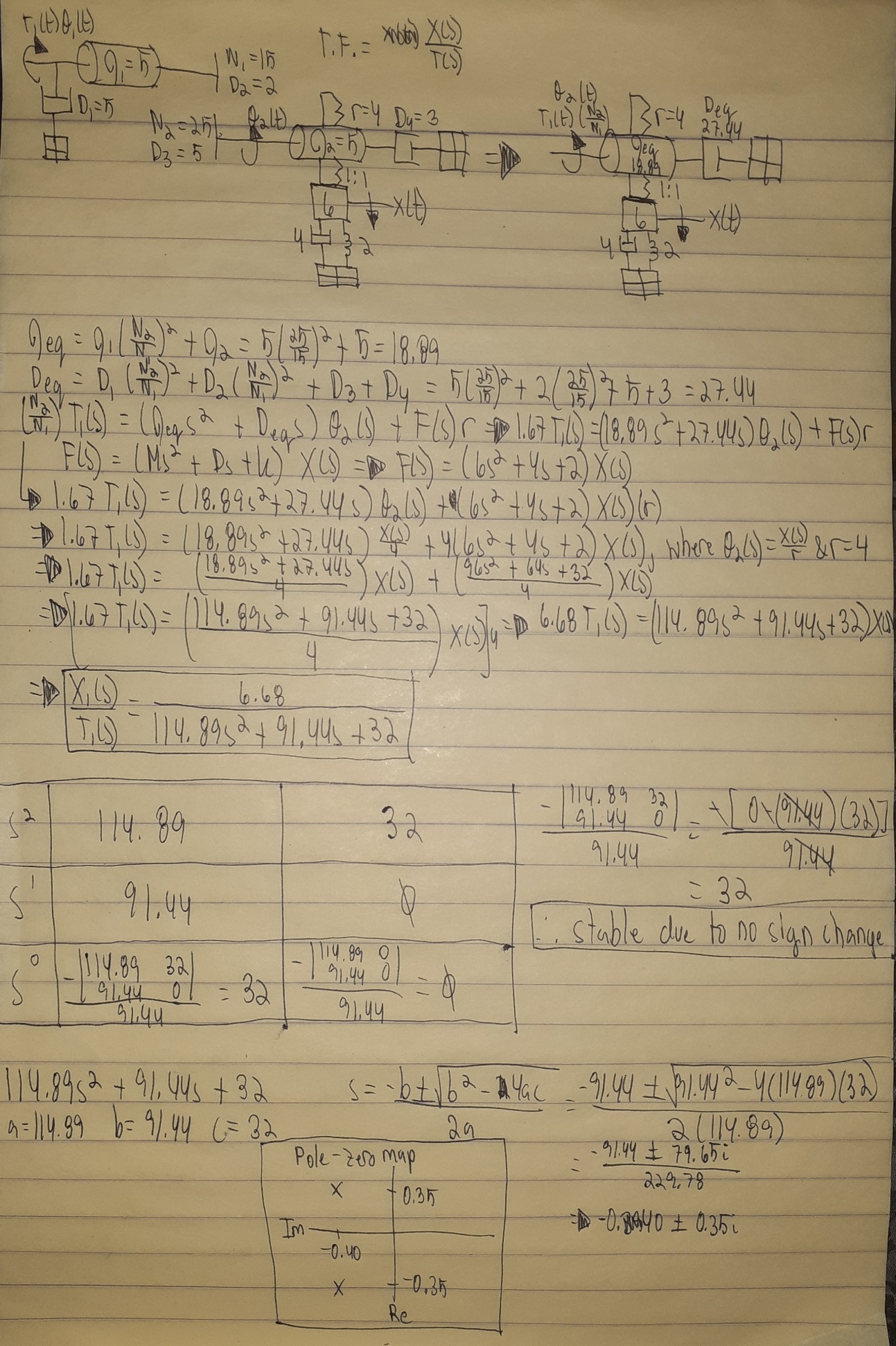
1. Original

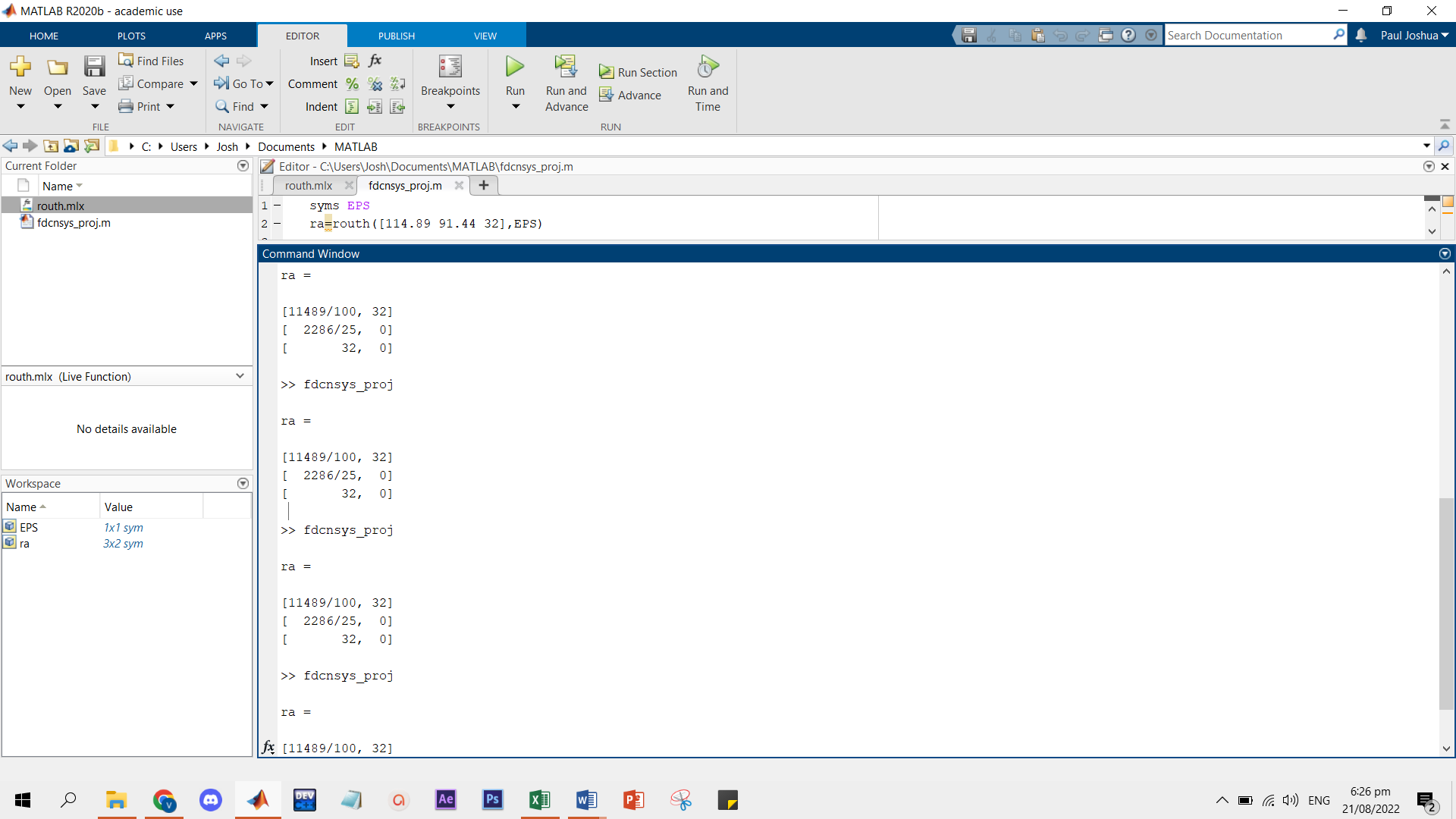


2. Modified

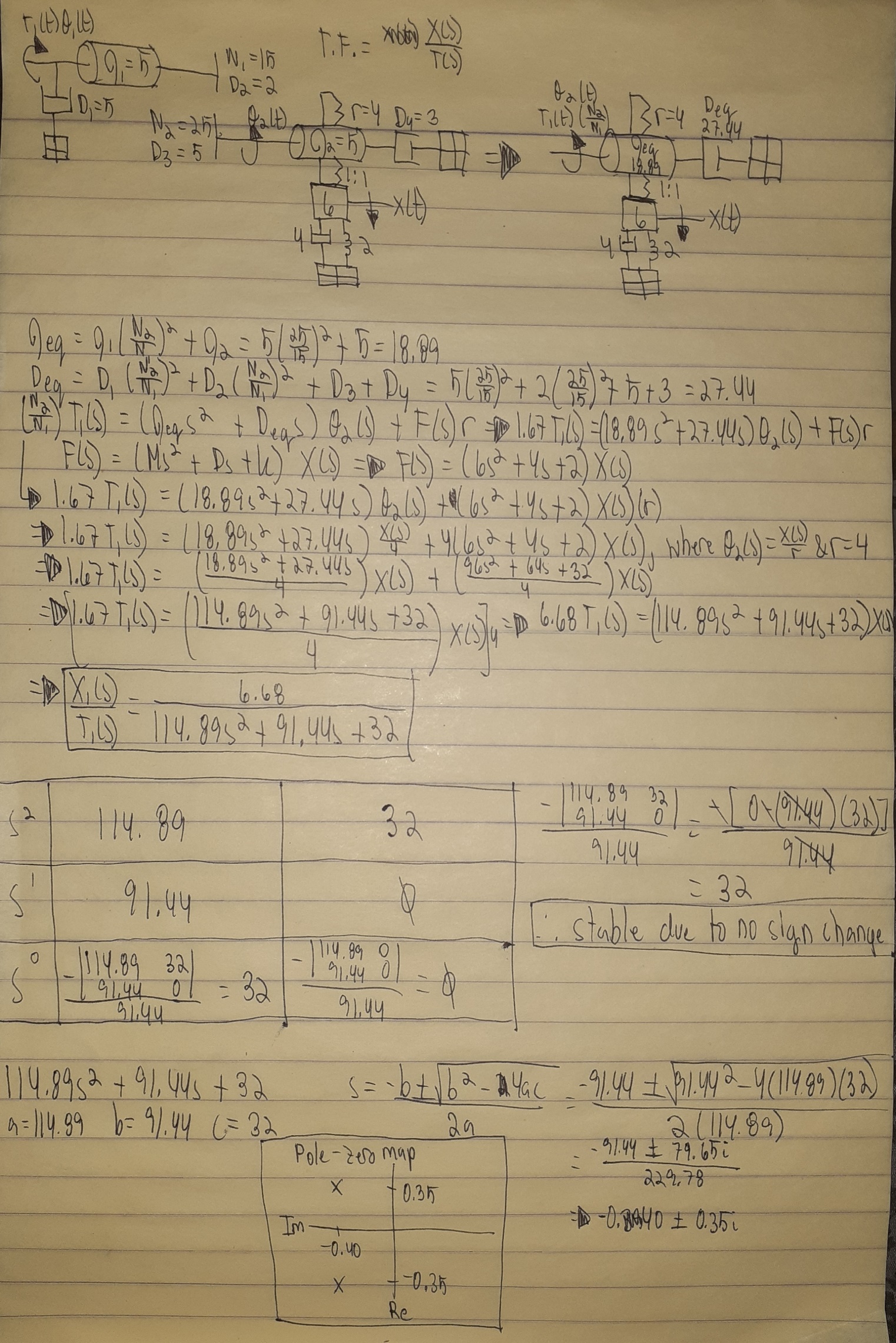


**Routh Table**

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**Pole-Zero Map**

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