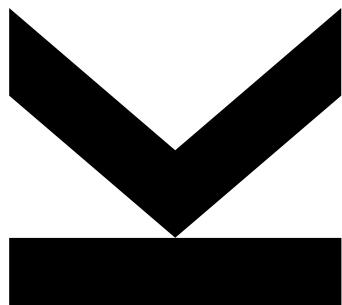


KV Entwurf Integrierter Schaltungen

WiSe25

PROJEKT-TITEL



Datum: XY.10.2025

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1 ToDo

2 Section ToDo

ToDo Text

2.1 Subsection ToDo

Example: Another approach to increasing the linearity, in the case of source-degeneration Sec. 2 and Sec. 2.1 is the technique of gain-boosting. The measurement result can be verified with (1).

... results into

$$R_{\text{eq}} = \frac{1}{f_{\text{NOC}} C_s} \quad (1)$$

Due to the following items

- Item 1
- Item 2

Due to the following items

1. Item 1
2. Item 2

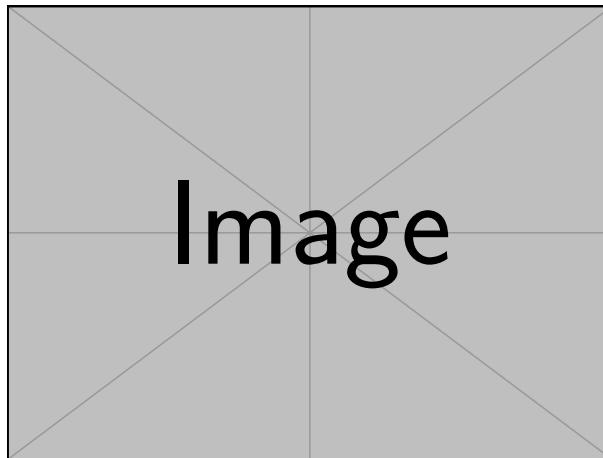


Figure 1: ToDo figure caption

Table:

SPECIFICATION ITEM	PARAMETER
System clock frequency	65 536 Hz
Signal bandwidth	0.1 Hz to 128 Hz
DC offset range	$<\pm 250 \text{ mV}$
Input impedance (in signal bandwidth)	$>100 \text{ M}\Omega$
Maximum input signal (full scale)	$\pm 10 \text{ mV}$
Nominal input signal (peak-to-peak)	$10 \text{ }\mu\text{V}$
Minimum input signal resolution	$<0.2 \text{ }\mu\text{V}$
Number of bits	$\log_2(20 \text{ mV}/0.2 \text{ }\mu\text{V}) = 16.6 \text{ bits}$
Input structure type	fully differential
Input common-mode rejection ratio	$>80 \text{ dB}$
Pre-amplifier	no
Chip area	to be minimized ($<0.5 \text{ mm}^2$)
Supply voltage V_{DD}	1.8 V
Power consumption	$<1 \text{ }\mu\text{W}$
Technology	IHP

Table 1: Target specifications of the biosignal aquisition ADC