

# Felix Maximilian Schlegel

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## Education

**Technical University of Munich** Oct. 2020 — Present

*B.Sc. Computer Science, Current Grade: 1.9 (equivalent to GPA of 3.1)*

Relevant Courses: Introduction to Computer Organization and Technology covering logic circuit design, FPGA design in VHDL and low-level software development in Assembly

**Schiller Schule, Bochum** Aug. 2012 — Jun. 2020

*High School Diploma, Grade: 1.9 (equivalent to GPA of 3.1)*

Majored in Mathematics and English, member of Computer Science club

**St Edmund's School, Canterbury** Sep. 2016 — Jul. 2017

*Year abroad*

Member of 1<sup>st</sup> Tennis Team, playing the trumpet in Big Band

## Work Experience

**Techbuddy AB, Stockholm** Aug. 2020 — Sep. 2020

*Summer Internship*

Development of experimental features for the Techbuddy App using React Native, Swift and Java

**G Data CyberDefense AG, Bochum** Jan. 2018 — Feb. 2018

*Internship*

Development of a web service in Java that enabled malware analysts to see correlations inside the company's malware database

**Alpha 9 Software Ltd., Canterbury** Jun. 2017

*Internship*

Introduction into Test-driven Development building applications in C#

## Projects and Awards

**Jugend Forscht** Feb. 2020

*2<sup>nd</sup> prize Regional Competition*

Automated creation of MIDI files using Recurrent Neural Networks with the help of PyTorch

**Macoun Conference** Oct. 2019

*Motion Capturing in ARKit + RealityKit*

Talk about Motion Capturing in ARKit at Europe's biggest macOS and iOS conference

**Apple Inc.** Jun. 2019

*WWDC19 Scholarship*

Augmented Reality application visualising San Francisco International Airport built in Swift Playgrounds (Click to see)

**Apple Inc.** Jun. 2018

*WWDC18 Scholarship*

2D beach volleyball game built in Swift Playgrounds (Click to see)

## Skills and Interests

**Languages** German (native), English (fluent), Spanish (B2)

**Technologies** HTML/CSS/JavaScript, React, Java, Python, C, Assembly, VHDL