Group对照表

mod R/M	xx000xxx	xx001xxx	xx010xxx	xx011xxx	xx100xxx	xx101xxx	xx110xxx	xx111xxx
group #1 (80h)	ADD Eb,Ib	OR Eb,Ib	ADC Eb,Ib	SBB Eb,Ib	AND Eb,Ib	SUB Eb,Ib	XOR Eb,Ib	CMP Eb,Ib
group #1 (81h)	ADD Ev,Iz	OR Ev,Iz	ADC Ev,Iz	SBB Ev,Iz	AND Ev,Iz	SUB Ev,Iz	XOR Ev,Iz	CMP Ev,Iz
group #1 (82h)	ADD* Eb,Ib	OR* Eb,Ib	ADC* Eb,Ib	SBB* Eb,Ib	AND* Eb,Ib	SUB* Eb,Ib	XOR* Eb,Ib	CMP* Eb,Ib
group #1 (83h)	ADD Ev,Ib	OR Ev,Ib	ADC Ev,Ib	SBB Ev,Ib	AND Ev,Ib	SUB Ev,Ib	XOR Ev,Ib	CMP Ev,Ib
group #1A (8Fh)	POP^{D64} Ev	XOP (000b) XOP (001b) XOP (010b)						
group #2 (C0h) (80186+)	ROL Eb,Ib	ROR Eb,Ib	RCL Eb,Ib	RCR Eb,Ib	SHL Eb,Ib	SHR Eb,Ib	SAL* Eb,Ib	SAR Eb,Ib
group #2 (C1h) (80186+)	ROL Ev,Ib	ROR Ev,Ib	RCL Ev,Ib	RCR Ev,Ib	SHL Ev,Ib	SHR Ev,Ib	SAL* Ev,Ib	SAR Ev,Ib
group #2 (D0h)	ROL Eb,1	ROR Eb,1	RCL Eb,1	RCR Eb,1	SHL Eb,1	SHR Eb,1	SAL* Eb,1	SAR Eb,1
group #2 (D1h)	ROL Ev,1	ROR Ev,1	RCL Ev,1	RCR Ev,1	SHL Ev,1	SHR Ev,1	SAL* Ev,1	SAR Ev,1
group #2 (D2h)	ROL Eb,CL	ROR Eb,CL	RCL Eb,CL	RCR Eb,CL	SHL Eb,CL	SHR Eb,CL	SAL* Eb,CL	SAR Eb,CL
group #2 (D3h)	ROL Ev,CL	ROR Ev,CL	RCL Ev,CL	RCR Ev,CL	SHL Ev,CL	SHR Ev,CL	SAL* Ev,CL	SAR Ev,CL
group #3 (F6h)	TEST Eb,Ib	TEST* Eb,Ib	NOT Eb	NEG Eb	MUL Eb	IMUL Eb	DIV Eb	IDIV Eb
group #3 (F7h)	TEST Ev,Iz	TEST* Ev,Iz	NOT Ev	NEG Ev	MUL Ev	IMUL Ev	DIV Ev	IDIV Ev
group #4 (FEh)	INC Eb	DEC Eb						
group #5 (FFh)	INC Ev	DEC Ev	CALL^{Df64} Ev	CALL Mp (w:z) (or call gate's *y*)	JMP^{Df64} Ev	JMP Mp (w:z) (or call gate's *y*)	PUSH^{D64} Ev	

group #6 (0Fh,00h)	SLDT Mw	STR Mw	LLDT Mw	LTR Mw	VERR Mw	VERW Mw	JMPE Ev (IA-64)	
	SLDT Rv	STR Rv	LLDT Rv	LTR Rv	VERR Rv	VERW Rv		
group #7 (0Fh,01h)	SGDT^{F64} Mp (w:*y*)	SIDT^{F64} Mp (w:*y*)	LGDT^{F64} Mp (w:*y*)	LIDT^{F64} Mp (w:*y*)	SMSW Mw	(F3h) RSTORSSP Mq (see CPUID)	LMSW Mw	INVLPG M (80486+)
	VMCALL (C1h) VMLAUNCH (C2h) VMRESUME (C3h) VMXOFF (C4h) (see CPUID)	MONITOR (C8h) MWAIT (C9h) CLAC (CAh) STAC (CBh) ENCLS (CFh) (see CPUID)	XGETBV (D0h) XSETBV (D1h) VMFUNC (D4h) XEND (D5h) XTEST (D6h) ENCLU (D7h) (see CPUID)	VMRUN (D8h) VMMCALL (D9h) VMLOAD (DAh) VMSAVE (DBh) STGI (DCh) CLGI (DDh) SKINIT (DEh) INVLPGA (DFh) (see CPUID) (F3h) VMGexit (D9h) (F2h) VMGexit (D9h)	SMSW Rv	(F3h) INCSSP (E9h) (F3h) SAVESSP (EAh) RDPKRU (EEh) WRPKRU (EFh) (see CPUID)	LMSW Rv	SWAPGS (F8h) RDTSCP (F9h) MONITORX (FAh) MWAITX (FBh) CLZERO (FCh) (see CPUID)
group #8 (0Fh,BAh) (80386+)					BT Ev,Ib	BTS Ev,Ib	BTR Ev,Ib	BTC Ev,Ib
group #9 (0Fh,C7h)		CMPXCHG8B Mq CMPXCHG16B Mo (see CPUID)	XRSTORS M (see CPUID)	XSAVEC M (see CPUID)	XSAVES M (see CPUID)		(n/a) VMPTRLD Mq (66h) VMCLEAR Mq (F3h) VMXON Mq (see CPUID)	(n/a) VMPTRST Mq (see CPUID)
							(n/a) RDRAND Rv (66h) RDRAND Rv (see CPUID)	(n/a) RDSEED Rv (66h) RDSEED Rv (F3h) RDPID Ry (see CPUID)
group #10 (0Fh,B9h)	UD2	UD2	UD2	UD2	UD2	UD2	UD2	UD2
group #11 (C6h)								XABORT Ib (F8h)

	MOV Eb,Ib							(see CPUID)
group #11 (C7h)	MOV Ev,Iz							XBEGIN Jz (F8h) (see CPUID)
group #12 (0Fh,71h)			PSRLW Nq,Ib (MMX) (66h) **!V**PSRLW **Hx,**Ux,Ib (SSE2)		PSRAW Nq,Ib (MMX) (66h) **!V**PSRAW **Hx,**Ux,Ib (SSE2)		PSLLW Nq,Ib (MMX) (66h) **!V**PSLLW **Hx,**Ux,Ib (SSE2)	
group #12 (EVEX 66h) (0Fh,71h)			VPSRLW Hn {K} {z}, Wn,Ib (W=x) (AVX512BW, VL)		VPSRAW Hn {K} {z}, Wn,Ib (W=x) (AVX512BW, VL)		VPSLLW Hn {K} {z}, Wn,Ib (W=x) (AVX512BW, VL)	
group #13 (0Fh,72h)			PSRLD Nq,Ib (MMX) (66h) **!V**PSRLD **Hx,**Ux,Ib (SSE2)		PSRAD Nq,Ib (MMX) (66h) **!V**PSRAD **Hx,**Ux,Ib (SSE2)		PSLLD Nq,Ib (MMX) (66h) **!V**PSLLD **Hx,**Ux,Ib (SSE2)	
group #13 (MVEX 66h) (0Fh,72h)			VPSRLD Hz {Kw}, Si32r (Wzt),Ib (W=0) (K1OM)		VPSRAD Hz {Kw}, Si32r (Wzt),Ib (W=0) (K1OM)		VPSLLD Hz {Kw}, Si32r (Wzt),Ib (W=0) (K1OM)	
group #13 (EVEX 66h) (0Fh,72h)	VPRORD Hn {K} {z}, B32 (Wn),Ib (W=0) VPRORQ Hn {K} {z}, B64 (Wn),Ib (W=1) (AVX512F,VL)	VPROLD Hn {K} {z}, B32 (Wn),Ib (W=0) VPROLQ Hn {K} {z}, B64 (Wn),Ib (W=1) (AVX512F,VL)	VPSRLD Hn {K} {z}, B32 (Wn),Ib (W=0) (AVX512F,VL)		VPSRAD Hn {K} {z}, B32 (Wn),Ib (W=0) VPSRAQ Hn {K} {z}, B64 (Wn),Ib (W=1) (AVX512F,VL)		VPSLLD Hn {K} {z}, B32 (Wn),Ib (W=0) (AVX512F,VL)	
group #14 (0Fh,73h)			PSRLQ Nq,Ib (MMX) (66h) **!V**PSRLQ **Hx,**Ux,Ib (SSE2)	(66h) **!V**PSRLDQ **Hx,**Ux,Ib (SSE2)			PSLLQ Nq,Ib (MMX) (66h) **!V**PSLLQ **Hx,**Ux,Ib (SSE2)	(66h) **!V**PSLLDQ **Hx,**Ux,Ib (SSE2)
group #14 (EVEX 66h) (0Fh,73h)			VPSRLQ Hn {K} {z}, B64 (Wn),Ib (W=1) (AVX512F,VL)	VPSRLDQ Hn, Wn,Ib (W=x) (AVX512BW, VL)			VPSLLQ Hn {K} {z}, B64 (Wn),Ib (W=1) (AVX512F,VL)	VPSLLDQ Hn, Wn,Ib (W=x) (AVX512BW, VL)
group #15 (0Fh,AEh)	FXSAVE M (see CPUID)	FXRSTOR M (see CPUID)	**V**LDMXCSR Md (SSE)	**V**STMXCSR Md (SSE)	XSAVE M (n/a) (see CPUID)	XRSTOR M (n/a) (see CPUID)	XSAVEOPT M (n/a) (see CPUID)	CLFLUSH M (n/a) (see CPUID)

					PTWRITE My (F3h) (see CPUID)	SETSSBSY Mq (F3h) (see CPUID)	CLWB M (66h) (see CPUID) CLRSSBSY Mq (F3h) (see CPUID)	CLFLUSHOPT M (66h) (see CPUID)
	RDFSBASE Ry (F3h) (see CPUID)	RDGSBASE Ry (F3h) (see CPUID)	WRFSBASE Ry (F3h) (see CPUID)	WRGSBASE Ry (F3h) (see CPUID)	PTWRITE Ry (F3h) (see CPUID)	LFENCE#r/m (n/a) (SSE2-MEM)	MFENCE#r/m (n/a) (SSE2-MEM)	SFENCE#r/m (n/a) (SSE-MEM) PCOMMIT (F8h) (66h) (see CPUID)
group #15 (VEX F2h) (0Fh,AEh)							SPFLT Ry (K1OM)	CLEVICT0 M (K1OM)
group #15 (MVEX F2h) (0Fh,AEh)								CLEVICT0 M (K1OM)
group #15 (VEX F3h) (0Fh,AEh)							DELAY Ry (K1OM)	CLEVICT1 M (K1OM)
group #15 (MVEX F3h) (0Fh,AEh)								CLEVICT1 M (K1OM)
group #16 (0Fh,18h)	PREFETCHNTA M (SSE-MEM) HINT_NOP Rv (P6+)	PREFETCHT0 M (SSE-MEM) HINT_NOP Rv (P6+)	PREFETCHT1 M (SSE-MEM) HINT_NOP Rv (P6+)	PREFETCHT2 M (SSE-MEM) HINT_NOP Rv (P6+)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)
group #16 (VEX) (0Fh,18h)	VPREFETCHNTA M (K1OM)	VPREFETCHT0 M (K1OM)	VPREFETCHT1 M (K1OM)	VPREFETCHT2 M (K1OM)	VPREFETCHENTA M (K1OM)	VPREFETCHET0 M (K1OM)	VPREFETCHET1 M (K1OM)	VPREFETCHET2 M (K1OM)
group #16 (MVEX) (0Fh,18h)	VPREFETCHNTA M (K1OM)	VPREFETCHT0 M (K1OM)	VPREFETCHT1 M (K1OM)	VPREFETCHT2 M (K1OM)	VPREFETCHENTA M (K1OM)	VPREFETCHET0 M (K1OM)	VPREFETCHET1 M (K1OM)	VPREFETCHET2 M (K1OM)

group #16 (0Fh,19h)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)
group #16 (0Fh,1Ah)	(n/a) BNDLDX rB,M.ib (MPX) (66h) BNDMOV rB,mB/M (MPX) (F3h) BNDCL rB,E*y* (MPX) (F2h) BNDCU rB,E*y* (MPX)							
group #16 (0Fh,1Bh)	(n/a) BNDSTX M.ib,rB (MPX) (66h) BNDMOV mB/M,rB (MPX) (F3h) BNDMK rB,M*y* (MPX) (F2h) BNDCN rB,E*y* (MPX)							
group #16 (0Fh,1Ch)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)
group #16 (0Fh,1Dh)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)
group #16 (0Fh,1Eh)	HINT_NOP E[bv] (P6+)	(F3h) RDSSPD Rd (F3h) RDSSPQ Rq (see CPUID)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)	(F3h) ENDBR64 (FAh) (F3h) ENDBR32 (FBh) (see CPUID)
group #16 (0Fh,1Fh)	NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)	HINT_NOP E[bv] (P6+)
group #17 (VEX) (0F 38 F3)		BLSR^{v} B*y*,E*y* (BMI)	BLSMSK^{v} B*y*,E*y* (BMI)	BLSI^{v} B*y*,E*y* (BMI)				
group #18 (MVEX 66) (0F 38 C6)	VGATHER-PF0HINTDPS Uf32 (Mdt.z) {Kw.!0} (W=0) VGATHER-PF0HINTDPD Uf64 (Mqt.z.y) {Kw.!0} (1) (K1OM,VSIB)	VGATHER-PF0DPS Uf32 (Mdt.z) {Kw.!0} (W=0) (K1OM,VSIB)	VGATHER-PF1DPS Uf32 (Mdt.z) {Kw.!0} (W=0) (K1OM,VSIB)		VSCATTER-PF0HINTDPS Uf32 (Mdt.z) {Kw.!0} (W=0) VSCATTER-PF0HINTDPD Uf64 (Mqt.z.y) {Kw.!0} (1) (K1OM,VSIB)	VSCATTER-PF0DPS Uf32 (Mdt.z) {Kw.!0} (W=0) (K1OM,VSIB)	VSCATTER-PF1DPS Uf32 (Mdt.z) {Kw.!0} (W=0) (K1OM,VSIB)	
group #18 (EVEX 66) (0F 38 C6)		VGATHER-PF0DPS Md.n {K.!0} (W=0) VGATHER-PF0DPD Mq.h {K.!0} (W=1) (A`PF,VL,VSIB)	VGATHER-PF1DPS Md.n {K.!0} (W=0) VGATHER-PF1DPD Mq.h {K.!0} (W=1) (A`PF,VL,VSIB)			VSCATTER-PF0DPS Md.n {K.!0} (W=0) VSCATTER-PF0DPD Mq.h {K.!0} (W=1) (A`PF,VL,VSIB)	VSCATTER-PF1DPS Md.n {K.!0} (W=0) VSCATTER-PF1DPD Mq.h {K.!0} (W=1) (A`PF,VL,VSIB)	
group #18 (EVEX 66) (0F 38 C7)		VGATHER-PF0QPS Md.n {K.!0} (W=0) VGATHER-	VGATHER-PF1QPS Md.n {K.!0} (W=0) VGATHER-			VSCATTER-PF0QPS Md.n {K.!0} (W=0) VSCATTER-	VSCATTER-PF1QPS Md.n {K.!0} (W=0) VSCATTER-	

		PF0QPD Mq.n {K.!0} (W=1) (A`PF,VL,VSIB)	PF1QPD Mq.n {K.!0} (W=1) (A`PF,VL,VSIB)			PF0QPD Mq.n {K.!0} (W=1) (A`PF,VL,VSIB)	PF1QPD Mq.n {K.!0} (W=1) (A`PF,VL,VSIB)	