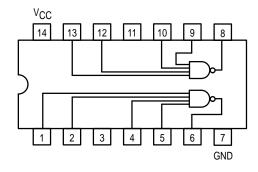


# **DUAL 4-INPUT NAND BUFFER**

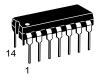
## SN54/74LS40



DUAL 4-INPUT NAND BUFFER LOW POWER SCHOTTKY



J SUFFIX CERAMIC CASE 632-08



N SUFFIX PLASTIC CASE 646-06



D SUFFIX SOIC CASE 751A-02

#### **ORDERING INFORMATION**

SN54LSXXJ SN74LSXXN SN74LSXXD Ceramic Plastic SOIC

#### **GUARANTEED OPERATING RANGES**

Symbol	Parameter		Min	Тур	Max	Unit
VCC	Supply Voltage	54 74	4.5 4.75	5.0 5.0	5.5 5.25	V
T <sub>A</sub>	Operating Ambient Temperature Range	54 74	-55 0	25 25	125 70	°C
Іон	Output Current — High	54, 74			-1.2	mA
lOL	Output Current — Low	54 74			12 24	mA

### SN54/74LS40

#### DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

			Limits					
Symbol	Parameter		Min	Тур	Max	Unit	Test Conditions	
VIH	Input HIGH Voltage		2.0			V	Guaranteed Input HIGH Voltage for All Inputs	
V <sub>IL</sub>	Input LOW Voltage	54			0.7	٧	Guaranteed Input LOW Voltage for All Inputs	
		74			0.8			
٧ <sub>IK</sub>	Input Clamp Diode Voltage			-0.65	-1.5	V	V <sub>CC</sub> = MIN, I <sub>IN</sub> = -18 mA	
V <sub>OH</sub>	Output HIGH Voltage	54	2.5	3.5		V	$V_{CC}$ = MIN, $I_{OH}$ = MAX, $V_{IN}$ = $V_{IH}$ or $V_{IL}$ per Truth Table	
		74	2.7	3.5		V		
Voi	Output LOW Voltage	54, 74		0.25	0.4	V	I <sub>OL</sub> = 12 mA	V <sub>CC</sub> = V <sub>CC</sub> MIN, V <sub>IN</sub> = V <sub>IL</sub> or V <sub>IH</sub>
VOL		74		0.35	0.5	V	I <sub>OL</sub> = 24 mA	per Truth Table
1	Input HIGH Current				20	μΑ	$V_{CC} = MAX$ , $V_{IN} = 2.7 V$	
l IH					0.1	mA	$V_{CC} = MAX$ , $V_{IN} = 7.0 V$	
Ι <sub>ΙL</sub>	Input LOW Current				-0.4	mA	$V_{CC} = MAX$ , $V_{IN} = 0.4 V$	
los	Short Circuit Current (Note 1)		-30		-130	mA	V <sub>CC</sub> = MAX	
lcc	Power Supply Current Total, Output HIGH Total, Output LOW				1.0	mA	V <sub>CC</sub> = MAX	
					6.0			

Note 1: Not more than one output should be shorted at a time, nor for more than 1 second.

### AC CHARACTERISTICS $(T_A = 25^{\circ}C)$

		Limits		Limits		
Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions
tPLH	Turn-Off Delay, Input to Output		12	24	ns	$V_{CC}$ = 5.0 V, $R_L$ = 667 $\Omega$
tPHL	Turn-On Delay, Input to Output		12	24	ns	$C_L = 45 pF$