

DM74LS564 Octal D-Type Flip-Flop with TRI-STATE® Outputs

General Description

The 'LS564 is a high speed low power octal flip-flop with a buffered common Clock (CP) and a buffered common Output Enable $(\overline{\text{OE}})$. The information presented to the D inputs is stored in the flip-flops on the LOW-to-HIGH Clock (CP) transition

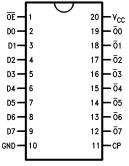
This device is functionally identical to the 'LS574, but has inverted outputs. For complete discussions of operations, truth tables, AC and DC electrical specifications, refer to the 'LS374 data sheet.

Features

- Inputs and outputs on opposite sides of package allowing easy interface with microprocessors
- Useful as input or output port for microprocessors
- Functionally identical to 'LS574
- Input clamp diodes limit high speed termination effects
- Fully TTL and CMOS compatible

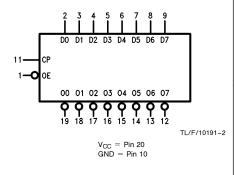
Connection Diagram

Dual-In-Line Package



TL/F/10191-

Logic Symbol



Order Number DM74LS564WM or DM74LS564N See NS Package Number M20B or N20A

Pin Names	Description
D0-D7	Data Inputs
CP	Clock Pulse Input (Active Rising Edge)
ŌĒ	TRI-STATE® Output Enable Input
	(Active LOW)
0 0− 0 7	TRI-STATE Outputs

TRI-STATE® is a registered trademark of National Semiconductor Corporation

Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage 7V
Input Voltage 7V
Operating Free Air Temperature Range
DM74LS 0°C to +70°C

Storage Temperature Range -65° C to $+150^{\circ}$ C

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	DM74LS564			Units
Oyill Doi	Tarameter	Min	Nom	Max	Onits
V_{CC}	Supply Voltage	4.75	5	5.25	V
V_{IH}	High Level Input Voltage	2			V
V _{IL}	Low Level Input Voltage			0.8	V
Гон	High Level Output Current			-2.6	mA
loL	Low Level Output Current			24	mA
TA	Free Air Operating Temperature	0		70	°C

Electrical Characteristics over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 1)	Max	Units
VI	Input Clamp Voltage	$V_{CC} = Min, I_{I} = -18 \text{ mA}$			-1.5	V
V _{OH}	High Level Output Voltage	$V_{CC} = Min, I_{OH} = Max,$ $V_{IL} = Max$	2.4	3.4		V
V _{OL}	Low Level Output Voltage	$V_{CC} = Min, I_{OL} = Max,$ $V_{IH} = Min$		0.35	0.5	V
		$I_{OL} = 12 \text{ mA}, V_{CC} = \text{Min}$		0.25	0.4	
I _I	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 7V$			0.1	mA
I _{IH}	High Level Input Current	$V_{CC} = Max, V_I = 2.7V$			20	μΑ
I _{IL}	Low Level Input Current	$V_{CC} = Max, V_I = 0.4V$			-0.4	mA
los	Short Circuit Output Current	V _{CC} = Max (Note 2)	-30		-130	mA
I _{CC}	Supply Current	V _{CC} = Max			60	mA
Гохн	TRI-STATE Output OFF Current HIGH	$V_{CC} = Max, V_O = 2.4V$			20	μΑ
lozL	TRI-STATE Output OFF Current LOW	$V_{CC} = Max, V_O = 0.4V$			-20	μΑ

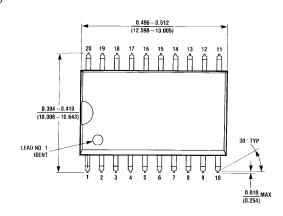
Note 1: All typicals are at $V_{CC} = 5V$, $T_A = 25^{\circ}C$.

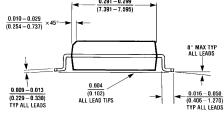
Note 2: Not more than one output should be shorted at a time, and the duration should not exceed one second.

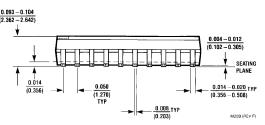
Switching Characteristics $V_{CC} = +5.0V$, $T_A = +25^{\circ}C$

Symbol	Parameter	Min	Max	Units
t _{PLH} t _{PHL}	Propagation Delay CP to On		28 28	ns
[†] PZH [†] PZL	Enable Time OE to On		28 28	ns
^t PHZ ^t PLZ	Enable Time OE to On		20 25	ns
t _s	Setup Time Dn to CP	20		ns
t _h	Hold Time Dn to CP	5		ns
t _w (H) t _w (L)	Pulse Width (HIGH/LOW) CP	20 15		ns

Physical Dimensions inches (millimeters)







20-Lead Wide Small Outline Molded Package (M) Order Number DM74LS564WM NS Package Number M20B

Physical Dimensions inches (millimeters) (Continued) 1.013-1.040 (25.73-26.42) 0.092×0.030 (2.337 × 0.762) MAX DP 0.032 ± 0.005 20 19 18 17 16 15 14 13 12 11 20 19 (0.813±0.127) RAD 0.260 ±0.005 PIN NO. 1 IDENT PIN NO. 1 IDENT (6.604 ±0.127) 0.280 (7.112) 1 2 3 4 5 6 7 8 9 10 0.090 OPTION 2 0.300-0.320 (2.286) (7.620-8.128) 0.060 NOM 0.040 OPTION 2 0.130 0.005 (1.016) 4° (4X) (1.524) 0.065 (3.302 0.127) TYP (1.651) 0.145-0.200 (3.683-5.080) 0.009-0.015 90°± 0.004° (0.229-0.381) 0.020 0.100±0.010 (0.508) MIN 0.125-0.140 0.060 +0.005 (2.540 ± 0.254) 0.018 ± 0.003 $\overline{(3.175 - 3.556)}$ 0.325 +0.040 -0.015 (1.524 ± 0.127) (0.457 ± 0.076) (8.255 +1.016) N20A (REV G)

20-Lead Molded Dual-In-Line Package (N) Order Number DM74LS564N **NS Package Number N20A**

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- 2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.



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