

DM74LS05

Hex Inverters with Open-Collector Outputs

General Description

This device contains six independent gates each of which performs the logic INVERT function. The open-collector outputs require external pull-up resistors for proper logical operation.

Features

 Alternate Military/Aerospace device (54LS05) is available. Contact a Fairchild Semiconductor Sales Office/Distributor for specifications.

Pull-Up Resistor Equations

$$\mathsf{R}_{\mathsf{MAX}} = \frac{\mathsf{V}_{\mathsf{CC}} \left(\mathsf{Min}\right) \, - \, \mathsf{V}_{\mathsf{OH}}}{\mathsf{N}_{\mathsf{1}} \left(\mathsf{I}_{\mathsf{OH}}\right) \, + \, \mathsf{N}_{\mathsf{2}} \left(\mathsf{I}_{\mathsf{IH}}\right)}$$

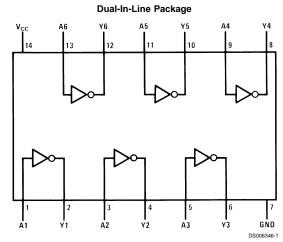
$$\mathsf{R}_{MIN} = \frac{\mathsf{V}_{CC} \left(\mathsf{Max}\right) - \mathsf{V}_{OL}}{\mathsf{I}_{OL} - \mathsf{N}_{3} \left(\mathsf{I}_{IL}\right)}$$

Where: N_1 (I_{OH}) = total maximum output high current for all outputs tied to pull-up resistor

 ${\rm N_2~(I_{IH})}$ = total maximum input high current for all inputs tied to pull-up resistor

 N_3 (I_{IL}) = total maximum input low current for all inputs tied to pull-up resistor

Connection Diagram



Order Number 54LS05DMQB, 54LS05FMQB, DM54LS05J, DM54LS05W, DM74LS05M or DM74LS05N See Package Number E20A, J14A, M14A, N14A or W14B

Function Table

$$Y = \overline{A}$$

Input	Output
Α	Y
L	Н
н	L

H = High Logic Level L = Low Logic Level **Absolute Maximum Ratings** (Note 1)

Supply Voltage 7V
Input Voltage 7V
Output Voltage 7V

Operating Free Air Temperature Range

Recommended Operating Conditions

Symbol	Parameter	DM54LS05			DM74LS05			Units
		Min	Nom	Max	Min	Nom	Max	
V _{cc}	Supply Voltage	4.5	5	5.5	4.75	5	5.25	V
V _{IH}	High Level Input Voltage	2			2			V
V _{IL}	Low Level Input Voltage			0.7			0.8	V
V _{OH}	High Level Output Voltage			5.5			5.5	V
I _{OL}	Low Level Output Current			4			8	mA
T _A	Free Air Operating Temperature	-55		125	0		70	°C

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Electrical Characteristics

over recommended operating free air temperature range (unless otherwise noted)

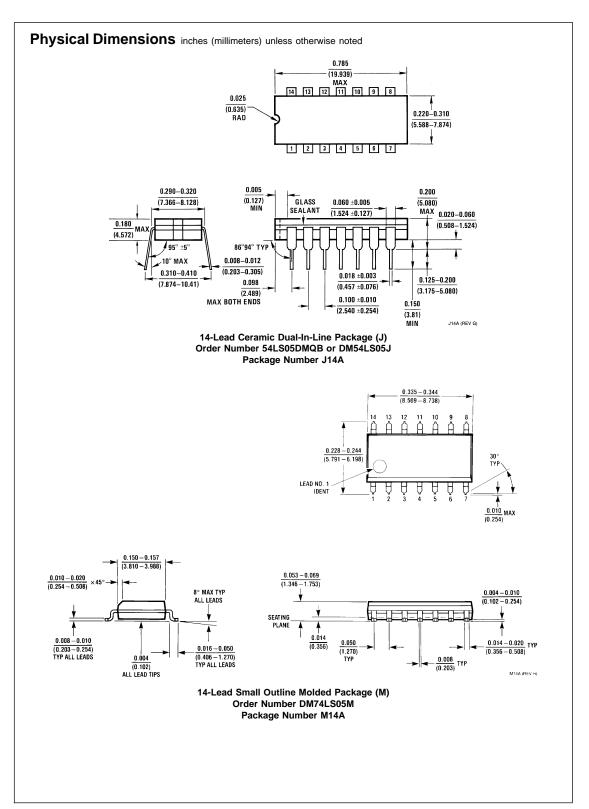
Symbol	Parameter	Conditions		Min	Тур	Max	Units
					(Note 2)		
V _I	Input Clamp Voltage	$V_{\rm CC}$ = Min, $I_{\rm I}$ = -18 mA				-1.5	V
I _{CEX}	High Level Output	V_{CC} = Min, V_{O} = 5.5V				100	μA
	Current	V _{IL} = Max					
V _{OL}	Low Level Output	V _{CC} = Min, I _{OL} = Max	DM54		0.25	0.4	
	Voltage	V _{IH} = Min	DM74		0.35	0.5	V
		I _{OL} = 4 mA, V _{CC} = Min	DM74		0.25	0.4	
I _I	Input Current @	V _{CC} = Max, V _I = 7V				0.1	mA
	Max Input Voltage						
I _{IH}	High Level Input Current	$V_{CC} = Max, V_I = 2.7V$				20	μA
I _{IL}	Low Level Input Current	$V_{CC} = Max, V_I = 0.4V$				-0.36	mA
I _{CCH}	Supply Current with	V _{CC} = Max			1.2	2.4	mA
	Outputs High						
I _{CCL}	Supply Current with	V _{CC} = Max			3.6	6.6	mA
	Outputs Low						

Switching Characteristics

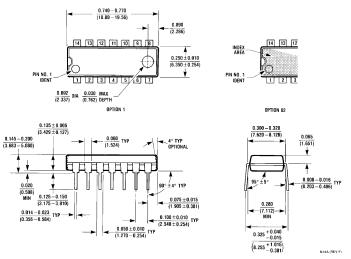
at V_{CC} = 5V and T_A = 25°C (See Section 1 for Test Waveforms and Output Load)

Symbol	Parameter		$R_L = 2 k\Omega$				
		C _L =	= 15 pF	C _L =	Units		
		Min	Max	Min	Max	1	
t _{PLH}	Propagation Delay Time	6	20	20	45	ns	
	Low to High Level Output						
t _{PHL}	Propagation Delay Time	3	15	4	20	ns	
	High to Low Level Output						

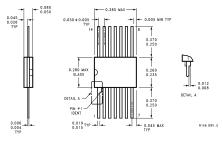
Note 2: All typicals are at V_{CC} = 5V, T_A = 25°C.



Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



14-Lead Molded Dual-In-Line Package (N) Order Number DM74LS05N Package Number N14A



14-Lead Ceramic Flat Package (W) Order Number 54LS05FMQB or DM54LS05W Package Number W14B

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