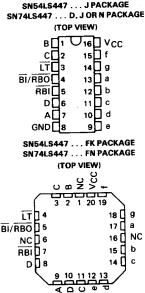
#### **TYPES SN54LS447, SN74LS447 BCD-TO-SEVEN-DECODERS/DRIVERS**

D2428, NOVEMBER 1977-REVISED DECEMBER 1983

- Low-Voltage Version of SN54LS247/SN74LS247
- Open-Collector Outputs **Drive Indicators Directly**
- Lamp-Test Provision
- Leading/Trailing Zero Suppression
- Lamp Intensity **Modulation Capability**



BI/RBO 5

- No internal connection

#### logic symbol BIN/7-SEG D (T2) ΪŦ BI/RBO (5) RBI-(13)a 20,21 ♦ (7) (12)ь 20,21 Ф (11)(1) c 20,21 Q (10) d 20,21 Q (2) e 20.21 Q (15) (6) f 20,21 Q (14)g 20,21 Q

Pin numbers shown on logic notation are for D, J or N packages.

#### **FUNCTION TABLE**

FORCTION TABLE																
DECIMAL	INPUTS						BI/RBO1	OUTPUTS								
OR FUNCTION	ίŦ	RBI	D	С	В	A		•	q	c	d	٠	•	9		
0	н	н	L	L	L	L	н	ON	DN	ON	ON	ON	ON	OFF		
	н	l x l	L	L	L	н	н	OFF	ON	ON	OFF	OFF	OFF	OFF		
2	Н	×	L	L	н	L	н	ON	ON	OFF	ON	ON	OFF	ON		
3	;;	x	Ł	L	н	н	н	ON	ON	ON	ON	OFF	OFF	ON		
4	H	×		Н.	L	L	н	OFF	ON	ON	OFF	OFF	ON	ON		
5	, ii	x	i.	н	ī	н	н	ON	OFF	ON	ON	OFF	ON	ON		
	1			н	н	L	l <sub>H</sub>	ON	OFF	ON	ON	ON	ON	ON		
6	Н	X	L .	н	н	н	"	ON	ON	ON	OFF	OFF	OFF	OFF	١.	
7	Н_	X	L		<del>-</del> :-		н	ON	ON	ON	ON	ON	ON	ON	,	
8	Н	×	н	L			''	ON	ON	QN	ON	OFF	ON	ON		
9	н	×	н	t	L	н		OFF	OFF	OFF	ON	ON	OFF	l on		
10	H	×	н	L	н	L	Н	1	OFF	ON	ON	OFF	OFF	ON	1	
11	H	х	н	L	н	н	н	OFF	+	OFF	OFF	OFF	ON	ON	1	
12	Н	×	Н	н	L	L	H H	OFF	ON		1		ON	ON	1	
13	н	×	н	H	Ł	Н	Н	ON	OFF	OFF	ON	OFF		1 -		
14	н	×	₩	н	н	L	н	OFF	OFF	OFF	ON	ON	ON	ON		
15	н	×	н	н	н	н	н	OFF	OFF	OFF	OFF	OFF	OFF	OFF	<del>  _</del>	
BI		×	×	×	×	×	L	OFF	OFF	OFF	OFF	OFF	OFF		2	
RBI	н	Li	١٠	L.	ι	L	L L	OFF	OFF	OFF	OFF	OFF	OFF	OFF	3	
<u>LT</u>	1	×	×	×	х	×	н	ON	ON	ON	ON	ON	ON	ON	4	

H = high level, L = low level, X = irrelevant

- NOTES: 1. The blanking input (BI) must be open or held at a high logic level when output functions 0 through 15 are desired. The ripple-blanking input (RBI) must be open or high if blanking of a decimal zero is not desired.
  - 2. When a low logic level is applied directly to the blanking input (BI), all segment outputs are off regardless of the level of any other input.
  - 3. When ripple-blanking input (RBI) and inputs A, B, C, and D are at a low level with the lamp test input high, all segment outputs go off and the ripple-blanking output (RBO) goes to a low level (response condition).
  - 4. When the blanking input/ripple blanking output (BI/RBO) is open or held high and a low is applied to the lamp-test input, all segment outputs are on.

†BI/RBO is wire-AND logic serving as blanking input (BI) and/or ripple-blanking output (RBO).



,		DRIVER OU	TYPICAL			
TYPE	ACTIVE LEVEL	OUTPUT CONFIGURATION	SINK CURRENT	MAX VOLTAGE	POWER DISSIPATION	PACKAGES
SN54LS447	low	open-collector	12 mA	7 V	35 mW	J
SN74LS447	low	open-collector	24 mA	7 V	35 mW	J, N



IDENTIFICATION

#### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, VCC (see Note 1)		 									7V	
Input voltage		 									7·V	-
Peak output current (t <sub>w</sub> ≤ 1 ms, duty cycle ≤ 10%												
Current forced into any output in the off state		 									1 mA	L
Operating free-air temperature range: SN54LS447		 								-55	°C to 125°C	;
SN74LS447								Ċ			0°C to 70°C	;
Storage temperature range										-65	°C to 150°C	;

NOTE 1: Voltage values are with respect to network ground terminal.

#### recommended operating conditions

		SI	154LS4	47	S	SN74LS447			
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT	
Supply voltage, VCC		4.5	5	5.5	4.75	5	5.25	V	
Off-state output voltage, VO(off)	a thru g			7			7	٧	
On-state output current, IO(on)	a thru g			12			24	mA	
High-level output current, IOH	BI/RBO			-50			50	μΑ	
Low-level output current, IOL	BI/R80			1.6			3.2	mA	
Operating free-sir temperature, TA		-55		125	0		70	°C	

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TTL DEVICES

# TYPES SN54LS447, SN74LS447 BCD-TO-SEVEN-SEGMENT DECODERS/DRIVERS

## electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

ectrical characteristics over recommend					SN54LS447			SI	UNIT		
	PARAMETER		TEST CONDITIONS†			TYP	MAX	MIN	TYP <sup>‡</sup>	MAX	
	High-level input voltage				2			2			٧
<u> </u>							0.7	Ī		0.8	>
/IL	Low-level input voltage		VCC = MIN,	I <sub>I</sub> = -18 mA			-1.5			-1.5	V
7IK	Input clamp voltage							<del></del>			v
Vон	High-level output voltage	BI/RBO	V <sub>CC</sub> = MIN, V <sub>IL</sub> = V <sub>IL</sub> max,	****	2.4	4.2		2.4	4.2		L v
			V <sub>CC</sub> = MIN,	I <sub>OL</sub> = 1.6 mA		0.25	0.4		0.25	0.4	l v
VOL	Low-level output voltage	BI/RBO	V <sub>IH</sub> = 2 V, V <sub>IL</sub> = V <sub>IL</sub> max	I <sub>OL</sub> = 3.2 mA					0.35	0.5	
101.00	Off-state output current	a thru g	V <sub>CC</sub> = MAX, V <sub>IL</sub> = V <sub>IL</sub> max,	V <sub>IH</sub> = 2 V,			250			250	μΔ
O(off)			VCC = MAX,	10(on) = 12 mA	<del> </del>	0.25	0.4		0.25	0.4	
VO(on)	On-state output voltage	a thru g	V <sub>IH</sub> = 2 V, V <sub>IL</sub> ≈ V <sub>IL</sub> max	10/> = 24 mA					0.35	0.5	
			VCC = MAX,	V <sub>1</sub> = 7 V	+		0.1			0.1	m/
կ	Input current at maximus	m input voltage		V <sub>1</sub> = 2.7 V.	+-		20	+		20	μA
I <sub>I</sub> H	High-level input current		V <sub>CC</sub> = MAX,	VI = 2.7 V.				+			+-
		Any input except BI/RBO	Vcc = MAX.	V <sub>1.</sub> = 0.4 V			-0.4			-0.4 	] m
IL	Low-level input current	BI/RBO		*			-1.2			-1.2	┼-
los	Short-circuit	BI/RBO	V <sub>CC</sub> = MAX		-0.3	;	-2	-0.3	·	-2	m.
Icc	output current Supply current		VCC = MAX,	See Note 2		7	13		7	13	3 m

<sup>†</sup>For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

‡All typical values are at  $V_{CC}$  = 5 V,  $T_A$  = 25° C. NOTE 2:  $I_{CC}$  is measured with all outputs open and all inputs at 4.5 V.

### switching characteristics, VCC = 5 V, $T_A$ = 25° C

switching characteristics, VCC = 5 V, rA - 25 C		MIN	TYP	MAX	UNIT
PARAMETER	TEST CONDITIONS	MILIA	- 111		-
toff Turn-off time from A input	C <sub>L</sub> = 15 pF, R <sub>L</sub> = 665 Ω,			100	ns
ton Turn-on time from A input		├		100	
toff Turn-off time from RBI input	See Note 4	<u></u>		100	ns
ton Turn-on time from RBI input		L			
Carried for land directits and voltage wavefor	ms.				

NOTE 4: See General Information Section for load circuits and voltage waveforms.