

March 1989 Revised March 2000

DM74LS503

8-Bit Successive Approximation Register (with Expansion Control)

General Description

The DM74LS503 register has an active LOW Enable (\overline{E}) input that is used in cascading two or more packages for longer word lengths. A HIGH signal on \overline{E} , after a START operation, forces Q7 HIGH and prevents the device from accepting serial data. With the \overline{E} input of an DM74LS503 connected to the \overline{CC} output of a preceding (more significant) device, the DM74LS503 will be inhibited until the preceding device is filled, causing its \overline{CC} output to go LOW. This LOW signal then enables the DM74LS503 to accept the serial data on subsequent clocks.

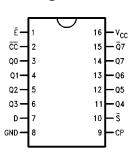
Features

- Performs serial-to-parallel conversion
- Expansion control for longer words
- Storage and control for successive approximation A to D conversion
- Low power Schottky version of 2503

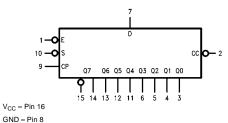
Ordering Code:

Order Number	Package Number	Package Description
DM74LS503N	N16E	16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide

Connection Diagram

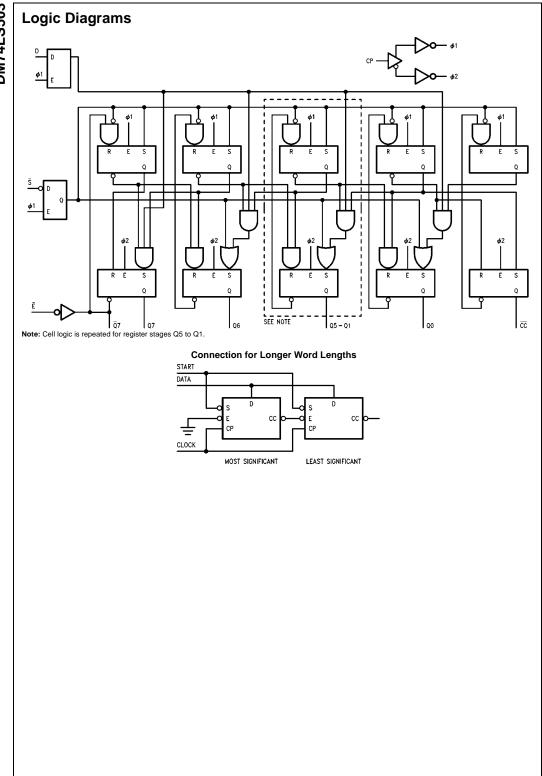


Logic Symbol



Pin Descriptions

Pin Names	Description		
D	Serial Data Input		
S	Start Input (Active LOW)		
CP	Clock Pulse Input (Active Rising Edge)		
Ē	Conversion Enable Input (Active LOW)		
CC	Conversion Complete Output (Active LOW)		
Q0-Q7	Parallel Register Outputs		
Q7	Complement of Q7 Output		



Absolute Maximum Ratings(Note 1)

Supply Voltage 7V Input Voltage 7V Operating Free Air Temperature Range 0° C to +70 $^{\circ}$ C Storage Temperature Range -65° C to +150 $^{\circ}$ C

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	Min	Nom	Max	Units	
V _{CC}	Supply Voltage	4.75	5	5.25	V	
V _{IH}	HIGH Level Input Voltage	2			V	
V _{IL}	LOW Level Input Voltage			0.8	V	
I _{OH}	HIGH Level Output Voltage			-0.4	mA	
I _{OL}	LOW Level Output Current			8	mA	
T _A	Free Air Operating Temperature	0		70	°C	
t _S (H)	Setup Time HIGH or LOW	16			ns	
t _S (L)	S to CP	16			115	
t _H (H)	Hold Time HIGH or LOW	0			20	
t _H (L)	S to CP	0			ns	
t _S (H)	Setup Time HIGH or LOW	8				
t _S (L)	D to CP	8			ns	
t _H (H)	Hold Time HIGH or LOW	10			20	
t _H (L)	D to CP	10			ns	
t _W (H)	CP Pulse Width HIGH or LOW	46			ns	
t _W (L)		46				

Electrical Characteristics

Over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 2)	Max	Units
VI	Input Clamp Voltage	V _{CC} = Min, I _I = -18 mA			-1.5	V
V _{OH}	HIGH Level Output Voltage	$V_{CC} = Min, I_{OH} = Max,$ $V_{IL} = Max$	2.7			V
V _{OL}	LOW Level Output Voltage	$V_{CC} = Min, I_{OL} = Max, V_{IH} = Min$ $I_{OL} = 4 \text{ mA}, V_{CC} = Min$			0.5 0.4	V
I _I	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 7V$ $V_I = 10V$			0.1	mA
I _{IH}	HIGH Level Input Current	$V_{CC} = Max, V_I = 2.7V$			20	μΑ
I _{IL}	LOW Level Input Current	$V_{CC} = Max, V_I = 0.4V$			-0.8	mA
Ios	Short Circuit Output Current	V _{CC} = Max (Note 3)	-20		-100	mA
I _{CC}	Supply Current	V _{CC} = Max			65	mA

Note 2: All typicals are at $V_{CC} = 5V$, $T_A = 25^{\circ}C$.

Note 3: Not more than one output should be shorted at a time, and the duration should not exceed one second.

Switching Characteristics

 $V_{CC} = +5.0V$, $T_A = +25^{\circ}C$

Symbol	Parameter	$R_L = 2 k\Omega$	$R_L = 2 k\Omega, C_L = 15 pF$			
Symbol		Min	Max	Units		
f_{MAX}	Maximum Count Frequency	15		MHz		
t _{PLH}	Propagation Delay		35	ns		
t _{PHL}	CP to Qn or CC		25			
t _{PLH}	Propagation Delay		20			
t _{PHL}	E to Q7		24	ns		

Physical Dimensions inches (millimeters) unless otherwise noted $\frac{0.740 - 0.780}{(18.80 - 19.81)}$ (2.286) 14 13 12 11 INDEX AREA 0.250 ± 0.010 (6.350 ± 0.254) PIN NO. 1 1 2 3 4 5 6 7 8 **IDENT** OPTION 01 OPTION 02 0.065 $\frac{0.130 \pm 0.005}{(3.302 \pm 0.127)}$ $\frac{0.060}{(1.524)}$ 4° TYP OPTIONAL 0.300 - 0.320 (1.651)(7.620 - 8.128) $\frac{0.145 - 0.200}{(3.683 - 5.080)}$ 95° ± 5° $\frac{0.008 - 0.016}{(0.203 - 0.406)}$ TYP 90° ± 4° TYP 0.020 $\frac{0.280}{(7.112)}$ (0.508)0.125 - 0.150 (3.175 - 3.810) 0.030 ± 0.015 (0.762 ± 0.381) MIN 0.014 - 0.023 0.100 ± 0.010 (0.325 +0.040 (0.356 - 0.584) TYP (2.540 **±** 0.254) TYP 0.050 ± 0.010 N16E (REV F)

16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide Package Number N16E

 (1.270 ± 0.254)

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