





# Getting Saturated with Induction

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**Abstract.** Induction in saturation-based first-order theorem proving is a new exciting direction in the automation of inductive reasoning. In this paper we survey our work on integrating induction directly into the saturation-based proof search framework of first-order theorem proving. We describe our induction inference rules proving properties with inductively defined datatypes and integers. We also present additional reasoning heuristics for strengthening inductive reasoning, as well as for using induction hypotheses and recursive function definitions for guiding induction. We present exhaustive experimental results demonstrating the practical impact of our approach as implemented within Vampire. This is an extended version of a Principles of Systems Design 2022 paper with the same title and the same authors.

**Keywords:** Induction · Formal Verification · Theorem Proving

## 1 Introduction

One commonly used theory in the development of imperative/functional programs is the theory of inductively defined data types, such as natural numbers (e.g. see Figure 1(a)). Automating reasoning in formal verification therefore also needs to automate induction. Previous works on automating induction mainly focus on inductive theorem proving [3,4,5,22,17]: deciding when induction should be applied and what induction axiom should be used. Recent advances related to automating inductive reasoning, such as first-order reasoning with inductively defined data types [14], inductive strengthening [19] and structural induction in superposition [13,6,18,9,8], open up new possibilities for automating induction. In this paper we survey our recent results towards automating inductive reasoning for first-order properties with inductively defined data types and beyond.

**Relation to the state-of-the-art.** Our work automates induction by integrating it directly in the saturation-based approach of first-order provers [15,21,25]. These provers implement saturation-based proof search using the superposition calculus [16]. Moreover, they rely on powerful indexing algorithms, notions of redundancy, selection functions and term orderings for making theorem proving efficient. First-order theorem provers complement SMT solvers in reasoning with

theories and quantifiers, as evidenced in the annual system competitions of SMT solvers [2,24] and first-order provers [23].

Our approach towards automating induction is conceptually different from previous attempts to use induction with superposition [13,6,8], as we are not restricted to specific clause splitting algorithms and heuristics [6], nor are we limited to induction over inductively defined data types using a subterm ordering [8]. As a result, we stay within the standard saturation framework and do not have to introduce constraint clauses, additional predicates, nor change the notion of redundancy as in [8]. In addition, our approach can be used to automate induction over arbitrary, and not just inductively defined, data types, such as integers (Section 9). Our work is also fundamentally different from rewrite-based approaches automating induction [3,4,17,5,19,22], as we do not rely on external algorithms/heuristics to generate subgoals/lemmas of an inductive property. Instead, applications of induction become inference rules of the saturation process, adding instances of appropriate induction schemata. We extend superposition reasoning with new inference rules capturing inductive steps (Sections 5-7), and optimize the saturation theorem proving process with induction. In addition, we instantiate induction axioms with logically stronger versions of the property being proved and use induction hypotheses as specialized rewrite rules (Section 8).

This combination of saturation with induction is very powerful. Our experimental results show that many problems previously unsolved by any system can be solved by our work, some resulting in very complex proofs of program properties and proofs of complex mathematical properties (Section 10). Some of these proofs involve, among tens of thousands of superposition inferences, over 100 applications of induction.

**Contributions.** This paper serves as *a survey* of our recent progress in automating induction using a first-order theorem prover [18,9,12,11].

- We give a small tutorial of induction in saturation, helping non-experts in theorem proving to understand and further use our methodology. To this end, we describe saturation theorem proving and the main concepts of saturation with induction (Sections 4-5).
- We overview technical considerations for turning saturation with induction into an efficient approach (Section 5). We discuss variants of induction inference rules over inductively defined data types (Section 6) and integers (Section 9).
- We present extensions of induction inference rules with multiple premises (Section 7), generalizations and integer reasoning (Section 8).
- We report on exhaustive experiments comparing and analysing our approach to state-of-the-art methods (Section 10).

## 2 Motivating Example

We motivate the challenges of automating induction for formal verification using the functional program of Figure 1(a). This program defines the inductively

<b><u>assume</u></b> $\text{even}(x)$	Axiomatization of <b>add</b> , <b>even</b> and <b>half</b> :
<b>datatype</b> $\text{nat} = 0 \mid \text{s}(x)$	$\forall y \in \text{nat}. (\text{add}(0, y) = y)$
<b>fun</b> $\text{add}(0, y) = y$	$\forall z, y \in \text{nat}. (\text{add}(\text{s}(z), y) = \text{s}(\text{add}(z, y)))$
$\mid \text{add}(\text{s}(z), y) = \text{s}(\text{add}(z, y));$	<b>even</b> (0)
<b>fun</b> $\text{even}(0) = \top$	$\forall z \in \text{nat}. (\text{even}(\text{s}(z)) \leftrightarrow \neg \text{even}(z))$
$\mid \text{even}(\text{s}(z)) = \neg \text{even}(z);$	<b>half</b> (0) = 0
<b>fun</b> $\text{half}(0) = 0$	<b>half</b> ( <b>s</b> (0)) = 0
$\mid \text{half}(\text{s}(0)) = 0$	$\forall z \in \text{nat}. (\text{half}(\text{s}(\text{s}(z))) = \text{s}(\text{half}(z)))$
$\mid \text{half}(\text{s}(\text{s}(z))) = \text{s}(\text{half}(z));$	Verification task (conjecture):
<b><u>assert</u></b> $x = \text{add}(\text{half}(x), \text{half}(x))$	$\forall x \in \text{nat}. (\text{even}(x) \rightarrow x = \text{add}(\text{half}(x), \text{half}(x)))$
(a)	(b)

**Fig. 1.** Motivating example over inductively defined data types.

defined data type **nat** of natural numbers. In first-order logic, this data type corresponds to a term algebra with constructors **0** (zero) and **s** (successor); inductively defined data types, such as **nat**, are special cases of term algebras. The functional program in Figure 1(a) implements **add**, **even** and **half** operations over naturals, by using recursive equations (function definitions) preceded by the **fun** construct. These recursive equations correspond to universally quantified equalities in first-order logic, as listed in the axioms of Figure 1(b).

The expected behaviour of Figure 1(a) is specified using program assertions in first-order logic: the pre-condition using the **assume** construct and the post-condition using **assert**. Figure 1(a) satisfies its requirements. Formally proving correctness of Figure 1(a) essentially requires proving the conjecture of Figure 1(b), establishing that **half**( $x$ ) of an **even** natural number  $x$  added to **half**( $x$ ) equals the original number  $x$ . That is,

$$\forall x \in \text{nat}. (\text{even}(x) \rightarrow x = \text{add}(\text{half}(x), \text{half}(x))). \quad (1)$$

Proving (1), and thus establishing correctness of Figure 1(a), is however challenging as it requires induction over the naturals. As such, finding and using an appropriate induction schemata is needed. The following sound *structural induction schema* for a formula  $F$  could, for example, be used, where  $F$  contains (multiple occurrences of) a natural-valued variable  $x$ :

$$\left( F[0] \wedge \forall z \in \text{nat}. (F[z] \rightarrow F[\text{s}(z)]) \right) \rightarrow \forall x \in \text{nat}. F[x] \quad (2)$$

We instantiate schema (2) by considering  $\forall x \in \mathbf{nat}. F(x)$  to be formula (1), yielding the induction formula:

$$\begin{aligned} & \text{(IB)} \quad (\text{even}(0) \rightarrow 0 = \text{add}(\text{half}(0), \text{half}(0))) \wedge \\ & \text{(IS)} \quad \forall z \in \mathbf{nat}. \left( \begin{array}{l} (\text{even}(z) \rightarrow z = \text{add}(\text{half}(z), \text{half}(z))) \rightarrow \\ (\text{even}(\mathbf{s}(z)) \rightarrow \mathbf{s}(z) = \text{add}(\text{half}(\mathbf{s}(z)), \text{half}(\mathbf{s}(z)))) \end{array} \right) \quad (3) \\ & \rightarrow \forall x \in \mathbf{nat}. \text{even}(x) \rightarrow x = \text{add}(\text{half}(x), \text{half}(x)), \end{aligned}$$

where the subformulas denoted by (IB) and (IS) correspond to the *induction base case* and the *induction step case* of (3). Since schema (2) is sound, its instance (3) is valid. As such, the task of proving (1) is reduced to proving the base case and step case of (3).

Using the definitions of `half` and `add` from Figure 1(b), the base case (IB) simplifies to the tautology  $\top \rightarrow 0 = 0$ . On the other hand, proving (IS) requires additional inductive reasoning. Yet, the induction scheme (2) cannot be used as `even(z)` and `even(s(z))` yield two different base cases. We overcome this limitation by using an additional induction schema with two base cases, as follows:

$$(F[0] \wedge F[\mathbf{s}(0)] \wedge \forall z. (F[z] \rightarrow F[\mathbf{s}(z)])) \rightarrow \forall x. F[x] \quad (4)$$

As before, by instantiating (4) with (1) and simplifying based on the axioms of Figure 1(b), we are left with proving the step case:

$$\begin{aligned} & \text{(IH)} \quad \forall z \in \mathbf{nat}. \left( (\text{even}(z) \rightarrow z = \text{add}(\text{half}(z), \text{half}(z))) \rightarrow \right. \\ & \text{(IC)} \quad \left. (\text{even}(\mathbf{s}(z)) \rightarrow \mathbf{s}(z) = \text{add}(\text{half}(\mathbf{s}(z)), \text{half}(\mathbf{s}(z)))) \right) \quad (5) \end{aligned}$$

The antecedent (IH) and conclusion (IC) of (5) are called the *induction (step) hypothesis* and *induction step conclusion* of the step case, respectively. After rewriting `even(s(z))` to `even(z)` in (IC), both (IH) and (IC) have the same assumption `even(z)`, which can be discarded. By rewriting the remaining conclusions in (IH) and (IC) using the definitions of `half` and `add`, we obtain:

$$\begin{aligned} & \text{(IH)} \quad \forall z \in \mathbf{nat}. (z = \text{add}(\text{half}(z), \text{half}(z)) \rightarrow \\ & \text{(IC)} \quad \mathbf{s}(z) = \text{add}(\text{half}(z), \mathbf{s}(\text{half}(z)))) \quad (6) \end{aligned}$$

We simplify (IC) in (6) by the injectivity of the term algebra constructor `s`:

$$\begin{aligned} & \text{(IH)} \quad \forall z \in \mathbf{nat}. (z = \text{add}(\text{half}(z), \text{half}(z)) \rightarrow \\ & \text{(IC)} \quad \mathbf{s}(z) = \text{add}(\text{half}(z), \mathbf{s}(\text{half}(z)))) \quad (7) \end{aligned}$$

Since the more complex right-hand side of (IH) is not equal to any subterm of (IC) in (7), we have to use (IH) in the left-to-right direction – in order to preserve validity, our only option is to rewrite  $z$  on the left-hand side of (IC):

$$\forall z \in \mathbf{nat}. (\mathbf{s}(\text{add}(\text{half}(z), \text{half}(z))) = \text{add}(\text{half}(z), \mathbf{s}(\text{half}(z)))) \quad (8)$$

Equation (8) is a special case of the formula  $\forall x, y \in \mathbf{nat}. \mathbf{s}(\text{add}(x, y)) = \text{add}(x, \mathbf{s}(y))$  which can be easily verified using the induction schema (2). This establishes the correctness of Figure 1(a).

The verification task of Figure 1(a) highlights the main difficulties in automating inductive reasoning: (i) incorporating induction into saturation (Section 5); (ii) finding suitable induction schemata (Section 6 and Section 9); and (iii) using extensions of induction inference rules to further push the boundaries of automating induction (Sections 7–8). We next present our solutions to these challenges, based on our results from [18,9,12,11].

### 3 Preliminaries

We assume familiarity with *standard multi-sorted first-order logic with equality*. Functions are denoted with  $f, g, h$ , predicates with  $p, q, r$ , variables with  $x, y, z, w$ , and Skolem constants with  $\sigma$ , all possibly with indices. A term is *ground* if it contains no variables. By  $\bar{x}$  and  $\bar{t}$  we denote tuples of variables and terms, respectively. We use the words *sort* and *type* interchangeably. We distinguish special sorts called *term algebra sorts*, function symbols for term algebra sorts called *constructors* and *destructors*. For a term algebra sort  $\tau$ , we denote its constructors with  $\Sigma_\tau$ . For each  $c \in \Sigma_\tau$ , we denote its arity with  $n_c$  and the corresponding destructor returning the value of the  $i$ th argument of  $c$  by  $d_c^i$ . Moreover, we denote with  $P_c$  the set of argument positions of  $c$  of the sort  $\tau$ . We say that  $c$  is a *recursive constructor* if  $P_c$  is non-empty, otherwise it is called a *base constructor*. We call the ground terms built from the constructor symbols of a sort its *term algebra*. We axiomatise term algebras using their *injectivity*, *distinctness*, *exhaustiveness* and *acyclicity* axioms [14]. We refer to term algebras also as algebraic data types or inductively defined data types. Additionally, we assume a distinguished *integer sort*, denoted by  $\mathbb{Z}$ . When we use standard integer predicates  $<, \leq, >, \geq$ , functions  $+, -, \dots$  and constants  $0, 1, \dots$ , we assume that they denote the corresponding interpreted integer predicates and functions with their standard interpretations. All other symbols are uninterpreted.

We use the standard logical connectives  $\neg, \vee, \wedge, \rightarrow$  and  $\leftrightarrow$ , and quantifiers  $\forall$  and  $\exists$ . We write quantifiers like  $\forall x \in \tau$  to denote that  $x$  has the sort  $\tau$  where it is not clear from the context. A *literal* is an atom or its negation. For a literal  $L$ , we write  $\bar{L}$  to denote its complementary literal. A disjunction of literals is a *clause*. We denote clauses by  $C, D$  and reserve the symbol  $\square$  for the *empty clause* which is logically equivalent to  $\perp$ . We denote the *clausal normal form* of a formula  $F$  by  $\text{cnf}(F)$ . We call every term, literal, clause or formula an *expression*. We use the notation  $s \trianglelefteq t$  to denote that  $s$  is a *subterm* of  $t$  and  $s \triangleleft t$  if  $s$  is a *proper subterm* of  $t$ .

We write  $E[s]$  to denote that the expression  $E$  contains  $k$  distinguished occurrence(s) of the term  $s$ , with  $k \geq 0$ . For simplicity,  $E[t]$  means that these occurrences of  $s$  are replaced by the term  $t$ . A *substitution*  $\theta$  is a mapping from variables to terms. A substitution  $\theta$  is a *unifier* of two terms  $s$  and  $t$  if  $s\theta = t\theta$ , and is a *most general unifier (mgu)* if for every unifier  $\eta$  of  $s$  and  $t$ , there exists substitution  $\mu$  s.t.  $\eta = \theta\mu$ . We denote the mgu of  $s$  and  $t$  with  $\text{mgu}(s, t)$ .

**Algorithm 1** The Saturation Loop.

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1  initial set of clauses  $S := A \cup \{\neg B\}$ 
2  repeat
3    Select clause  $G \in S$ 
4    Derive consequences  $C_1, \dots, C_n$  of  $G$  and formulas from  $S$  using rules of  $\mathcal{I}$ 
5     $S := S \cup \{C_1, \dots, C_n\}$ 
6    if  $\square \in S$  then return  $A \rightarrow B$  is UNSAT
8  return  $A \rightarrow B$  is SAT

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## 4 Saturation-Based Theorem Proving

We briefly introduce **saturation-based proof search**, which is the **leading technology for automated first-order theorem proving**. For details, we refer to [15].

First-order theorem provers work with clauses, rather than with arbitrary formulas. Given a set  $S$  of input clauses, first-order provers *saturate*  $S$  by computing all logical consequences of  $S$  with respect to a sound inference system  $\mathcal{I}$ . The saturated set of  $S$  is called the *closure* of  $S$  and the process of computing the closure of  $S$  is called *saturation*. If the closure of  $S$  contains the empty clause  $\square$ , the original set  $S$  of clauses is unsatisfiable. A simplified saturation algorithm for a sound inference system  $\mathcal{I}$  is given in Algorithm 1, with a clausified goal  $B$  ( $\neg B$  is also clausified) and clausified assumptions  $A$  as input.

Note that a saturation algorithm proves validity of  $B$  by establishing unsatisfiability of  $\neg B$  using the assumptions  $A$ ; we refer to this proving process as a *refutation* of  $\neg B$  from  $A$ . Completeness and efficiency of saturation-based reasoning rely heavily on properties of selection and addition of clauses from/to  $S$ , using the inference system  $\mathcal{I}$  (lines 3–5). To organize saturation, first-order provers use simplification *orderings* on terms, which are extended to orderings over literals and clauses; for simplicity, we write  $\succ$  for both the term ordering and its clause/multiset ordering extensions. Given an ordering  $\succ$ , a clause  $C$  is *redundant* with respect to a set  $S$  of clauses if there exists a subset  $S'$  of  $S$  such that  $S'$  is smaller than  $\{C\}$ , that is  $\{C\} \succ S'$  and  $S' \rightarrow C$ .

The *superposition calculus*, denoted as  $\text{Sup}$ , is the most common inference system employed by saturation-based first-order theorem provers for first-order logic with equality [16]. A summary of superposition inference rules is given in Figure 2. The superposition calculus  $\text{Sup}$  is *sound* and *refutationally complete*: for any unsatisfiable formula  $\neg B$ , the empty clause can be derived as a logical consequence of  $\neg B$ .

In addition to the rules of Figure 2, modern saturation-based theorem provers [21,15,6] using the superposition calculus also implement special cases of superposition, with the aim of keeping the search space  $S$  small. To this end, the general theory of redundancy is exploited ensuring that redundant clauses can be eliminated during proof search without destroying completeness of the  $\text{Sup}$  calculus.

**Superposition:**

$$\frac{l = r \vee C \quad L[l'] \vee D}{(L[r] \vee C \vee D)\theta} \quad \frac{l = r \vee C \quad s[l'] \neq t \vee D}{(s[r] \neq t \vee C \vee D)\theta} \quad \frac{l = r \vee C \quad s[l'] = t \vee D}{(s[r] = t \vee C \vee D)\theta}$$

where  $\theta := \text{mgu}(l, l')$ ,  $r\theta \not\preceq l\theta$ , (first rule only)  $L[l']$  is not an equality literal, and (second and third rules only)  $t\theta \not\preceq s[l']\theta$ .

**Binary resolution:**

$$\frac{L \vee C \quad \neg L' \vee D}{(C \vee D)\theta}$$

where  $\theta := \text{mgu}(L, L')$ .

**Equality resolution:**

$$\frac{s \neq t \vee C}{C\theta}$$

where  $\theta := \text{mgu}(s, t)$ .

**Equality factoring:**

$$\frac{s = t \vee s' = t' \vee C}{(s = t \vee t \neq t' \vee C)\theta}$$

where  $\theta := \text{mgu}(s, s')$ ,  
 $t\theta \not\preceq s\theta$ , and  $t'\theta \not\preceq t\theta$ .

**Fig. 2.** The superposition calculus Sup for first-order logic with equality.

## 5 Saturation with Induction

We now describe our approach towards automating inductive reasoning within saturation-based proof search. We illustrate the key ingredients of our method using our motivating example from Figure 1(a), that is proving (1) in order to establish correctness of Figure 1(a). As mentioned in Section 4, proving (1) in a saturation-based approach means refuting the clausified negation of (1), that is, refuting the following two clauses:

$$\text{even}(\sigma_0) \tag{9}$$

$$\sigma_0 \neq \text{add}(\text{half}(\sigma_0), \text{half}(\sigma_0)) \tag{10}$$

We establish invalidity of inductive formulas, such as (9)-(10), by *integrating the application of induction as additional inference rules of the saturation process*. Our induction inference rules are used directly in Algorithm 1, as follows:

- (i) we pick up an inductive property  $G$  in the search space  $S$  (line 3);
- (ii) derive new induction axioms  $C_1, \dots, C_n$  (instances of *induction schemata*), aiming at refuting  $G$ , or sometimes a more general formula than  $G$  (line 4);
- (iii) add the induction axioms  $C_1, \dots, C_n$  to the search space (line 5).

Our work therefore follows a different approach than the one used in inductive theorem provers, as we do not rely on external algorithms to generate subgoals/stronger formulas  $G'$  of an inductive property  $G$  nor do we replace  $G$  by subgoals/stronger formulas  $G'$ . Rather, new induction axioms  $C_i$ , and sometimes new induction axioms  $C'_i$  for more general formulas  $G'$ , are derived from  $G$  and used in the search space  $S$  *in addition* to  $G$ .

Finding the right induction schema and developing efficient induction inference rules for deriving inductive axioms/formulas (steps (i)-(ii) above) are crucial for saturation with induction. In [18] we introduced the following induction

inference rule, parametrized by a valid induction schema:

$$\frac{\overline{L}[t] \vee C}{\text{cnf}(F \rightarrow \forall x.L[x])} \text{ (Ind)},$$

where  $t$  is a ground term,  $L$  is a ground literal,  $C$  is a clause, and  $F \rightarrow \forall x.L[x]$  is a valid induction schema. For example, the induction schema (2) for  $F$  can be used in (Ind). We call  $\overline{L}[t]$  the *induction literal* and  $t$  the *induction term*. We note that (Ind) can naturally be generalized to handle multiple induction terms, as in [11]. In this paper, we only use the rule with one induction term.

Based on Algorithm 1 (the saturation-based proof search algorithm), note that the application of (Ind) adds new clauses to the search space by clausifying induction formulas ( $\text{cnf}()$  in (Ind)). These new clauses then become potential candidates to be selected in the next steps of the algorithm. As such, the selection of these new clauses are likely to be delayed, and thus their use in proving an inductive goal becomes highly inefficient. We therefore propose the application of (Ind) followed by a binary resolution step to “guide” induction over selected induction literals and terms. In particular, upon the application of (Ind), we do not add  $\text{cnf}(F \rightarrow \forall x.L[x])$  to the search space. Instead, we binary resolve the conclusion literal  $L[x]$  against  $\overline{L}[t]$ , allowing us to only add the formula  $\text{cnf}(\neg F) \vee C$  to the search space, whenever (Ind) is applied.

In order to “guide” and combine the application of (Ind) with a binary resolution rule, we exploit instances of (Ind) for special cases of induction schemata over term algebras (Section 6) and integers (Section 9). We also consider extension of (Ind) for more general and efficient inductive reasoning (Section 7–8).

## 6 Induction with Term Algebras

We first consider the theory of term algebras and introduce instances of the induction rule (Ind), by exploiting properties of the induction literal  $\overline{L}[t]$  and induction schemata over the induction term  $t$ . For now, the induction term  $t$  is a ground element from a term algebra.

*Structural Induction.* The first instance of (Ind) uses the following constructor-based structural induction schema, where  $L[x]$  is a literal containing (possibly multiple occurrences of)  $x$  of a term algebra sort  $\tau$ :

$$\left( \bigwedge_{c \in \Sigma_\tau} \forall y_1, \dots, y_{n_c}. (\wedge_{i \in P_c} L[y_i] \rightarrow L[c(y_1, \dots, y_{n_c})]) \right) \rightarrow \forall x \in \tau. L[x] \quad (11)$$

Note that the structural induction schema (2) over naturals is an instance of (11).

*Example 1.* By instantiating schema (11) with the sole literal of clause (10) and induction term  $\sigma_0$ , we obtain:

$$\left( \begin{array}{l} 0 = \text{add}(\text{half}(0), \text{half}(0)) \wedge \\ \forall z \in \text{nat}. \left( \begin{array}{l} z = \text{add}(\text{half}(z), \text{half}(z)) \rightarrow \\ \text{s}(z) = \text{add}(\text{half}(\text{s}(z)), \text{half}(\text{s}(z))) \end{array} \right) \end{array} \right) \rightarrow \forall x \in \text{nat}. (x = \text{add}(\text{half}(x), \text{half}(x))) \quad (12)$$



The clausified form of (12) consists of the following two clauses:

$$\begin{aligned} 0 \neq \text{add}(\text{half}(0), \text{half}(0)) \vee \sigma_1 = \text{add}(\text{half}(\sigma_1), \text{half}(\sigma_1)) \vee x = \text{add}(\text{half}(x), \text{half}(x)) \\ 0 \neq \text{add}(\text{half}(0), \text{half}(0)) \vee \mathbf{s}(\sigma_1) \neq \text{add}(\text{half}(\mathbf{s}(\sigma_1)), \text{half}(\mathbf{s}(\sigma_1))) \\ \vee x = \text{add}(\text{half}(x), \text{half}(x)) \end{aligned}$$

After applying (Ind) instantiated with (12) on (10), the above clauses are resolved with the literal in clause (10), adding to the search space the resulting clauses:

$$\begin{aligned} 0 \neq \text{add}(\text{half}(0), \text{half}(0)) \vee \sigma_1 = \text{add}(\text{half}(\sigma_1), \text{half}(\sigma_1)) \\ 0 \neq \text{add}(\text{half}(0), \text{half}(0)) \vee \mathbf{s}(\sigma_1) \neq \text{add}(\text{half}(\mathbf{s}(\sigma_1)), \text{half}(\mathbf{s}(\sigma_1))) \quad \square \end{aligned}$$

*Well-Founded Induction.* Two other instances of (Ind) exploit well-founded induction schemata, by using a binary well-founded relation  $R$  on a term algebra  $\tau$ . For such an  $R$ , if there does not exist a smallest value  $v \in \tau$  w.r.t.  $R$  such that  $L[v]$  does not hold, then  $L[x]$  holds for any  $x \in \tau$ . This principle is formalized by the following schema:

$$\left( \neg \exists y \in \tau. (\neg L[y] \wedge \forall z \in \tau. (R(y, z) \rightarrow L[z])) \right) \rightarrow \forall x \in \tau. L[x] \quad (13)$$

However, to instantiate (13), we need to find an  $R$  suitable for the considered  $\tau$ .

Similarly to [20], we first consider the direct subterm relation expressed using term algebra constructors and destructors of the term algebra sort  $\tau$ . We obtain the following instance of (13) to be applied in (Ind):

$$\left( \neg \exists y. (\neg L[y] \wedge \bigwedge_{c \in \Sigma_\tau} (y = c(d_c^1(y), \dots, d_c^{n_c}(y)) \rightarrow \bigwedge_{i \in P_c} L[d_c^i(y)])) \right) \rightarrow \forall x. L[x] \quad (14)$$

In the case of natural numbers, where  $\mathbf{p}$  is the destructor for  $\mathbf{s}$ , we have the following instance of (14) to be used in (Ind):

$$\left( \neg \exists y \in \mathbf{nat}. (\neg L[y] \wedge (y = \mathbf{s}(\mathbf{p}(y)) \rightarrow L[\mathbf{p}(y)])) \right) \rightarrow \forall x \in \mathbf{nat}. L[x] \quad (15)$$

Another instance of (13) to be used in (Ind) employs a fresh predicate  $\text{less}_y$ , as given next. The axiomatisation of such a predicate enables efficient reasoning over subterm properties withing saturation, as advocated in [14].

$$\begin{aligned} \left( \neg \exists y. (\neg F[y] \wedge \forall z. (\text{less}_y(z) \rightarrow F[z]) \wedge (y = \mathbf{s}(\mathbf{p}(y)) \rightarrow \text{less}_y(\mathbf{p}(y))) \right. \\ \left. \wedge \forall w. (\text{less}_y(\mathbf{s}(\mathbf{p}(w))) \rightarrow \text{less}_y(\mathbf{p}(w))) \right) \rightarrow \forall x. F[x] \end{aligned} \quad (16)$$

*Induction with Recursive Function Definitions.* In formalizing the induction schemata instances given e.g. in (2) and (15), we considered the term algebra  $\mathbf{nat}$  as an instance of  $\tau$ . To come up with the “right” term algebra instance of  $\tau$ , we can also use terminating recursive function definitions from the input problem

to be proven, such as **add**, **even** and **half** from Figure 1(a). The termination of such recursive functions naturally depends on a well-founded relation  $R$ .

For an  $n$ -ary function  $\mathbf{f}$  and a clausified function definition axiom  $\mathbf{f}(\overline{\mathbf{s}}) = t \vee C$  in the search space, we call  $\mathbf{f}(\overline{\mathbf{s}})$  a *function header* and any  $\mathbf{f}(\overline{\mathbf{s}'}) \trianglelefteq t$  a *recursive call* of this function header. Moreover, we call an argument position  $1 \leq i \leq n$  *inductive* if for any such function header-recursive call pairs,  $s_i$  is a term algebra term (i.e. it only contains constructors and variables) and  $s'_i \triangleleft s_i$ ; in this case,  $s_i$  is called an *inductive argument*. Using inductive argument positions from function definitions, we can then generate inductive schemata similar to (11), possibly with multiple induction terms.

*Example 2.* We can obtain schema (2) from the second axiom of **add** in Figure (1)(b) with function header **add**( $\mathbf{s}(x), y$ ) and recursive call **add**( $x, y$ ) due to  $\mathbf{s}(x)$  being a term algebra term and  $x \triangleleft \mathbf{s}(x)$ . Moreover, the first argument of **add** in its first axiom gives the base case 0.

Similarly, the induction step case of schema (4) is given by the third axiom of **half** in Figure (1)(b) where the only argument  $\mathbf{s}(\mathbf{s}(x))$  of the function header is a term algebra term and for the first argument of the recursive call **half**( $x$ ), we have  $x \triangleleft \mathbf{s}(\mathbf{s}(x))$ . Finally, the base cases of schema (4) are the first arguments of the function headers from the first two axioms of **half**.

Thus, based on the function definitions in clauses (9) and (10), we can instantiate both (2) and (4) inducting on term  $\sigma_0$ . However, such induction axioms do not yet lead to a refutation of (1), because for each clausified induction axiom, new Skolem constants are introduced. Thus, the literals in clauses resulting from applying (Ind) on (9) or (10), respectively, do not contain  $\sigma_0$ , and hence we cannot use (10) nor (9), respectively, to refute them. In the next section we therefore generalize (Ind) towards the use of induction schemata with multiple clauses.  $\square$

## 7 Multi-Clause Induction

Inducting on a single literal is sometimes not sufficient to get a refutation, as illustrated in Example 2 for Figure 1(a). In general however, induction can be applied on literals from multiple clauses, similarly to formula (3) in Section 2. We generalize the inference rule (Ind) towards multi-clause induction (IndMC):

$$\frac{L_1[t] \vee C_1 \quad \dots \quad L_n[t] \vee C_n \quad \overline{L}[t] \vee C}{\text{cnf}(F \rightarrow \forall x. (\bigwedge_{1 \leq i \leq n} L_i[x] \rightarrow L[x]))} \text{ (IndMC)}$$

where  $F \rightarrow \forall x. (\bigwedge_{1 \leq i \leq n} L_i[x] \rightarrow L[x])$  is a valid induction formula,  $\overline{L}$  and  $L_i$  are ground literals and  $C$  and  $C_i$  are clauses. Similarly to (Ind), our new rule (IndMC) is used within saturation-based proof as an additional inference rule, followed by an application of binary resolution for guiding inductive reasoning.

*Example 3.* We use schema (4) with formula (1) with induction term  $\sigma_0$  to instantiate (IndMC) for premises (9) and (10). The induction formula is:

$$\left( \begin{array}{l} (\text{even}(0) \rightarrow 0 = \text{add}(\text{half}(0), \text{half}(0))) \wedge \\ (\text{even}(s(0)) \rightarrow s(0) = \text{add}(\text{half}(s(0)), \text{half}(s(0)))) \wedge \\ \forall z \in \text{nat}. \left( \begin{array}{l} (\text{even}(z) \rightarrow z = \text{add}(\text{half}(z), \text{half}(z))) \rightarrow \\ (\text{even}(s(s(z))) \rightarrow s(s(z)) = \text{add}(\text{half}(s(s(z))), \text{half}(s(s(z)))) \end{array} \right) \end{array} \right) \rightarrow \forall x \in \text{nat}. (\text{even}(x) \rightarrow x = \text{add}(\text{half}(x), \text{half}(x))) \quad (17)$$

Clausification of formula (17) results in twelve clauses, each containing the literals  $\neg \text{even}(x)$  and  $x = \text{add}(\text{half}(x), \text{half}(x))$ , which we can binary resolve with clauses (9) and (10). After simplifications are applied to the clauses from formula (17), we are left with the following two clauses:

$$\sigma_2 = \text{add}(\text{half}(\sigma_2), \text{half}(\sigma_2)) \quad (18)$$

$$s(\sigma_2) \neq \text{add}(\text{half}(\sigma_2), s(\text{half}(\sigma_2))) \quad (19)$$

We now need to rewrite (19) with the induction hypothesis clause (18) in the left-to-right orientation. However,  $\sigma_2 \prec \text{add}(\text{half}(\sigma_2), \text{half}(\sigma_2))$ , which holds for any simplification ordering  $\prec$ , contradicts the superposition ordering conditions. Moreover, even if we rewrote against the ordering, we would be left with

$$s(\text{add}(\text{half}(\sigma_2), \text{half}(\sigma_2))) \neq \text{add}(\text{half}(\sigma_2), s(\text{half}(\sigma_2))), \quad (20)$$

which is hard to refute using induction due to the induction term  $\sigma_2$  occurring in the second argument of  $\text{add}$ , which does not change in the recursive definition of  $\text{add}$  (see Figure 1(b)). We overcome this limitation by extensions of inductive reasoning in Section 8.  $\square$

## 8 Extensions of Inductions in Saturation

*Induction with Generalizations.* It is common in mathematics that for proving a formula  $A$ , we prove instead a formula  $B$  such that  $B \rightarrow A$ . In other words, we prove a *generalization*  $B$  of  $A$ . Inductive theorem provers implement various heuristics to *guess formulas/lemmas*  $B$  and use  $B$  instead of  $A$  during proof search, see e.g. [4,5,3,17]. However, a saturation-based theorem prover would not/can not do this, since goals/conjectures are not replaced by sub-goals in saturation-based proof search. We thus propose a different approach for implementing the common generalization recipe of mathematical theorem proving. Namely, we introduce the inference rule (IndGen) of *induction with generalization*, allowing us to (i) add instances of induction schemata not only for  $A$  but also for versions of  $B$  and then (ii) perform saturation over these induction schemata instances, using superposition reasoning. Our (IndGen) rule inducts only on *some* occurrences of the induction term  $t$ , as follows:

$$\frac{\overline{L}[t] \vee C}{\text{cnf}(F \rightarrow \forall x. L'[x])} \text{ (IndGen)},$$

where  $t$  is a ground term,  $L$  is a ground literal,  $C$  is a clause,  $F \rightarrow \forall x.L'[x]$  is a valid induction schema and  $L'[x]$  is obtained from  $L[t]$  by replacing some occurrences of  $t$  with  $x$ .

*Example 4.* We illustrate induction with generalization on the unit clause (20). One generalization that would help refute (20) by eliminating  $\mathbf{half}(\sigma_2)$  is:

$$\forall x, y \in \mathbf{nat}. \mathbf{s}(\mathbf{add}(x, y)) = \mathbf{add}(x, \mathbf{s}(y)) \quad (21)$$

Instantiating schema (2) with (21) and variable  $x$  would lead to a refutation when used with rule (IndGen) on (20). However, since we do not use  $y$  from the generalization in the induction, there is no need to replace the occurrences of  $\mathbf{half}(\sigma_2)$  corresponding to it in the generalized literal. Our final generalized induction formula, also leading to the refutation of (20), is:

$$\left( \begin{array}{c} \mathbf{s}(\mathbf{add}(0, \mathbf{half}(\sigma_2))) = \mathbf{add}(0, \mathbf{s}(\mathbf{half}(\sigma_2))) \wedge \\ \forall z \in \mathbf{nat}. \left( \begin{array}{c} \mathbf{s}(\mathbf{add}(z, \mathbf{half}(\sigma_2))) = \mathbf{add}(z, \mathbf{s}(\mathbf{half}(\sigma_2))) \rightarrow \\ \mathbf{s}(\mathbf{add}(\mathbf{s}(z), \mathbf{half}(\sigma_2))) = \mathbf{add}(\mathbf{s}(z), \mathbf{s}(\mathbf{half}(\sigma_2))) \end{array} \right) \end{array} \right) \quad (22)$$

$$\rightarrow \forall x \in \mathbf{nat}. \mathbf{s}(\mathbf{add}(x, \mathbf{half}(\sigma_2))) = \mathbf{add}(x, \mathbf{s}(\mathbf{half}(\sigma_2))) \quad \square$$

*Rewriting with Induction Hypotheses.* For turning saturation-based proof search into an efficient process, one key ingredient is to ensure that bigger terms/literals are rewritten by small ones (big/small w.r.t. the simplification ordering  $\succ$ ), and not vice versa. However, this often prohibits using induction hypotheses to rewrite their corresponding conclusions which would be the necessary step to proceed with the proof, such as rewriting of (19) with (18) in the left-to-right orientation to obtain (20), on which we would then use (IndGen) with induction formula (22) to proceed with the proof. To overcome this obstacle, we introduce the following inference rule which uses an induction hypothesis literal to rewrite its conclusion:

$$\frac{l = r \vee D \quad s[l] \neq t \vee C}{\mathbf{cnf}(F \rightarrow \forall x.(s[r] = t)[x])} \quad (\mathbf{IndHRW})$$

where  $s[l] \neq t$  is an induction conclusion literal with corresponding induction hypothesis literal  $l = r$ ,  $l \not\prec r$ , and  $F \rightarrow \forall x.(s[r] = t)[x]$  is a valid induction formula. Moreover, we resolve the clauses with the intermediate clause  $s[r] \neq t \vee C \vee D$ , obtained from the rewriting of the premises of (IndHRW).

*Example 5.* Using unit clause (18) in a left-to-right orientation and rewriting the sides of unit clause (19) one after the other, we get intermediate clauses, which are then used for generating induction formulas. One such intermediate clause is (20), from which the induction formula (22) is generated. Below, the

clausification of formula (22) is shown:

$$\begin{aligned} & \mathbf{s}(\text{add}(0, \text{half}(\sigma_2))) \neq \text{add}(0, \mathbf{s}(\text{half}(\sigma_2))) \\ & \vee \mathbf{s}(\text{add}(\sigma_3, \text{half}(\sigma_2))) = \text{add}(\sigma_3, \mathbf{s}(\text{half}(\sigma_2))) \\ & \vee \mathbf{s}(\text{add}(x, \text{half}(\sigma_2))) = \text{add}(x, \mathbf{s}(\text{half}(\sigma_2))) \end{aligned} \quad (23)$$

$$\begin{aligned} & \mathbf{s}(\text{add}(0, \text{half}(\sigma_2))) \neq \text{add}(0, \mathbf{s}(\text{half}(\sigma_2))) \\ & \vee \mathbf{s}(\text{add}(\mathbf{s}(\sigma_3), \text{half}(\sigma_2))) \neq \text{add}(\mathbf{s}(\sigma_3), \mathbf{s}(\text{half}(\sigma_2))) \\ & \vee \mathbf{s}(\text{add}(x, \text{half}(\sigma_2))) = \text{add}(x, \mathbf{s}(\text{half}(\sigma_2))) \end{aligned} \quad (24)$$

By resolving both (23) and (24) with the intermediate clause (20), we get the following clauses:

$$\begin{aligned} & \mathbf{s}(\text{add}(0, \text{half}(\sigma_2))) \neq \text{add}(0, \mathbf{s}(\text{half}(\sigma_2))) \\ & \vee \mathbf{s}(\text{add}(\sigma_3, \text{half}(\sigma_2))) = \text{add}(\sigma_3, \mathbf{s}(\text{half}(\sigma_2))) \end{aligned} \quad (25)$$

$$\begin{aligned} & \mathbf{s}(\text{add}(0, \text{half}(\sigma_2))) \neq \text{add}(0, \mathbf{s}(\text{half}(\sigma_2))) \\ & \vee \mathbf{s}(\text{add}(\mathbf{s}(\sigma_3), \text{half}(\sigma_2))) \neq \text{add}(\mathbf{s}(\sigma_3), \mathbf{s}(\text{half}(\sigma_2))) \end{aligned} \quad (26)$$

After we rewrite the first literal,  $\mathbf{s}(\text{add}(0, \text{half}(\sigma_2))) \neq \text{add}(0, \mathbf{s}(\text{half}(\sigma_2)))$ , by the first axiom of **add**, we obtain the literal  $\mathbf{s}(\text{half}(\sigma_2)) \neq \mathbf{s}(\text{half}(\sigma_2))$  in both clauses, which we can remove as it is a trivially invalid inequality. We are thus left only with the second literal from both (25) and (26):

$$\mathbf{s}(\text{add}(\sigma_3, \text{half}(\sigma_2))) = \text{add}(\sigma_3, \mathbf{s}(\text{half}(\sigma_2))) \quad (27)$$

$$\mathbf{s}(\text{add}(\mathbf{s}(\sigma_3), \text{half}(\sigma_2))) \neq \text{add}(\mathbf{s}(\sigma_3), \mathbf{s}(\text{half}(\sigma_2))) \quad (28)$$

We rewrite (28) by the second axiom of **add** twice, obtaining:

$$\mathbf{s}(\mathbf{s}(\text{add}(\sigma_3, \text{half}(\sigma_2)))) \neq \mathbf{s}(\text{add}(\sigma_3, \mathbf{s}(\text{half}(\sigma_2)))) \quad (29)$$

Using injectivity of **s** we derive  $\mathbf{s}(\text{add}(\sigma_3, \text{half}(\sigma_2))) \neq \text{add}(\sigma_3, \mathbf{s}(\text{half}(\sigma_2)))$ , which we can finally resolve with (27), resulting into  $\square$ . For the whole formal proof of the assertion of Figure 1(a), we refer the reader to Appendix A.1.  $\square$

## 9 Integer Induction

In this section we introduce *integer induction* in saturation as a natural extension of our term algebra induction framework discussed so far. Inductive reasoning with integers is another common task in program analysis and verification, as illustrated in Figure 3(a). The first-order axiomatisation of the functional behavior and requirement for Figure 3(a) is given in Figure 3(b).

The main insight of integer induction comes with the following observation of [12]. As the standard order  $<$  (or  $>$ ) over integers  $\mathbb{Z}$  is not well-founded, we work with *subsets of  $\mathbb{Z}$  with a lower (and/or an upper) bound*. We therefore define the *downward, respectively upward, induction schema with symbolic bound  $b$*  as any formula of the form

$$\begin{aligned} & F[b] \wedge \forall y \in \mathbb{Z}. (y \leq b \wedge F[y] \rightarrow F[y-1]) \rightarrow \forall x \in \mathbb{Z}. (x \leq b \rightarrow F[x]); \quad (\text{downward}) \\ & F[b] \wedge \forall y \in \mathbb{Z}. (y \geq b \wedge F[y] \rightarrow F[y+1]) \rightarrow \forall x \in \mathbb{Z}. (x \geq b \rightarrow F[x]), \quad (\text{upward}) \end{aligned}$$

<p><b>assume</b> <math>e \geq 0</math></p> <p><b>fun</b> <math>\text{pow}(x, 0) = 1</math>  <math>\quad   \text{pow}(x, e) = x \cdot \text{pow}(x, e - 1);</math></p> <p><b>assert</b> <math>\text{pow}(2, e) &gt; e</math></p> <p style="text-align: center;">(a)</p>	<p>Axiomatization of <b>pow</b>:</p> <p><math>\forall x \in \mathbb{Z}.(\text{pow}(x, 0) = 1)</math></p> <p><math>\forall x, e \in \mathbb{Z}.(1 \leq e \rightarrow \text{pow}(x, e) = x \cdot \text{pow}(x, e - 1))</math></p> <p>Verification task (conjecture):</p> <p><math>\forall e \in \mathbb{Z}.(0 \leq e \rightarrow e &lt; \text{pow}(2, e))</math></p> <p style="text-align: center;">(b)</p>
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**Fig. 3.** Functional program over integers.

respectively, where  $F[x]$  is a formula with one or more occurrences of an integer variable  $x$  and  $b$  is an integer term not containing  $x$  nor  $y$ . Further, we also define *interval downward*, *respectively upward*, *induction schema with symbolic bounds*  $b_1, b_2$  as any formula of the form

$$\begin{aligned}
 & F[b_2] \wedge \forall y \in \mathbb{Z}.(b_1 < y \leq b_2 \wedge F[y] \rightarrow F[y - 1]) \\
 & \quad \rightarrow \forall x \in \mathbb{Z}.(b_1 \leq x \leq b_2 \rightarrow F[x]); \quad (\text{downward}) \\
 & F[b_1] \wedge \forall y \in \mathbb{Z}.(b_1 \leq y < b_2 \wedge F[y] \rightarrow F[y + 1]) \\
 & \quad \rightarrow \forall x \in \mathbb{Z}.(b_1 \leq x \leq b_2 \rightarrow F[x]), \quad (\text{upward})
 \end{aligned}$$

respectively, where  $F[x]$  is a formula with one or more occurrences of an integer variable  $x$  and  $b_1, b_2$  are integer terms not containing  $x$  nor  $y$ .<sup>4</sup>

*Example 6.* Note that the verification task of Figure 3(b) holds also only over the non-negative subset of integers (lower bound 0). For proving correctness of Figure 3(a), we would therefore use the following instance of the upward induction schema with symbolic bound with  $b \stackrel{\text{def}}{=} 0$  and  $F[x] \stackrel{\text{def}}{=} x < \text{pow}(2, x)$ :

$$\begin{aligned}
 & (0 < \text{pow}(2, 0) \wedge \forall x \in \mathbb{Z}.(x \geq 0 \wedge x < \text{pow}(2, x) \rightarrow x + 1 < \text{pow}(2, x + 1))) \\
 & \quad \rightarrow \forall y \in \mathbb{Z}.(y \geq 0 \rightarrow y < \text{pow}(2, y)) \quad (30)
 \end{aligned}$$

□

To automate inductive reasoning over integers, we further need to automatically generate suitable instances of our integer induction schemata, for example upward induction schema instances for Figure 3(a). Similarly as for term algebra reasoning, we introduce induction rules with the integer induction schemata in the conclusion, which give us the recipe for instantiating the schemata. Note that since our schemata above are sound, all our resulting induction rules are sound as well. For brevity we only show the upward inference rules and leave out the symmetric downward rules. When  $t$  is a ground term,  $b$  is a ground term and  $L[t]$  a ground literal, the following are *integer upward induction rules*:

$$\frac{\overline{L[t] \vee C} \quad t \geq b}{\text{cnf}\left((L[b] \wedge \forall y \in \mathbb{Z}.(y \geq b \wedge L[y] \rightarrow L[y + 1])) \rightarrow \forall x \in \mathbb{Z}.(x \geq b \rightarrow L[x])\right)} \quad (\text{IntInd}_{\geq})$$

<sup>4</sup> The above schemata can be seen as a special case of the multi-clause schemata used in the (IndMC) rule from Section 7, tailored specifically for integers.

$$\frac{\overline{L}[t] \vee C \quad t > b}{\text{cnf}\left(\left(L[b] \wedge \forall y \in \mathbb{Z}.(y \geq b \wedge L[y] \rightarrow L[y+1])\right) \rightarrow \forall x \in \mathbb{Z}.(x > b \rightarrow L[x])\right)} \quad (\text{IntInd}_{>})$$

While the  $\text{IntInd}_{\geq}$  rule uses the upward schema exactly as defined above, the  $\text{IntInd}_{>}$  rule uses a modified schema with weakened conclusion, containing  $x > b$  instead of  $x \geq b$ . This is a practical optimization: by resolving the clausified schema against  $\overline{L}[t] \vee C$ , we obtain clauses containing  $\neg(t > b)$ , which can be immediately resolved against the premise  $t > b$ . If we instead needed to resolve away the literal  $\neg(t \geq b)$ , we would need to first apply some theory reasoning to weaken  $\neg(t \geq b)$  into  $\neg(t > b)$ .

Similar to the above rules with one bound, we introduce *integer interval upward induction rules* for a ground term  $t$ , ground terms  $b_1, b_2$  and a ground literal  $L[t]$ :

$$\frac{\overline{L}[t] \vee C \quad t \geq b_1 \quad t \leq b_2}{\text{cnf}\left(\left(L[b_1] \wedge \forall y \in \mathbb{Z}.(b_1 \leq y < b_2 \wedge L[y] \rightarrow L[y+1])\right) \rightarrow \forall x \in \mathbb{Z}.(b_1 \leq x \leq b_2 \rightarrow L[x])\right)} \quad (\text{IntInd}_{[\geq]})$$

$$\frac{\overline{L}[t] \vee C \quad t > b_1 \quad t \leq b_2}{\text{cnf}\left(\left(L[b_1] \wedge \forall y \in \mathbb{Z}.(b_1 \leq y < b_2 \wedge L[y] \rightarrow L[y+1])\right) \rightarrow \forall x \in \mathbb{Z}.(b_1 < x \leq b_2 \rightarrow L[x])\right)} \quad (\text{IntInd}_{[>]})$$

Note that in addition to the  $\text{IntInd}_{[\geq]}$  and  $\text{IntInd}_{[>]}$  rules, we can also introduce analogous rules  $\text{IntInd}_{[\geq']}$  and  $\text{IntInd}_{[>']}$  using the premise  $t < b_2$  instead of  $t \leq b_2$ , and using correspondingly weakened conclusion.

Finally, we also introduce *integer upward induction rule with default bound 0* for a ground term  $t$  and a ground literal  $L[t]$ ,

$$\frac{\overline{L}[t] \vee C}{\text{cnf}\left(\left(L[0] \wedge \forall y \in \mathbb{Z}.(y \geq 0 \wedge L[y] \rightarrow L[y+1])\right) \rightarrow \forall x \in \mathbb{Z}.(x \geq 0 \rightarrow L[x])\right)} \quad (\text{IntInd}_{\geq 0}),$$

which is together with the analogous integer downward induction rule with default bound 0 useful for proving properties holding for all integers.<sup>5</sup>

*Example 7.* The key steps of proving the correctness of Figure 3(a) using the induction rule  $\text{IntInd}_{\geq}$  are displayed in Table 4. For clarity, we convert  $\neg(s < t)$  into  $t \leq s$  and  $\neg(s \leq t)$  into  $t < s$  for any terms  $s, t$ . Clauses  $C_1, C_2$  are the clausified axioms from Figure 3, while  $C_3, C_4$  are the negated clausified conjecture from Figure 3. We apply  $\text{IntInd}_{\geq}$  on  $C_4$  and  $C_3$ , producing clauses  $C_5, C_6$  and  $C_7$ . Next, we use superposition to rewrite the term  $\text{pow}(2, 0)$  in clauses  $C_5, C_6, C_7$  by the definition from  $C_1$  and then evaluate the resulting inequality  $1 \leq 0$  to false, and we remove it, obtaining clauses  $C_8, C_9, C_{10}$ . We then apply theory reasoning by applying binary resolution with suitable axiom for the predicate  $\leq$ , resulting into clause  $C_{11}$ . Next we apply superposition on  $C_{11}, C_{10}$ , obtaining  $C_{12}$ . Then we evaluate the interpreted  $0 + 1$  to 1, apply superposition with  $C_2$ , and evaluate

<sup>5</sup> See problem (13) in [12].

$(C_1)$	$\text{pow}(x, 0) = 1$	[input – axiom]
$(C_2)$	$e < 1 \vee \text{pow}(x, e) = x \cdot \text{pow}(x, e - 1)$	[input – axiom]
$(C_3)$	$0 \leq \sigma_0$	[input – conjecture]
$(C_4)$	$\text{pow}(2, \sigma_0) \leq \sigma_0$	[input – conjecture]
$(C_5)$	$\text{pow}(2, 0) \leq 0 \vee 0 \leq \sigma_1$	[IntInd $_{\geq}$ $C_4, C_3$ , BR with $C_3, C_4$ ]
$(C_6)$	$\text{pow}(2, 0) \leq 0 \vee \sigma_1 < \text{pow}(2, \sigma_1)$	[IntInd $_{\geq}$ $C_4, C_3$ , BR with $C_3, C_4$ ]
$(C_7)$	$\text{pow}(2, 0) \leq 0 \vee \text{pow}(2, \sigma_1 + 1) \leq \sigma_1 + 1$	[IntInd $_{\geq}$ $C_4, C_3$ , BR with $C_3, C_4$ ]
$(C_8)$	$0 \leq \sigma_1$	[rewriting $C_5$ by $C_1$ and eval.]
$(C_9)$	$\sigma_1 < \text{pow}(2, \sigma_1)$	[rewriting $C_6$ by $C_1$ and eval.]
$(C_{10})$	$\text{pow}(2, \sigma_1 + 1) \leq \sigma_1 + 1$	[rewriting $C_7$ by $C_1$ and eval.]
$(C_{11})$	$1 \leq \sigma_1 \vee 0 = \sigma_1$	[ $\leq$ axioms $C_8$ ]
$(C_{12})$	$1 \leq \sigma_1 \vee \text{pow}(2, 0 + 1) \leq 0 + 1$	[rewriting $C_{10}$ by $C_{11}$ ]
$(C_{13})$	$1 \leq \sigma_1 \vee 2 \cdot \text{pow}(2, 0) \leq 1 \vee 1 < 1$	[eval. $C_{12}$ , rewriting by $C_2$ , and eval.]
$(C_{14})$	$1 \leq \sigma_1$	[rewriting $C_{13}$ by $C_1$ and eval.]
$(C_{15})$	$2 \cdot \text{pow}(2, \sigma_1) \leq \sigma_1 + 1$	[rewriting $C_{10}$ by $C_2$ using $C_{14}$ ]
$(C_{16})$	$2 \cdot \sigma_1 < \sigma_1 + 1$	[ $<, \leq$ axioms $C_9, C_{15}$ ]
$(C_{17})$	$\sigma_1 < 1$	[cancellation of $\sigma_1$ in $C_{16}$ ]
$(C_{18})$	$\square$	[BR $C_{14}, C_{17}$ ]

**Fig. 4.** Key steps of a saturation-based proof certifying correctness of Figure 3(a).

$1 - 1$  to 0, obtaining  $C_{13}$ . We next rewrite  $C_{13}$  by  $C_1$  and remove both  $2 \cdot 1 \leq 1$  and  $1 < 1$  since they are evaluated to false, resulting into  $C_{14}$ . We then rewrite  $C_{10}$  by  $C_2$  using superposition and binary resolution with  $C_{14}$ , obtaining  $C_{15}$ . Using more theory reasoning, we arrive at  $C_{17}$ , which can be finally resolved against  $C_{14}$ , yielding the empty clause.  $\square$

## 10 Implementation and Experiments

### 10.1 Implementation

Our approach for automating induction in saturation is implemented in the VAMPIRE prover. All together, our implementation consists of around 7,800 lines of C++ code and is available online at <https://github.com/vprover/vampire/tree/int-induction>. In the following, VAMPIRE\* refers to the VAMPIRE version supporting induction.

Our induction rules allow us to derive many new clauses potentially leading to refutation of inductive properties. These new clauses – especially in combination with theory reasoning in case of integer induction – might however pollute the search space without advancing the proof. We therefore introduce options to control the use of induction rules by inducting only on negative literals, unit clauses or clauses derived from the goal. Further, for induction over algebraic types, we only allow induction on terms containing a constant other than a base constructor. For integer induction, by default we disable rules with default bound, and induction on interpreted constants. Finally, by default we do not apply integer induction on  $\overline{L}[t] \vee C$  if  $\overline{L}[t]$  is in the form  $t \circ s$  or  $s \circ t$  where



Name & comma-separated values	Description
--induction int, struct, both, <u>none</u>	Enable induction on integers only, or induction on algebraic types only, or both, or none
--structural_induction_kind <u>one</u> , two, three, rec_def, all	What kind of induction axioms to use for induction on term algebras
--induction_max_depth <u>0</u> , 1, 2, ...	Maximum number of induction steps in any sequence of inferences, 0 means no maximum
--induction_neg_only <u>on</u> , off	Only apply induction on negative literals
--induction_unit_only <u>on</u> , off	Only apply induction on unit clauses
--induction_on_complex_terms on, <u>off</u>	Apply induction also on complex terms
--induction_multiclaue <u>on</u> , off	Enable the (IndMC) form of induction rules
--induction_gen on, <u>off</u>	Enable the (IndGen) form of induction rules
--induction_hypothesis_rewriting <u>on</u> , off	Enable the (IndHRW) form of induction rules
--function_definition_rewriting <u>on</u> , off	Use function definitions as rewrite rules with the intended orientation
--int_induction_interval infinite, finite, <u>both</u>	Enable the integer induction rules, or interval integer induction rules, or both
--int_induction_default_bound on, <u>off</u>	Enable the integer induction rules with default bound

**Table 1.** Summary of VAMPIRE’s induction options. Default values are underlined.

$\circ \in \{<, \leq, >, \geq\}$  and  $t$  does not occur in  $s$ . Our most relevant induction options are summarized in Table 1.<sup>6</sup>

## 10.2 Experimental Setup

The main goal of our experiments was to evaluate how much induction improves VAMPIRE’s performance. We therefore compared VAMPIRE\* to VAMPIRE without induction. We also show the numbers of problems solved by the SMT solvers CVC4 [20], Z3 [7], where only CVC4 supports induction. In our experiments, we do not include other provers, such as ACL2 [3] or ZIPPERPOSITION [6], as these solvers do not support the SMT-LIB input format [1]; yet for further comparison we refer to [9,12,11].

We ran our experiments using (i) benchmarks over inductive data types (**UFDT** set of the SMT-LIB benchmark library and *dtv* set of the inductive benchmarks [10]), (ii) benchmarks using integers (LIA, UFLIA, NIA and UF-NIA of SMT-LIB and *int* of [10]), and (iii) benchmarks using both integers and data types (UFDTLIA of SMT-LIB). From these datasets, we excluded those problems that are marked satisfiable, as our work is meant for validity checking<sup>7</sup>

<sup>6</sup> VAMPIRE also offers a so-called portfolio mode, in which it sequentially tries different option configurations for short amounts of time.

<sup>7</sup> we have excluded all together 1562 satisfiable problems from LIA, UFLIA, NIA and UFNIA; and 86 satisfiable problems from UFDT.

Problem set	SMT-LIB						ind. set [10]		sum
	UFDT	UFDTLIA	LIA	UFLIA	NIA	UFNIA	<i>dtg</i>	<i>int</i>	
Total count	4483	327	404	10118	8	12181	3397	120	31038
VAMPIRE	1848	82	241	6125	3	3704	17	0	12020
VAMPIRE*	1792	186	241	6240	4	3679	464	76	12682
Cvc4	2072	200	357	6911	7	3022	164	30	12763
Z3	1807	76	242	6710	2	4938	17	0	13792

**Table 2.** Comparison of the number of solved problems. The configuration of VAMPIRE and VAMPIRE\* depends on the benchmark set.

For our experiments, we used Z3 version 4.8.12 in the default configuration, and CVC4 version 1.8 with parameters `--conjecture-gen` `--quant-ind`. To extensively compare VAMPIRE and VAMPIRE\*, we ran multiple instances of both for each experiment: we used a portfolio of 18 base configurations differing in the parameters not related to induction. Additionally, we varied the induction parameters of VAMPIRE\* for each experiment: for (i) we used `--induction_struct` `--structural_induction_kind one` `--induction_gen on` `-induction_on_complex_terms on`, for (ii) `--induction_int` `--induction_multiclaue off`, for (iii) `--induction both` `--structural_induction_kind one` `--induction_gen on` `-induction_on_complex_terms on`. In experiments (ii) and (iii), for each of the 18 base configurations we ran 7 instances of VAMPIRE\* with different integer induction parameters, chosen based on preliminary experimentation on a smaller set of benchmarks. For an overview of the VAMPIRE\* configurations, see Appendix A.2. Each prover configuration was given 10 seconds and 16 GB of memory per each problem. The experiments were ran on computers with 32 cores (AMD Epyc 7502, 2.5 GHz) and 1 TB RAM.

### 10.3 Experimental Results

*Results overview.* Our results are summarized in Table 2. For VAMPIRE and VAMPIRE\* we show the number of problems solved by the most successful configuration. Note that for different benchmark sets the most successful configurations might be different. In the inductive problems, the maximum and average numbers of induction steps in a proof were 20 and 1.54, respectively, and the maximum number of nested induction steps was 9. Overall, Table 2 shows that VAMPIRE\* outperforms VAMPIRE without induction. Moreover, VAMPIRE\* is competitive with leading SMT solvers.

*Comparison of VAMPIRE and VAMPIRE\*.* To evaluate the impact of inductive reasoning in VAMPIRE, we look at two key metrics: the *overall number of solved problems*; and the *number of newly solved problems*, which we define as the

Benchmarks	Configurations	Combined	Most solved	Most new	Default mode
UFDT	VAMPIRE	2082	1848	-	1827
	any VAMPIRE*	2047	1792 (12)	1754 (17)	1761
<i>dtty</i>	VAMPIRE	17	17	-	17
	any VAMPIRE*	525	464 (453)	464 (453)	432
LIA, UFLIA, NIA, UFNIA	VAMPIRE	11260	10073	-	9835
	any VAMPIRE*	11334 (81)	10051 (0)	9006 (41)	9773 (0)
<i>int</i>	VAMPIRE	0	0	-	0
	any VAMPIRE*	118 (118)	76 (76)	76 (76)	49 (49)
UFDTLIA	VAMPIRE	91	82	-	65
	any VAMPIRE*	197 (108)	186 (101)	186 (101)	136 (72)

**Table 3.** Comparison of VAMPIRE and VAMPIRE\* configurations; numbers given (in parentheses) indicate new problems solved using induction but not without induction.

number of problems solved using induction<sup>8</sup> by some VAMPIRE\*, but not solved by any VAMPIRE. The latter metric is especially important, since in practice, one can run multiple solvers or configurations in parallel, and thereby solve the union of all problems solved by individual solvers.

Table 3 summarizes our result. Column “Combined” lists the number of problems solved by any instance of the configuration, and in the parentheses the number of problems newly solved by the configuration. The other columns (most solved, most new, default mode) give the numbers of solved problems, and in parentheses newly solved problems, for the corresponding VAMPIRE/VAMPIRE\* instance. The “Default mode” columns shows results for the best induction configuration with all non-induction parameters set to default.

Induction helped most with the *dtty*, *int* and UFDTLIA benchmark sets, as these sets contain a lot of problems focused on induction (induction was used in 91% of proofs for problems in *dtty*, in all proofs in *int*, and in 71% of proofs in UFDTLIA), while the other sets contain a wide variety of problems (induction was only used in 2% of proofs in UFDT and 8.8% of proofs in LIA, UFLIA, NIA and UFNIA). Interestingly, the configuration which solved most problems in *int* solved the least in LIA, UFLIA, NIA, UFNIA combined, what illustrates the difficulty in choosing the right values for integer induction parameters for such a mixed benchmark set.

## 11 Conclusion

Motivated by application of program analysis and verification, we describe recent advances in automating inductive reasoning about first-order (program) properties using inductively defined data types and beyond. We integrate induction in

<sup>8</sup> New rules change proof search organization and VAMPIRE\* might solve a problem without using induction, while this problem was not solved by VAMPIRE. We do not consider such problems to be newly solved.

the saturation-based proof engine of first-order theorem provers, without radical changes in the existing machinery of such provers. Our inductive inference rules and heuristics open up new research directions to be further studied in automating induction. Guiding and further extending the application of multi-clause induction with theory-specific induction schema variants is an interesting line of research. Combining induction schemas and rules and using lemma generation and rewriting procedures from inductive theorem provers are another ways to further improve saturation-based inductive reasoning.

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## A Appendix

### A.1 Full proof of Example 1

1.  $\forall x. \text{add}(0, x) = x$  [axiom]
2.  $\forall x, y. \text{add}(s(x), y) = s(\text{add}(x, y))$  [axiom]
3.  $\text{even}(0)$  [axiom]
4.  $\forall x. \text{even}(s(x)) \leftrightarrow \neg \text{even}(x)$  [axiom]
5.  $\text{even}(s(x)) \vee \text{even}(x)$  [cnf 4]
6.  $\neg \text{even}(s(x)) \vee \neg \text{even}(x)$  [cnf 4]
7.  $\text{half}(0) = 0$  [axiom]
8.  $\text{half}(s(0)) = 0$  [axiom]
9.  $\forall x. \text{half}(s(s(x))) = s(\text{half}(x))$  [axiom]
10.  $\forall x. \text{even}(x) \rightarrow x = \text{add}(\text{half}(x), \text{half}(x))$  [conjecture]
11.  $\text{even}(\sigma_1)$  [cnf 10]
12.  $x \neq \text{add}(\text{half}(\sigma_1), \text{half}(\sigma_1))$  [cnf 10]
13.  $\text{even}(0) \vee \text{even}(s(0)) \vee \neg \text{even}(\sigma_2) \vee \sigma_2 = \text{add}(\text{half}(\sigma_2), \text{half}(\sigma_2))$  [IndMC]
- $\vee \neg \text{even}(x) \vee x = \text{add}(\text{half}(x), \text{half}(x))$  11,12]
14.  $\text{even}(0) \vee \text{even}(s(0)) \vee \text{even}(s(s(\sigma_2)))$  [IndMC]
- $\vee \neg \text{even}(x) \vee x = \text{add}(\text{half}(x), \text{half}(x))$  11,12]
15.  $\text{even}(0) \vee \text{even}(s(0))$
- $\vee s(s(\sigma_2)) \neq \text{add}(\text{half}(s(s(\sigma_2))), \text{half}(s(s(\sigma_2))))$  [IndMC]
- $\vee \neg \text{even}(x) \vee x = \text{add}(\text{half}(x), \text{half}(x))$  11,12]
16.  $\text{even}(0) \vee s(0) \neq \text{add}(\text{half}(s(0), s(0)))$
- $\vee \neg \text{even}(\sigma_2) \vee \sigma_2 = \text{add}(\text{half}(\sigma_2), \text{half}(\sigma_2))$  [IndMC]
- $\vee \neg \text{even}(x) \vee x = \text{add}(\text{half}(x), \text{half}(x))$  11,12]
17.  $\text{even}(0) \vee s(0) \neq \text{add}(\text{half}(s(0), s(0)))$  [IndMC]
- $\vee \text{even}(s(s(\sigma_2))) \vee \neg \text{even}(x) \vee x = \text{add}(\text{half}(x), \text{half}(x))$  11,12]
18.  $\text{even}(0) \vee s(0) \neq \text{add}(\text{half}(s(0), s(0)))$
- $\vee s(s(\sigma_2)) \neq \text{add}(\text{half}(s(s(\sigma_2))), \text{half}(s(s(\sigma_2))))$  [IndMC]
- $\vee \neg \text{even}(x) \vee x = \text{add}(\text{half}(x), \text{half}(x))$  11,12]
19.  $0 \neq \text{add}(\text{half}(0), \text{half}(0)) \vee \text{even}(s(0))$
- $\vee \neg \text{even}(\sigma_2) \vee \sigma_2 = \text{add}(\text{half}(\sigma_2), \text{half}(\sigma_2))$  [IndMC]
- $\vee \neg \text{even}(x) \vee x = \text{add}(\text{half}(x), \text{half}(x))$  11,12]
20.  $0 \neq \text{add}(\text{half}(0), \text{half}(0)) \vee \text{even}(s(0))$  [IndMC]
- $\vee \text{even}(s(s(\sigma_2))) \vee \neg \text{even}(x) \vee x = \text{add}(\text{half}(x), \text{half}(x))$  11,12]
21.  $0 \neq \text{add}(\text{half}(0), \text{half}(0)) \vee \text{even}(s(0))$
- $\vee s(s(\sigma_2)) \neq \text{add}(\text{half}(s(s(\sigma_2))), \text{half}(s(s(\sigma_2))))$  [IndMC]
- $\vee \neg \text{even}(x) \vee x = \text{add}(\text{half}(x), \text{half}(x))$  11,12]
22.  $0 \neq \text{add}(\text{half}(0), \text{half}(0)) \vee s(0) \neq \text{add}(\text{half}(s(0), s(0)))$
- $\vee \neg \text{even}(\sigma_2) \vee \sigma_2 = \text{add}(\text{half}(\sigma_2), \text{half}(\sigma_2))$  [IndMC]
- $\vee \neg \text{even}(x) \vee x = \text{add}(\text{half}(x), \text{half}(x))$  11,12]
23.  $0 \neq \text{add}(\text{half}(0), \text{half}(0)) \vee s(0) \neq \text{add}(\text{half}(s(0), s(0)))$  [IndMC]
- $\vee \text{even}(s(s(\sigma_2))) \vee \neg \text{even}(x) \vee x = \text{add}(\text{half}(x), \text{half}(x))$  11,12]
24.  $0 \neq \text{add}(\text{half}(0), \text{half}(0)) \vee s(0) \neq \text{add}(\text{half}(s(0), s(0)))$
- $\vee s(s(\sigma_2)) \neq \text{add}(\text{half}(s(s(\sigma_2))), \text{half}(s(s(\sigma_2))))$  [IndMC]
- $\vee \neg \text{even}(x) \vee x = \text{add}(\text{half}(x), \text{half}(x))$  11,12]
25.  $\text{even}(0) \vee \text{even}(s(0)) \vee \neg \text{even}(\sigma_2) \vee$
- $\sigma_2 = \text{add}(\text{half}(\sigma_2), \text{half}(\sigma_2)) \vee \neg \text{even}(\sigma_1)$  [BR 13,12]

26.  $\text{even}(0) \vee \text{even}(s(0)) \vee \neg \text{even}(\sigma_2) \vee \sigma_2 = \text{add}(\text{half}(\sigma_2), \text{half}(\sigma_2))$  [BR 25,11]
27.  $\text{even}(0) \vee \text{even}(s(0)) \vee \text{even}(s(s(\sigma_2))) \vee \neg \text{even}(\sigma_1)$  [BR 14,12]
28.  $\text{even}(0) \vee \text{even}(s(0)) \vee \text{even}(s(s(\sigma_2)))$  [BR 27,11]
29.  $\text{even}(0) \vee \text{even}(s(0))$   
 $\quad \vee s(s(\sigma_2)) \neq \text{add}(\text{half}(s(s(\sigma_2))), \text{half}(s(s(\sigma_2)))) \vee \neg \text{even}(\sigma_1)$  [BR 15,12]
30.  $\text{even}(0) \vee \text{even}(s(0))$   
 $\quad \vee s(s(\sigma_2)) \neq \text{add}(\text{half}(s(s(\sigma_2))), \text{half}(s(s(\sigma_2))))$  [BR 29,11]
31.  $\text{even}(0) \vee s(0) \neq \text{add}(\text{half}(s(0), s(0))) \vee \neg \text{even}(\sigma_2)$   
 $\quad \vee \sigma_2 = \text{add}(\text{half}(\sigma_2), \text{half}(\sigma_2)) \vee \neg \text{even}(\sigma_1)$  [BR 16,12]
32.  $\text{even}(0) \vee s(0) \neq \text{add}(\text{half}(s(0), s(0))) \vee \neg \text{even}(\sigma_2)$   
 $\quad \vee \sigma_2 = \text{add}(\text{half}(\sigma_2), \text{half}(\sigma_2))$  [BR 31,11]
33.  $\text{even}(0) \vee s(0) \neq \text{add}(\text{half}(s(0), s(0)))$   
 $\quad \vee \text{even}(s(s(\sigma_2))) \vee \neg \text{even}(\sigma_1)$  [BR 17,12]
34.  $\text{even}(0) \vee s(0) \neq \text{add}(\text{half}(s(0), s(0))) \vee \text{even}(s(s(\sigma_2)))$  [BR 33,11]
35.  $\text{even}(0) \vee s(0) \neq \text{add}(\text{half}(s(0), s(0)))$   
 $\quad \vee s(s(\sigma_2)) \neq \text{add}(\text{half}(s(s(\sigma_2))), \text{half}(s(s(\sigma_2)))) \vee \neg \text{even}(\sigma_1)$  [BR 18,12]
36.  $\text{even}(0) \vee s(0) \neq \text{add}(\text{half}(s(0), s(0)))$   
 $\quad \vee s(s(\sigma_2)) \neq \text{add}(\text{half}(s(s(\sigma_2))), \text{half}(s(s(\sigma_2))))$  [BR 35,11]
37.  $0 \neq \text{add}(\text{half}(0), \text{half}(0)) \vee \text{even}(s(0)) \vee \neg \text{even}(\sigma_2)$   
 $\quad \vee \sigma_2 = \text{add}(\text{half}(\sigma_2), \text{half}(\sigma_2)) \vee \neg \text{even}(\sigma_1)$  [BR 19,12]
38.  $0 \neq \text{add}(\text{half}(0), \text{half}(0)) \vee \text{even}(s(0)) \vee \neg \text{even}(\sigma_2)$   
 $\quad \vee \sigma_2 = \text{add}(\text{half}(\sigma_2), \text{half}(\sigma_2))$  [BR 37,11]
39.  $0 \neq \text{add}(\text{half}(0), \text{half}(0)) \vee \text{even}(s(0))$   
 $\quad \vee \text{even}(s(s(\sigma_2))) \vee \neg \text{even}(\sigma_1)$  [BR 20,12]
40.  $0 \neq \text{add}(\text{half}(0), \text{half}(0)) \vee \text{even}(s(0)) \vee \text{even}(s(s(\sigma_2)))$  [BR 39,11]
41.  $0 \neq \text{add}(\text{half}(0), \text{half}(0)) \vee \text{even}(s(0))$   
 $\quad \vee s(s(\sigma_2)) \neq \text{add}(\text{half}(s(s(\sigma_2))), \text{half}(s(s(\sigma_2)))) \vee \neg \text{even}(\sigma_1)$  [BR 21,12]
42.  $0 \neq \text{add}(\text{half}(0), \text{half}(0)) \vee \text{even}(s(0))$   
 $\quad \vee s(s(\sigma_2)) \neq \text{add}(\text{half}(s(s(\sigma_2))), \text{half}(s(s(\sigma_2))))$  [BR 41,11]
43.  $0 \neq \text{add}(\text{half}(0), \text{half}(0)) \vee s(0) \neq \text{add}(\text{half}(s(0), s(0)))$   
 $\quad \vee \neg \text{even}(\sigma_2) \vee \sigma_2 = \text{add}(\text{half}(\sigma_2), \text{half}(\sigma_2)) \vee \neg \text{even}(\sigma_1)$  [BR 22,12]
44.  $0 \neq \text{add}(\text{half}(0), \text{half}(0)) \vee s(0) \neq \text{add}(\text{half}(s(0), s(0)))$   
 $\quad \vee \neg \text{even}(\sigma_2) \vee \sigma_2 = \text{add}(\text{half}(\sigma_2), \text{half}(\sigma_2))$  [BR 43,11]
45.  $0 \neq \text{add}(\text{half}(0), \text{half}(0)) \vee s(0) \neq \text{add}(\text{half}(s(0), s(0)))$   
 $\quad \vee \text{even}(s(s(\sigma_2))) \vee \neg \text{even}(\sigma_1)$  [BR 23,12]
46.  $0 \neq \text{add}(\text{half}(0), \text{half}(0)) \vee s(0) \neq \text{add}(\text{half}(s(0), s(0)))$   
 $\quad \vee \text{even}(s(s(\sigma_2)))$  [BR 45,11]
47.  $0 \neq \text{add}(\text{half}(0), \text{half}(0)) \vee s(0) \neq \text{add}(\text{half}(s(0), s(0)))$   
 $\quad \vee s(s(\sigma_2)) \neq \text{add}(\text{half}(s(s(\sigma_2))), \text{half}(s(s(\sigma_2))))$   
 $\quad \vee \neg \text{even}(x) \vee x = \text{add}(\text{half}(x), \text{half}(x))$  [BR 24,12]
48.  $0 \neq \text{add}(\text{half}(0), \text{half}(0)) \vee s(0) \neq \text{add}(\text{half}(s(0), s(0)))$   
 $\quad \vee s(s(\sigma_2)) \neq \text{add}(\text{half}(s(s(\sigma_2))), \text{half}(s(s(\sigma_2))))$   
 $\quad \vee \neg \text{even}(x) \vee x = \text{add}(\text{half}(x), \text{half}(x))$  [BR 47,11]
49.  $0 \neq \text{add}(0, 0) \vee \text{even}(s(0)) \vee \neg \text{even}(\sigma_2)$   
 $\quad \vee \sigma_2 = \text{add}(\text{half}(\sigma_2), \text{half}(\sigma_2))$  [Sup 38,7]
50.  $0 \neq 0 \vee \text{even}(s(0)) \vee \neg \text{even}(\sigma_2) \vee \sigma_2 = \text{add}(\text{half}(\sigma_2), \text{half}(\sigma_2))$  [Sup 49,1]
51.  $\text{even}(s(0)) \vee \neg \text{even}(\sigma_2) \vee \sigma_2 = \text{add}(\text{half}(\sigma_2), \text{half}(\sigma_2))$  [ER 50]
52.  $\neg \text{even}(0) \vee \neg \text{even}(\sigma_2) \vee \sigma_2 = \text{add}(\text{half}(\sigma_2), \text{half}(\sigma_2))$  [BR 51,6]
53.  $\neg \text{even}(\sigma_2) \vee \sigma_2 = \text{add}(\text{half}(\sigma_2), \text{half}(\sigma_2))$  [BR 52,3]
54.  $0 \neq \text{add}(0, 0) \vee \text{even}(s(0)) \vee \text{even}(s(s(\sigma_2)))$  [Sup 40,7]

55. $0 \neq 0 \vee \text{even}(s(0)) \vee \text{even}(s(s(\sigma_2)))$	[Sup 54,1]
56. $\text{even}(s(0)) \vee \text{even}(s(s(\sigma_2)))$	[ER 55]
57. $\neg \text{even}(0) \vee \text{even}(s(s(\sigma_2)))$	[BR 56,6]
58. $\text{even}(s(s(\sigma_2)))$	[BR 57,3]
59. $\neg \text{even}(s(\sigma_2))$	[BR 58,6]
60. $\text{even}(\sigma_2)$	[BR 59,5]
61. $\sigma_2 = \text{add}(\text{half}(\sigma_2), \text{half}(\sigma_2))$	[BR 60,53]
62. $0 \neq \text{add}(0, 0) \vee \text{even}(s(0))$ $\vee s(s(\sigma_2)) \neq \text{add}(\text{half}(s(s(\sigma_2))), \text{half}(s(s(\sigma_2))))$	[Sup 42,7]
63. $0 \neq 0 \vee \text{even}(s(0))$ $\vee s(s(\sigma_2)) \neq \text{add}(\text{half}(s(s(\sigma_2))), \text{half}(s(s(\sigma_2))))$	[Sup 62,1]
64. $\text{even}(s(0)) \vee s(s(\sigma_2)) \neq \text{add}(\text{half}(s(s(\sigma_2))), \text{half}(s(s(\sigma_2))))$	[ER 63]
65. $\neg \text{even}(0) \vee s(s(\sigma_2)) \neq \text{add}(\text{half}(s(s(\sigma_2))), \text{half}(s(s(\sigma_2))))$	[BR 64,6]
66. $s(s(\sigma_2)) \neq \text{add}(\text{half}(s(s(\sigma_2))), \text{half}(s(s(\sigma_2))))$	[BR 65,3]
67. $s(s(\sigma_2)) \neq \text{add}(s(\text{half}(\sigma_2)), s(\text{half}(\sigma_2)))$	[Sup 66,9]
68. $s(s(\sigma_2)) \neq s(\text{add}(\text{half}(\sigma_2), s(\text{half}(\sigma_2))))$	[Sup 67,2]
69. $s(\sigma_2) \neq \text{add}(\text{half}(\sigma_2), s(\text{half}(\sigma_2)))$	[inj s 68]
70. $s(\text{add}(0, \text{half}(\sigma_2))) \neq \text{add}(0, s(\text{half}(\sigma_2)))$ $\vee s(\text{add}(\sigma_3, \text{half}(\sigma_2))) = \text{add}(\sigma_3, s(\text{half}(\sigma_2)))$ $\vee s(\text{add}(x, \text{half}(\sigma_2))) = \text{add}(x, s(\text{half}(\sigma_2)))$	[IndHRW 69,61]
71. $s(\text{add}(0, \text{half}(\sigma_2))) \neq \text{add}(0, s(\text{half}(\sigma_2)))$ $\vee s(\text{add}(s(\sigma_3), \text{half}(\sigma_2))) \neq \text{add}(s(\sigma_3), s(\text{half}(\sigma_2)))$ $\vee s(\text{add}(x, \text{half}(\sigma_2))) = \text{add}(x, s(\text{half}(\sigma_2)))$	[IndHRW 69,61]
72. $s(\text{add}(0, \text{half}(\sigma_2))) \neq \text{add}(0, s(\text{half}(\sigma_2)))$ $\vee s(\text{add}(\sigma_3, \text{half}(\sigma_2))) = \text{add}(\sigma_3, s(\text{half}(\sigma_2)))$ $\vee s(\sigma_2) = \text{add}(\text{half}(\sigma_2), s(\text{half}(\sigma_2)))$	[Sup 70,61]
73. $s(\text{add}(0, \text{half}(\sigma_2))) \neq \text{add}(0, s(\text{half}(\sigma_2)))$ $\vee s(\text{add}(s(\sigma_3), \text{half}(\sigma_2))) \neq \text{add}(s(\sigma_3), s(\text{half}(\sigma_2)))$ $\vee s(\sigma_2) = \text{add}(\text{half}(\sigma_2), s(\text{half}(\sigma_2)))$	[Sup 71,61]
74. $s(\text{add}(0, \text{half}(\sigma_2))) \neq \text{add}(0, s(\text{half}(\sigma_2)))$ $\vee s(\text{add}(\sigma_3, \text{half}(\sigma_2))) = \text{add}(\sigma_3, s(\text{half}(\sigma_2)))$	[BR 72,69]
75. $s(\text{add}(0, \text{half}(\sigma_2))) \neq \text{add}(0, s(\text{half}(\sigma_2)))$ $\vee s(\text{add}(s(\sigma_3), \text{half}(\sigma_2))) \neq \text{add}(s(\sigma_3), s(\text{half}(\sigma_2)))$	[BR 73,69]
76. $s(\text{half}(\sigma_2)) \neq s(\text{half}(\sigma_2))$ $\vee s(\text{add}(\sigma_3, \text{half}(\sigma_2))) = \text{add}(\sigma_3, s(\text{half}(\sigma_2)))$	[Sup 74,1]
77. $s(\text{add}(\sigma_3, \text{half}(\sigma_2))) = \text{add}(\sigma_3, s(\text{half}(\sigma_2)))$	[ER 76]
78. $s(\text{half}(\sigma_2)) \neq s(\text{half}(\sigma_2))$ $\vee s(\text{add}(s(\sigma_3), \text{half}(\sigma_2))) \neq \text{add}(s(\sigma_3), s(\text{half}(\sigma_2)))$	[Sup 75,1]
79. $s(\text{add}(s(\sigma_3), \text{half}(\sigma_2))) \neq \text{add}(s(\sigma_3), s(\text{half}(\sigma_2)))$	[ER 78]
80. $s(s(\text{add}(\sigma_3, \text{half}(\sigma_2)))) \neq \text{add}(s(\sigma_3), s(\text{half}(\sigma_2)))$	[Sup 79,2]
81. $s(s(\text{add}(\sigma_3, \text{half}(\sigma_2)))) \neq s(\text{add}(\sigma_3, s(\text{half}(\sigma_2))))$	[Sup 80,2]
82. $s(\text{add}(\sigma_3, \text{half}(\sigma_2))) \neq \text{add}(\sigma_3, s(\text{half}(\sigma_2)))$	[inj s 81]
83. $\square$	[BR 82,77]

## A.2 Vampire configurations used in experiments

The 18 strategies we used as base configurations for both VAMPIRE and VAMPIRE\* consisted of all combinations of configurations 0-5 and A-C displayed in Table 5. Configurations 0 and A correspond to the default values.



Parameter	Value used in configuration with ID:					
	0	1	2	3	4	5
--age_weight_ratio	1	1	2	3	5	10
--saturation_algorithm	lrs	lrs	lrs	discount	lrs	discount
--selection	10	11	1010	11	4	1011
--theory_instantiation	off	off	off	strong	off	off
--unification_with_abstraction	off	one_side_ interpreted	one_side_ interpreted	one_side_ interpreted	off	off

Parameter	Value used in configuration with ID:		
	A	B	C
--term_ordering	kbo	lpo	lpo
--demodulation_redundancy_check	on	off	off
--unit_resulting_resolution	off	off	on
--sos	off	theory	off
--sos_theory_limit	0	1	0
--evaluation	simple	simple	force
--gaussian_variable_elimination	off	off	force
--arithmetic_subterm_generalizations	off	off	force
--push_unary_minus	off	off	on
--cancellation	off	off	force

Table 5. General parameter combinations for configurations with ID {0-5}{A-C}

Parameter	Value used in configuration with ID:						
	322c0	031c1	121c0	030-1	011-1	001-1	140-0d
--int_induction_strictness_eq	not_in_both	none	toplevel_not_in_other	none	none	none	toplevel_not_in_other
--int_induction_strictness_comp	only_one_occurrence	not_in_both	only_one_occurrence	not_in_both	toplevel_not_in_other	none	always
--int_induction_strictness_term	no_skolems	interpreted_constant	interpreted_constant	none	interpreted_constant	interpreted_constant	none
--induction_on_complex_terms	on	on	on	off	off	off	off
--induction_max_depth	0	1	0	1	1	1	0
--int_induction_default_bound	off	off	off	off	off	off	on

Table 6. Integer induction parameter combinations for configurations with ID 322c0, 031c1, 121c0, 030-1, 011-1, 001-1 and 140-0d.

The 7 integer induction configurations used for VAMPIRE\* are displayed in Table 6. For the `--int_induction_strictness` parameter values, each digit controls an aspect of integer induction: what equality literals we apply induction on, what comparison literals, what terms. The larger the value the less we apply induction.