- Hierarchy architecture
- Same numerical kernel for all grid levels
- Two-way nesting
- Equal-power MPI scheme
- Direct data transfer through ParentID and ChildID
- Moving grid algorithm

	npx=0	npx=1	npx=2
npy=2 →	myid=2 ProcessorID=(1,3)	myid=5 ProcessorID=(2,3)	myid=8 ProcessorID=(3,3)
npy=1 →	myid=1 ProcessorID=(1,2)	myid=4 ProcessorID=(2,2)	myid=7 ProcessorID=(3,2)
npy=0 →	myid=0 ProcessorID=(1,1)	myid=3 ProcessorID=(2,1)	myid=6 ProcessorID=(3,1)

■ : ghost cell

ex) West boundary

myid=2
WestParentID=4
myid=1
WestParentID=4
myid=1
WestParentID=3
myid=0
(mb,nb)
WestParentID=3

Domain decomposition for MPI

PX   PY