## **Integration Manual**

for S32K14X CAN Driver

Document Number: IM2CANASR4.2 Rev0002R1.0.1

Rev. 1.0



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# **Chapter 1 Revision History**

## Table 1-1. Revision History

Revision	Date	Author	Description
1.0	13/07/2018		Updated version for ASR 4.2.2S32K14X1.0.1 Release

## Chapter 2 Introduction

This integration manual describes the integration requirements for CAN Driver for S32K14X microcontrollers.

## 2.1 Supported Derivatives

The software described in this document is intented to be used with the following microcontroller devices of NXP Semiconductors .

Table 2-1. S32K14X Derivatives

NXP Semiconductors	s32k148_lqfp144, s32k148_lqfp176,
	s32k148_mapbga100, s32k146_lqfp144,
	s32k146_lqfp100, s32k146_lqfp64,
	s32k146_mapbga100, s32k144_lqfp100,
	s32k144_lqfp64, s32k144_mapbga100,
	s32k142_lqfp100, s32k142_lqfp64,
	s32k118_lqfp48, s32k118_lqfp64

All of the above microcontroller devices are collectively named as S32K14X.

## 2.2 Overview

**AUTOSAR** (**AUTomotive Open System ARchitecture**) is an industry partnership working to establish standards for software interfaces and software modules for automobile electronic control systems.

#### **AUTOSAR**

- paves the way for innovative electronic systems that further improve performance, safety and environmental friendliness.
- is a strong global partnership that creates one common standard: "Cooperate on standards, compete on implementation".

#### **About this Manual**

- is a key enabling technology to manage the growing electrics/electronics complexity. It aims to be prepared for the upcoming technologies and to improve cost-efficiency without making any compromise with respect to quality.
- facilitates the exchange and update of software and hardware over the service life of the vehicle.

#### 2.3 About this Manual

This Technical Reference employs the following typographical conventions:

**Boldface** type: Bold is used for important terms, notes and warnings.

*Italic* font: Italic typeface is used for code snippets in the text. Note that C language modifiers such "const" or "volatile" are sometimes omitted to improve readability of the presented code.

Notes and warnings are shown as below:

**Note** 

This is a note.

## 2.4 Acronyms and Definitions

Table 2-2. Acronyms and Definitions

Term	Definition
API	Application Programming Interface
AUTOSAR	Automotive Open System Architecture
ASM	Assembler
BSMI	Basic Software Make file Interface
CAN	Controller Area Network
DEM	Diagnostic Event Manager
DET	Development Error Tracer
C/CPP	C and C++ Source Code
VLE	Variable Length Encoding
N/A	Not Applicable
MCU	Micro Controller Unit

## 2.5 Reference List

#### **Table 2-3. Reference List**

#	Title	Version
1	Specification of CAN Driver	AUTOSAR Release 4.2.2
2	S32K14X Reference Manual	Reference Manual, Rev. 7, 4/2018
3	S32K142 Mask Set Errata for Mask 0N33V (0N33V)	30/11/2017
4	S32K144 Mask Set Errata for Mask 0N57U (0N57U)	30/11/2017
5	S32K146 Mask Set Errata for Mask 0N73V (0N73V)	30/11/2017
6	S32K148 Mask Set Errata for Mask 0N20V (0N20V)	30/11/2017
7	S32K118 Mask Set Errata for Mask 0N97V (0N97V)	26/02/2018

Reference List

## **Chapter 3 Building the Driver**

This section describes the source files and various compilers, linker options used for building the Autosar CAN driver for NXP SemiconductorsS32K14X . It also explains the EB Tresos Studio plugin setup procedure.

## 3.1 Build Options

The CAN driver files are compiled using

- Green Hills Multi 7.1.4 / Compiler 2017.1.4
- (Linaro GCC 6.3-2017.06~dev) 6.3.1 20170509 (Thu Dec 7 13:28:42 CST 2017 build.sh rev=g7fea41d s=L631 Earmv7 -V release\_g7fea41d\_build\_Fed\_Earmv7) (from S32-DS-ARM\_v2018)
- IAR: V8.11.2

The compiler, linker flags used for building the driver are explained below:

#### **Note**

The TS\_T40D2M10I1R0 plugin name is composed as follow:

 $TS_T = Target_Id$ 

D = Derivative\_Id

M = SW\_Version\_Major

I = SW\_Version\_Minor

R = Revision

(i.e. Target\_Id = 40 identifies CORTEXM architecture and Derivative\_Id = 2 identifies the S32K14X)

## 3.1.1 GHS Compiler/Linker/Assembler Options

**Table 3-1. Compiler Options** 

Option	Description
-cpu=cortexm4	Selects target processor: Arm Cortex M4
-ansi	Specifies ANSI C with extensions. This mode extends the ANSI X3.159-1989 standard with certain useful and compatible constructs.
-Osize	Optimize for size.
-dual_debug	Enables the generation of DWARF, COFF, or BSD debugging information in the object file
-G	Generates source level debugging information and allows procedure call from debugger's command line.
no_exceptions	Disables support for exception handling
-Wundef	Generates warnings for undefined symbols in preprocessor expressions
-Wimplicit-int	Issues a warning if the return type of a function is not declared before it is called
-Wshadow	Issues a warning if the declaration of a local variable shadows the declaration of a variable of the same name declared at the global scope, or at an outer scope
-Wtrigraphs	Issues a warning for any use of trigraphs
-Wall	Enables all the warnings about constructions that some users consider questionable, and that are easy to avoid even in conjunction with macros.
prototype_errors	Generates errors when functions referenced or called have no prototype
incorrect_pragma_warnings	Valid #pragma directives with wrong syntax are treated as warnings
-noslashcomment	C++ like comments will generate a compilation error
-preprocess_assembly_files	Preprocesses assembly files
-nostartfile	Do not use Start files
short_enum	Store enumerations in the smallest possible type
-c	Produces an object file (called input-file.o) for each source file.
no_commons	Allocates uninitialized global variables to a section and initializes them to zero at program startup.
-keeptempfiles	Prevents the deletion of temporary files after they are used. If an assembly language file is created by the compiler, this option will place it in the current directory instead of the temporary directory. Produces an object file (called input-file.o) for each source file.
-list	Creates a listing by using the name of the object file with the .lst extension. Assembler option
DAUTOSAR_OS_NOT_USE	-D defines a preprocessor symbol and optionally can set it to a value.  AUTOSAR_OS_NOT_USED: By default in the package, the drivers are compiled to be used without Autosar OS. If the drivers are used with Autosar OS, the compiler option '-DAUTOSAR_OS_NOT_USED' must be removed from project options
- DDISABLE_MCAL_INTERMO DULE_ASR_CHECK	-D defines a preprocessor symbol to disable the inter-module version check for AR_RELEASE versions. DISABLE_MCAL_INTERMODULE_ASR_CHECK: By default in the package, drivers are compiled to perform the inter-module version check as per Autosar BSW004. When the inter-module version check needs to be disabled then the DISABLE_MCAL_INTERMODULE_ASR_CHECK global define must be added to the list of compiler options.
-DGHS	-D defines a preprocessor symbol and optionally can set it to a value. This one defines the GHS preprocessor symbol.

## **Table 3-2. Assembler Options**

Option	Description
-cpu=cortexm4	Selects target processor: Arm Cortex M4
-с	Produces an object file (called input-file.o) for each source file.
-preprocess_assembly_files	Preprocesses assembly files
-asm=list	Creates a listing by using the name of the object file with the .lst extension. Assembler option

### **Table 3-3. Linker Options**

Option	Description
-Mn	Map file numeric ordering
-delete	Removal from the executable of functions that are unused and unreferenced
-V	Display removed unused functions
-ignore_debug_references	Ignores relocations from DWARF debug sections when using -delete.
-map	Creates a detailed map file
-keepmap	Keep the map file in the event of a link error
-Istartup	Link libstartup library -Run-time environment startup routines
-lsys	Link libsys library -Run-time environment system routines
-larch	Link libarch library -Target-specific run-time support. Any file produced by the Green Hills Compiler may depend on symbols in this library.
-lansi	Link libansi library -the standard C library
-L(/lib/thumb2)	Link thumb2 library
-lutf8_s32	Include utf8_s32.a to use the Wide Character Functions

## 3.1.2 IAR Compiler/Linker/Assembler Options

**Table 3-4. Compiler Options** 

Option	Description
cpu=Cortex-M4	Selects target processor: Arm Cortex M4
cpu_mode=thumb	Selects generating code that executes in Thumb state.
endian=little	Specifies the endianess of core: little endian.
-Ohz	Sets the optimization level to High, favoring size.
-c	Produces an object file (called input-file.o) for each source file.
no_clustering	Disables static clustering optimizations.
no_mem_idioms	Makes the compiler to not optimize code sequences that clear, set, or copy a memory region.
no_explicit_zero_opt	Places the zero initialized variables in data section instead of bss.
debug	Makes the compiler include information in the object modules.
diag_suppress=Pa050	Suppresses diagnostic messages (warnings) about non-standard line endings.

Table continues on the next page...

## **Table 3-4. Compiler Options (continued)**

Option	Description
DAUTOSAR_OS_NOT_USE	-D defines a preprocessor symbol and optionally can set it to a value.  AUTOSAR_OS_NOT_USED: By default in the package, the drivers are compiled to be used without Autosar OS. If the drivers are used with Autosar OS, the compiler option '-DAUTOSAR_OS_NOT_USED' must be removed from project options
-DIAR	-D defines a preprocessor symbol and optionally can set it to a value. This one defines the IAR preprocessor symbol.
require_prototypes	Forces the compiler to verify that all functions have proper prototypes.
no_wrap_diagnostics	Disables line wrapping of diagnostic messages issued by compiler.
no_system_include	Disables the automatic search for system include files.
-е	Enables language extensions. This option is needed by FLS driver which uses _packed structures.

#### **Table 3-5. Assembler Options**

Option	Description
cpu=Cortex-M4	Selects target processor: Arm Cortex M4
cpu_mode=thumb	Selects generating code that executes in Thumb state.
-g	Use this option to disable the automatic search for system include files.

### **Table 3-6. Linker Options**

Option	Description
cpu=Cortex-M4	Selects target processor: Arm Cortex M4
map filename	Produces a map file.
no_library_search	Disables automatic runtime library search.
entry _start	Treats the symbol _start as a root symbol and as the start of the application.
enable_stack_usage	Enables stack usage analysis.
skip_dynamic_initialization	Suppress dynamic initialization during system startup.
no_wrap_diagnostics	Disables line wrapping of diagnostic messages issued by linker.
config	Specifies the configuration file to be used by the linker.

## 3.1.3 GCC Compiler/Linker/Assembler Options

## **Table 3-7. Compiler Options**

Option	Description
-c	Produces an object file (called input-file.o) for each source file.
-01	Use optimization for size.
1 00	Produce debugging information for use by GDB. Level 3 includes extra information, such as all the macro definitions present in the program.

Table continues on the next page...

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## **Table 3-7. Compiler Options (continued)**

Option	Description
-mcpu=cortex-m4	Selects target processor: Arm Cortex M4
-mthumb	Selects generating code that executes in Thumb state.
-mlittle-endian	Generate code for a processor running in little-endian mode.
-fomit-frame-pointer	Removes the frame pointer for all functions, which might make debugging harder.
-mhard-float	Use hardware floating-point instructions.
-fno-common	Specifies that the compiler should place uninitialized global variables in the data section of the object file, rather than generating them as common blocks.
-Wall	Enables all the warnings about constructions that some users consider questionable, and that are easy to avoid even in conjunction with macros.
-Wextra	Enables some extra warning flags that are not enabled by '-Wall'.
-Wstrict-prototypes	Warn if a function is declared or defined without specifying the argument types.
-Wno-sign-compare	Do not warn when a comparison between signed and unsigned values could produce an incorrect result when the signed value is converted to unsigned.
DAUTOSAR_OS_NOT_USE	-D defines a preprocessor symbol and optionally can set it to a value.  AUTOSAR_OS_NOT_USED: By default in the package, the drivers are compiled to be used without Autosar OS. If the drivers are used with Autosar OS, the compiler option '-DAUTOSAR_OS_NOT_USED' must be removed from project options
-DLINARO	-D defines a preprocessor symbol and optionally can set it to a value. This one defines the LINARO preprocessor symbol.
-fstack-usage	Geneates an extra file that specifies the maximum amount of stack used, on a per-function basis.
-fdump-ipa-all	Enables all inter-procedural analysis dumps.
-Werror=implicit-function-declaration	Generates an error when the prototype of the function is not defined

## **Table 3-8. Assembler Options**

Option Description				
-mcpu=cortex-m4	Selects target processor: Arm Cortex M4			
-с	Produces an object file (called input-file.o) for each source file.			
-mthumb	This option specifies that the assembler should start assembling Thumb instructions.			
-x assembler-with-cpp	Indicates that the assembly code contains C directives and the C preprocessor must be run.			

## Table 3-9. Linker Options

Option Description			
-Map=filename	Print a link map to the file mapfile.		
-T scriptfile	Use scriptfile as the linker script. This script replaces Id's default linker script(rather than adding to it), so commandfile must specify everything necessary to describe the output file.		

minor versions can be compiled.

## 3.2 Files Required for the Compilation

This section describes the include files required to compile, assemble (if assembler code) and link the CAN driver for S32K14X microcontrollers.

To avoid integration of incompatible files, all the include files from other modules shall have the same AR\_RELEASE\_MAJOR\_VERSION and AR\_RELEASE\_MINOR\_VERSION, i.e. only files with the same AUTOSAR major and

#### **CAN Files**

- ..\Can\_TS\_T40D2M10I1R0\include\Can.h
- ..\Can\_TS\_T40D2M10I1R0\include\Can\_IPW.h
- ..\Can\_TS\_T40D2M10I1R0\include\Can\_FlexCan.h
- ..\Can\_TS\_T40D2M10I1R0\include\Reg\_eSys\_FlexCan.h
- ..\Can TS T40D2M10I1R0\src\Can.c
- ..\Can\_TS\_T40D2M10I1R0\src\Can\_Irq.c
- ..\Can\_TS\_T40D2M10I1R0\src\Can\_FlexCan.c

#### **CAN Generated Files**

For driver compilation, Can\_[VariantName]\_PBcfg.c should be generated by the user using a configuration tool. The file contains the definition of the init pointer for the respective variant.

As a deviation from standard:

- Can\_[VariantName]\_PBcfg.c (For PB Variant) files will contain the definition for all parameters that are variant aware, independent of the configuration class that will be selected (PC, LT,PB).
- Can\_Cfg.c (For PC Variant) file will contain the definition for all configuration structures containing only variables that are not variant aware, configured and generated only once. This file alone does not contain the whole structure needed by Can\_Init function to configure the driver. Based on the number of variants configured in the EcuC, there can be more than one configuration structure for one module even for PreCompile variant.

#### Files from Base common folder

- ..\Base\_TS\_T40D2M10I1R0\include\Compiler.h
- ..\Base\_TS\_T40D2M10I1R0\include\Compiler\_Cfg.h
- ..\Base\_TS\_T40D2M10I1R0\include\ComStack\_Types.h
- ..\Base\_TS\_T40D2M10I1R0\include\Mcal.h
- ..\Base\_TS\_T40D2M10I1R0\include\Platform\_Types.h

- ..\Base\_TS\_T40D2M10I1R0\include\Reg\_eSys.h
- ..\Base\_TS\_T40D2M10I1R0\include\Soc\_Ips.h
- ..\Base\_TS\_T40D2M10I1R0\include\Reg\_Macros.h
- ..\Base\_TS\_T40D2M10I1R0\include\Can\_GeneralTypes.h

#### Files from CanIf folder:

- ..\CanIf\_TS\_T40D2M10I1R0\include\CanIf\_Cbk.h
- ..\CanIf\_TS\_T40D2M10I1R0\include\CanIf\_Types.h.h
- ..\CanIf\_TS\_T40D2M10I1R0\include\CanIf.h

#### Files from Det folder:

- ..\Det\_TS\_T40D2M10I1R0\src\Det.c
- ..\Det\_TS\_T40D2M10I1R0\include\Det.h

#### Files from Rte folder:

- ..\Rte\_TS\_T40D2M10I1R0\src\Schm\_Can.c
- ..\Rte\_TS\_T40D2M10I1R0\include\Schm\_Can.h

#### Files from EcuM folder:

- ..\EcuM\_TS\_T40D2M10I1R0\src\EcuM.c
- ..\EcuM\_TS\_T40D2M10I1R0\include\EcuM.h
- ..\EcuM\_TS\_T40D2M10I1R0\include\EcuM\_Cbk.h

## 3.3 Setting up the Plugins

The CAN driver was designed to be configured by using the EB Tresos Studio (version EB tresos Studio 23.0.0 b170330-0431 or later.)

#### Location of various files inside the CAN module folder:

- VSMD (Vendor Specific Module Definition) file in EB tresos Studio XDM format:
  - ..\Can\_TS\_T40D2M10I1R0\config\Can.xdm
  - ..\CanIf\_TS\_T40D2M10I1R0\config\CanIf.xdm

  - ..\Base\_TS\_T40D2M10I1R0\config\Base.xdm

  - $\bullet \ .. \ \ Mcu\_TS\_T40D2M10I1R0 \ \ \ config\ \ \ Mcu.xdm$
- VSMD (Vendor Specific Module Definition) file(s) in AUTOSAR compliant EPD format:
  - ..\Can\_TS\_T40D2M10I1R0\autosar\Can.epd
  - ..\CanIf\_TS\_T40D2M10I1R0\autosar\CanIf.epd

#### Setting up the Plugins

- ..\EcuM\_TS\_T40D2M10I1R0\autosar\EcuM.epd
- ..\Mcu\_TS\_T40D2M10I1R0\autosar\Mcu.epd
- Code Generation Templates for Pre-Compile time configuration parameters:
  - ..\Can\_TS\_T40D2M10I1R0\generate\_PC\include\Can\_Cfg.h
  - ..\Can\_TS\_T40D2M10I1R0\generate\_PC\include\Can\_Cfg.c
  - ..\Can\_TS\_T40D2M10I1R0\generate\_PC\Can\_SourceClock.m
  - ..\Can\_TS\_T40D2M10I1R0\generate\_PC\Can\_VersionCheck\_Inc.m
  - ..\Can\_TS\_T40D2M10I1R0\generate\_PC\Can\_VersionCheck\_Src.m
  - ..\Can\_TS\_T40D2M10I1R0\generate\_PC\Can\_NotifyCheck\_Src.m
- Code Generation Templates for Post-Build time configuration parameters:
  - ..\Can\_TS\_T40D2M10I1R0\generate\_PB\include\Can\_Cfg.h
  - ..\Can\_TS\_T40D2M10I1R0\generate\_PB\src\ Can\_PBcfg.c
  - ..\Can\_TS\_T40D2M10I1R0\generate\_PB\Can\_SourceClock.m
  - ..\Can\_TS\_T40D2M10I1R0\generate\_PB\Can\_VersionCheck\_Src\_PB.m
  - ..\Can\_TS\_T40D2M10I1R0\generate\_PB\Can\_NotifyCheck\_Src\_PB.m

#### **Steps to generate the configuration:**

- 1. Copy the module folders Can\_TS\_T40D2M10I1R0, CanIf\_TS\_T40D2M10I1R0, Base\_TS\_T40D2M10I1R0, Resource\_TS\_T40D2M10I1R0, EcuM\_TS\_T40D2M10I1R0, Mcu\_TS\_T40D2M10I1R0 into the Tresos plugins folder.
- 2. Set the desired Tresos Output location folder for the generated sources and header files.
- 3. Use the EB tresos Studio GUI to modify ECU configuration parameters values.
- 4. Generate the configuration files.

### **Dependencies**

- MCU is required to use System Clock when clock source is used as Peripheral clock source to generate CAN Segment values.
- **RESOURCE** is required to select processor derivative. Current Can driver has support for the following derivatives, everyone having attached a Resource file: s32k148\_lqfp144, s32k148\_lqfp176, s32k148\_mapbga100, s32k146\_lqfp144, s32k146\_lqfp100, s32k146\_lqfp64, s32k146\_mapbga100, s32k144\_lqfp100, s32k144\_lqfp64, s32k144\_lqfp64, s32k144\_lqfp64, s32k144\_lqfp64.
- **CANIF** is required for reporting status of some events.
- **ECUM** is required for selecting the reference to the wakeup source for every Can controller.
- **DET** is required for signaling the development error detection (parameters out of range, null pointers, etc).

## Chapter 4 Function calls to module

## 4.1 Function Calls during Start-up

The CAN module shall be initialized by Can\_Init() service call during the start-up. API service Can\_SetController\_Mode(Can\_Controller, CAN\_T\_START) shall be used for setting the CAN controller to running mode.

#### **Note**

Pin settings are not related to Can driver or plugin configuration. GPIO pins used for connection of CAN physical layer have to be properly assigned to the IPV\_FlexCAN module prior the CAN initialization.

## 4.2 Function Calls during Shutdown

The IPV\_FlexCAN IP has many Low Power Modes, with programmable wake up on bus activity.

#### Freeze Mode

This low power mode is entered when the HALT and FRZ bits in the MCR Register are asserted.

Module ignores the Rx input pin and drives the Tx pin as recessive, stops the prescaler, thus halting all CAN protocol activities and grants write access to the Error Counters Register (ECR), which is read-only in other modes.

Exit from this mode is done by negating the FRZ and HALT bits in the MCR Register or when the MCU is removed from Debug Mode

#### **Note**

It is not possible to exit from this mode by receiving a message on the Can bus.

#### Module Disable Mode

This low power mode is entered when the MDIS bit in the MCR Register is asserted.

Module shuts down the clocks to the CAN Protocol Interface and Message Buffer Management sub-modules.

Exit from this mode is done by negating the MDIS bit in the MCR Register.

#### **Note**

It is not possible to exit from this mode by receiving a message on the Can bus.

#### Stop Mode

This low power mode is entered when Stop Mode is requested at MCU level.

When in Stop Mode, the module puts itself in an inactive state and then informs the CPU that the clocks can be shut down globally.

Exit from this mode happens when the Stop Mode request is removed or when activity is detected on the CAN bus and the Self Wake Up mechanism is enabled.

#### Note

Note that wake-up from Stop Mode only works when both bits, SLF\_WAK and WAK\_MSK, are asserted. If interrupt for Wakeup is implemented in INTC, the interrupt handler can change the state of the controller to RUN mode if it is needed. When exit from this mode controller is usually in Freeze mode.

#### **Note**

Refer the Reference Manual if wakuep is supported by hardware (bits register are implemented and INTC has connected the CAN wakeup interrupt signal).

## 4.3 Function Calls during Wake-up

The controller can be wakeup by a message when it is in Sleep mode only if interrupt for wakeup is enabled or self wakeup mechanism is enabled.

CAN stack can be changed from SLEEP mode to STOP mode by calling the Can\_SetControllerMode(CAN\_T\_WAKEUP) service call.

#### **Note**

Refer the Reference Manual if wakeup is supported by hardware (bits register are implemented and INTC has connected the CAN wakeup interrupt signal).

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Function Calls during Wake-up

## **Chapter 5 Module requirements**

#### 5.1 Exclusive Areas

**CAN\_EXCLUSIVE\_AREA\_00** - Used in "Can\_DisableControllerInterrupts" function, to protect the variable for nesting level of enabling/disabling interrupts. Refer to CAN202 requirement.

**CAN\_EXCLUSIVE\_AREA\_01** - Used in "Can\_EnableControllerInterrupts" function, to protect the variable for nesting level of enabling/disabling interrupts. Refer to CAN202 requirement.

CAN\_EXCLUSIVE\_AREA\_02 - Used in "Can\_FlexCan\_Write" function to protect the operation for checking the status of MB and for reserving it as a free to use for transmission. If hardware transmit object is free the mutex for that HTH is set to "signaled". Between this verification and signal operation the protection must be applied. Refer to CAN212 requirement.

**CAN\_EXCLUSIVE\_AREA\_03** - Used in "Can\_FlexCan\_GotoFreezeMode" function to protect the operation which putting controller into Freeze Mode. In this mode, no transmission or reception of frames is done and synchronicity to the CAN bus is lost.

**CAN\_EXCLUSIVE\_AREA\_04** - Used in "Can\_FlexCan\_ResetController" function to protect the operation for enabling the CAN module and requesting a Soft Reset.

**CAN\_EXCLUSIVE\_AREA\_05** - Used in "Can\_FlexCan\_InitController" function, to protect the operation which shall initialize all CAN controllers according to their configuration.

#### **CAN\_EXCLUSIVE\_AREA\_06** - Used in a sub-function of

"Can\_FlexCan\_SetControllerToStopMode" function, to protect the operation to transient all controllers into Stop Mode which puts itself in an inactive state and then informs the CPU that the clocks can be shut down globally.

#### **Exclusive Areas**

#### **CAN\_EXCLUSIVE\_AREA\_07** - Used in a sub-function of

"Can\_FlexCan\_DisableInterrupts" function, to protect the operation for disabling interrupts of the Can module.

#### CAN\_EXCLUSIVE\_AREA\_08 - Used in a sub-function of

"Can\_FlexCan\_EnableInterrupts" function, to protect the operation for enabling interrupts of the Can module.

#### CAN\_EXCLUSIVE\_AREA\_09 - Used in a sub-function of

"Can\_FlexCan\_SellectClockSource" function, to protect the operation for selecting the clock source to the CAN Protocol Engine (PE) to be either the peripheral clock or the oscillator clock

**CAN\_EXCLUSIVE\_AREA\_11** - Used in a sub-function of "Can\_FlexCan\_InitRxFiFo" function to protect the operation for configurating ID Acceptance Mode of the Rx FIFO ID Filter Table elements.

#### CAN EXCLUSIVE AREA 13 - Used in a sub-function of

"Can\_FlexCan\_SetControllerToStartMode" function to protect the operation which transient Controller to Start Mode.

#### **Critical Region Exclusive Matrix**

Below is the table depicting the exclusivity between different critical region IDs from the Can driver. If there is an "X" in a table, it means that those 2 critical regions cannot interrupt each other.

The critical regions from interrupts are grouped in "Interrupt Service Routines Critical Regions (composed diagram)". If an exclusive area is "exclusive" with the composed "Interrupt Service Routines Critical Regions (composed diagram)" group, it means that it is exclusive with each one of the ISR critical regions.

CAN Interru **EXCL EXCL EXCL** pt USIVE Servic ARE A\_00 A\_01 A\_02 A\_03 A\_04 A\_05 A\_06 A\_07 A\_08 A\_09 A\_11 A\_13 Routin es Critica Regio ns CAN\_E Χ **XCLUS** IVE\_A REA\_0

**Table 5-1. Exclusive Areas** 

Table continues on the next page...

Table 5-1. Exclusive Areas (continued)

	CAN_ EXCL USIVE _ARE A_00	CAN_ EXCL USIVE _ARE A_01	CAN_ EXCL USIVE _ARE A_02	CAN_ EXCL USIVE _ARE A_03	CAN_ EXCL USIVE _ARE A_04	CAN_ EXCL USIVE _ARE A_05	CAN_ EXCL USIVE _ARE A_06	CAN_ EXCL USIVE _ARE A_07	CAN_ EXCL USIVE _ARE A_08	CAN_ EXCL USIVE _ARE A_09	CAN_ EXCL USIVE _ARE A_11	CAN_ EXCL USIVE _ARE A_13	Interru pt Servic e Routin es Critica I Regio ns
CAN_E XCLUS IVE_A REA_0 1	x												
CAN_E XCLUS IVE_A REA_0 2						Х							
CAN_E XCLUS IVE_A REA_0 3					X	X	X			X	Х	X	
CAN_E XCLUS IVE_A REA_0 4				X		X	Х			X	Х	X	
CAN_E XCLUS IVE_A REA_0 5			Х	Х	Х		Х			Х	Х	Х	
CAN_E XCLUS IVE_A REA_0 6				Х	Х	Х		Х	Х	Х	Х	Х	
CAN_E XCLUS IVE_A REA_0 7							Х		Х	Х			
CAN_E XCLUS IVE_A REA_0 8							Х	X		X			

Table continues on the next page...

**Table 5-1. Exclusive Areas (continued)** 

	CAN_ EXCL USIVE _ARE A_00	CAN_ EXCL USIVE _ARE A_01	CAN_ EXCL USIVE _ARE A_02	CAN_ EXCL USIVE _ARE A_03	CAN_ EXCL USIVE _ARE A_04	CAN_ EXCL USIVE _ARE A_05	CAN_ EXCL USIVE _ARE A_06	CAN_ EXCL USIVE _ARE A_07	CAN_ EXCL USIVE _ARE A_08	CAN_ EXCL USIVE _ARE A_09	CAN_ EXCL USIVE _ARE A_11	CAN_ EXCL USIVE _ARE A_13	Interru pt Servic e Routin es Critica I Regio ns
CAN_E XCLUS IVE_A REA_0 9				Х	Х	Х	Х	Х	Х		Х	Х	
CAN_E XCLUS IVE_A REA_1 1				X	х	X	х			х		X	
CAN_E XCLUS IVE_A REA_1 3				X	Х	X	Х			Х	X		
Interrup t Service Routine s Critical Region s													

## 5.2 Peripheral Hardware Requirements

The CAN physical interface should be connected to the CAN module pins in order to get the CAN bus voltage levels.

There have to be another one CAN node present on the CAN bus in order to get the CAN bus synchronized.

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### 5.3 ISR Macro

MCAL drivers use the ISR macro to define the functions that will process hardware interrupts. Depending on whether the OS is used or not, this macro can have different definitions:

- a. OS is not used AUTOSAR\_OS\_NOT\_USED is defined:
- i. If USE\_SW\_VECTOR\_MODE is defined:

```
#define ISR(IsrName) void IsrName(void)
```

In this case, drivers' interrupt handlers are normal C functions and the prolog/epilog handle the context save and restore.

ii. If USE\_SW\_VECTOR\_MODE is not defined:

```
#define ISR(IsrName) INTERRUPT FUNC void IsrName(void)
```

In this case, drivers' interrupt handlers must save and restore the execution context.

NXP Semiconductors OS is used - AUTOSAR\_OS\_NOT\_USED is not defined

```
#define ISR(IsrName) void OS_isr_##IsrName()
```

In this case, OS is handling the execution context when an interrupt occurs. Drivers' interrupt handlers are normal C functions.

Other vendor's OS is used - AUTOSAR\_OS\_NOT\_USED is not defined. Please refer to the OS documentation for description of the ISR macro.

## 5.4 Other AUTOSAR Modules - Dependencies

- Base: The Base module contains the common files/definitions needed by all MCAL modules.
- **Mcu:** The Mcu driver provides services for basic microcontroller initialization, power down functionality, reset and microcontroller specific functions required by other MCAL software modules. The clocks need to be initialized prior to using the Can driver. The clock frequency may affect the Can bit rate, the transmitting or receiving a Can frame process.
- **Port:** The Port module is used to configure the port pins with the needed modes, before they are used by the Can module.
- **EcuC**: (only for ASR 4.2) The ECUC module is used for ECU configuration. Can modules need ECUC to retrieve the variant information.

#### **Data Cache Restriction**

- **Det** The Det module is used for enabling Development error detection. The API function used is Det\_ReportError(). The activation / deactivation of Development error detection is configurable using the 'CanDevErrorDetect' configuration parameter.
- **Resource:** Sub-Derivative model is selected from Resource configuration.
- **Rte:** The Rte module is needed for implementing data consistency of exclusive areas that are used by Can module.
- **EcuM:** This module is used for processing the Wakeup notifications of CAN. Whenever the module is in 'Sleep' mode and a wakeup event occurs, it is reported to EcuM through the EcuM\_CheckWakeupEvent() API.

#### 5.5 Data Cache Restriction

In the DMA transfer mode, DMA transfers may issue cache coherency problems. To avoid possible coherency issues when D-CACHE is enabled, the user shall ensure that the buffers used as TCD source and destination are allocated in the NON-CACHEABLE area (by means of Memmap).

## 5.6 User Mode Support Requirement

The Can module can be run in **user mode** if the following steps are performed:

- Enable CanEnabelUserModeSupport from the configuration
- Call the following function as trusted functions:
  - Can\_FlexCan\_Init() to configure the CAN\_MCR register which is only able to be accessed in the Supervisor mode.
  - Can\_FlexCan\_SetControllerMode() to configure the CAN\_MCR register for setting the Can controller mode.
  - Can\_FlexCan\_ChangeBaudrate() to configure the CAN\_MCR register for change the Can baudrate.
  - Can\_FlexCan\_MainFunctionMode() to read the CAN\_MCR register for checking the state transition of the CAN controller.
  - Can\_FlexCan\_SetIcomConfig() to configure the CAN\_MCR register for the Icom (Pretended Networking) configuration
  - Can\_FlexCan\_DeactivateIcom() to configure the CAN\_MCR register for deactivate Icom.
  - Can\_FlexCan\_ResetController() to set CAN\_MCR[SOFTRST] bit for the Soft reset controller request

## **Chapter 6 Main API Requirements**

### 6.1 Main Functions Calls within BSW Scheduler

CAN Driver support 4 main functions that can be configured to be scheduled by BSW scheduler:

```
    FUNC (void, CAN_CODE) Can_MainFunction_Write( void )
    FUNC (void, CAN_CODE) Can_MainFunction_Read( void )
    FUNC (void, CAN_CODE) Can_MainFunction_BusOff( void )
    FUNC (void, CAN_CODE) Can_MainFunction_Mode( void )
```

These Autosar APIs are scheduled if these 3 events are configured to be in "Polling" mode by the following parameters:

• CanTxProcessing

```
#define CAN TXPOLL SUPPORTED (STD ON)
```

• CanRxProcessing

```
#define CAN_RXPOLL_SUPPORTED (STD_ON)
```

• CanBusoffProcessing

```
#define CAN_BUSOFFPOLL_SUPPORTED (STD_ON)
```

The period for polling is configured by the following 4 parameters:

CanMainFunctionWritePeriod

```
#define CAN_MAINFUNCTION_PERIOD_WRITE (uint32)0.0010U
```

CanMainFunctionReadPeriod

#### **API Requirements**

#define CAN MAINFUNCTION PERIOD READ (uint32)0.0010U

CanMainFunctionBusoffPeriod

#define CAN MAINFUNCTION PERIOD BUSOFF (uint32)0.0010U

CanMainFunctionModePeriod

#define CAN MAINFUNCTION MODE PERIOD (uint32)0.0010U

#### **Note**

A configuration for an hardware unit can be possible in such a way that one controller will handle events by interrupts and another by polling method.

## 6.2 API Requirements

CAN360,CAN361,CAN362,CAN363,CAN228,CAN112,CAN185,CAN186,CAN235,

## 6.3 Calls to Notification Functions, Callbacks, Callouts

#### **Call-back Notifications**

The CAN stack provides the following call-back notifications:

• CanIf\_TxConfirmation: This CAN Interface call-back function is called when a CAN message has been transmitted.

```
FUNC (void, CAN_CODE) CanIf_TxConfirmation(PduIdType CanTxPduId)
```

 CanIf\_RxIndication: This CAN Interface call-back function is called when valid CAN message is received.

```
FUNC (void, CAN_CODE) CanIf_RxIndication(uint8 Hrh, Can_IdType CanId, uint8 CanDlc,
uint8Ptr CanSduPtr)
```

• CanIf\_CancelTxConfirmation: This CAN Interface call-back function is called when the CAN message has been canceled during the transmission.

```
FUNC (void, CAN_CODE) CanIf_CancelTxConfirmation(const Can_PduType * PduInfoPtr)
```

• CanIf\_ControllerBusOff: This CAN Interface call-back function is called when the CAN controller reached the bus-off state (see CAN specification for further details).

FUNC (void, CAN\_CODE) CanIf\_ControllerBusOff(uint8 Controller)

## **User Notification**

• None

Calls to Notification Functions, Callbacks, Callouts

## **Chapter 7 Memory Allocation**

## 7.1 Sections to be defined in MemMap.h

Table 7-1. Sections to be defined in MemMap.h

Section name	Type of section	Description
CAN_START_SEC_CONFIG_DATA_UNSPECIFIED	Configuration Data	Start of Memory Section for Config Data
CAN_STOP_SEC_CONFIG_DATA_UNSPECIFIED	Configuration Data	End of Memory Section for Config Data
CAN_START_SEC_CODE	Code	Start of memory Section for Code
CAN_STOP_SEC_CODE	Code	End of memory Section for Code
CAN_START_SEC_VAR_INIT_UNSPECIFIED	Variables	Used for variables, structures, arrays, when the SIZE (alignment) does not fit the criteria of 8,16 or 32 bit. These variables are initialized with values after every reset.
CAN_STOP_SEC_VAR_INIT_UNSPECIFIED	Variables	End of above section.
CAN_START_SEC_VAR_NO_INIT_UNSPECIFIED	Variables	These variables are never cleared and never initialized by start-up code.
CAN_STOP_SEC_VAR_NO_INIT_UNSPECIFIED	Variables	End of above section.
CAN_START_SEC_CONST_UNSPECIFIED	Constant Data	Used for constants
CAN_STOP_SEC_CONST_UNSPECIFIED	Constant Data	End of above section.

## 7.2 Linker command file

Memory shall be allocated for every section defined in CAN\_MemMap.h

Linker command file

## **Chapter 8 Configuration parameters considerations**

Configuration parameter class for Autosar CAN driver fall into the following variants as defined below:

## 8.1 Configuration Parameters

Specifies whether the configuration parameter shall be of configuration class Post Build

**Table 8-1. Configuration Parameters** 

Configuration Container	Configuration Parameters	Configuration Variant	Current Implementation
Can	IMPLEMENTATION_CONFIG _VARIANT	Pre Compile parameter for all Variants of Configuration	Pre compile
CanGeneral			
	CanDevErrorDetection	Pre Compile parameter for all Variants of Configuration	Pre compile
	CanVersionInfoApi	Pre Compile parameter for all Variants of Configuration	Pre compile
	CanIndex	Pre Compile parameter for all Variants of Configuration	Pre compile
	CanMainFunctionBusoffPerio d	Pre Compile parameter for all Variants of Configuration	Pre compile
	CanMultiplexedTransmission	Pre Compile parameter for all Variants of Configuration	Pre compile
	CanHardwareCancellation	Pre Compile parameter for all Variants of Configuration	Pre compile
	CanIdenticalIdCancellation	Pre Compile parameter for all Variants of Configuration	Pre compile
	CanTimeoutDuration	Pre Compile parameter for all Variants of Configuration	Pre compile
	CanLPduReceiveCalloutFunct ion	Pre Compile parameter for all Variants of Configuration	Pre compile
	CanCounterRef	Pre Compile parameter for all Variants of Configuration	Pre compile

Table continues on the next page...

#### **Configuration Parameters**

**Table 8-1. Configuration Parameters (continued)** 

Configuration Parameters	Configuration Variant	Current Implementation
CanMainFunctionReadPeriod Ref	Pre Compile parameter for all Variants of Configuration	Pre compile
CanMainFunctionWritePeriod Ref	Pre Compile parameter for all Variants of Configuration	Pre compile
CanSupportTTCANRef	Pre Compile parameter for all Variants of Configuration	Pre compile
CanCodeSizeOptimization	Pre Compile parameter for all Variants of Configuration	Pre compile
CanExtendedIDSupport	Pre Compile parameter for all Variants of Configuration	Pre compile
CanMBCountExtensionSuppo	Pre Compile parameter for all Variants of Configuration	Pre compile
CanApiEnableMbAbort	Pre Compile parameter for all Variants of Configuration	Pre compile
CanEnableDualClockMode	Pre Compile parameter for all Variants of Configuration	Pre compile
CanChangeBaudrateApi	Pre Compile parameter for all Variants of Configuration	Pre compile
CanMainFunctionReadPeriod	Pre Compile parameter for all Variants of Configuration	Pre compile
CanMainFunctionWritePeriod	Pre Compile parameter for all Variants of Configuration	Pre compile
CanHwChannel	Pre Compile parameter for all Variants of Configuration	Pre compile
CanControllerActivation	Pre Compile parameter for all Variants of Configuration	Pre compile
CanControllerBaseAddress	Pre Compile parameter for all Variants of Configuration	Pre compile
CanControllerId	Pre Compile parameter for all Variants of Configuration	Pre compile
CanRxProcessing	Pre Compile parameter for all Variants of Configuration	Pre compile
CanTxProcessing	Pre Compile parameter for all Variants of Configuration	Pre compile
CanBusoffProcessing	Pre Compile parameter for all Variants of Configuration	Pre compile
CanBccSupport	Pre Compile parameter for all Variants of Configuration	Pre compile
CanListenOnlyMode	VariantPC or VariantPB	VariantPC or VariantPB
CanLoopBackMode	VariantPC or VariantPB	VariantPC or VariantPB
CanSoftwareBusOffRecovery	VariantPC or VariantPB	VariantPC or VariantPB
CanAutoBusOffRecovery	VariantPC or VariantPB	VariantPC or VariantPB
	CanMainFunctionReadPeriod Ref CanMainFunctionWritePeriod Ref CanSupportTTCANRef CanCodeSizeOptimization CanExtendedIDSupport CanMBCountExtensionSupport CanApiEnableMbAbort CanEnableDualClockMode CanChangeBaudrateApi  CanMainFunctionReadPeriod CanMainFunctionWritePeriod  CanHwChannel CanControllerActivation  CanControllerBaseAddress  CanControllerId  CanRxProcessing  CanBusoffProcessing  CanBusoffProcessing  CanLoopBackMode CanLoopBackMode CanLoopBackMode CanSoftwareBusOffRecovery	CanMainFunctionReadPeriod Ref Variants of Configuration  CanMainFunctionWritePeriod Ref Variants of Configuration  CanSupportTTCANRef Pre Compile parameter for all Variants of Configuration  CanCodeSizeOptimization Pre Compile parameter for all Variants of Configuration  CanExtendedIDSupport Pre Compile parameter for all Variants of Configuration  CanMBCountExtensionSupport Pre Compile parameter for all Variants of Configuration  CanApiEnableMbAbort Pre Compile parameter for all Variants of Configuration  CanEnableDualClockMode Pre Compile parameter for all Variants of Configuration  CanCanCanCanCanCanCanCanCanCanCanCanCanC

Table continues on the next page...

**Table 8-1. Configuration Parameters (continued)** 

Configuration Container	Configuration Parameters	Configuration Variant	Current Implementation
	CanTrippleSamplingEnable	VariantPC or VariantPB	VariantPC or VariantPB
	CanLowestBuffTransmitFirst	VariantPC or VariantPB	VariantPC or VariantPB
	CanLocalPriorityEn	VariantPC or VariantPB	VariantPC or VariantPB
	CanWarningEnable	VariantPC or VariantPB	VariantPC or VariantPB
	CanClockFromBus	Pre Compile parameter for all Variants of Configuration	Pre compile
	CanCpuClockRef	Pre Compile parameter for all Variants of Configuration	Pre compile
	CanCpuClockRef_Alternate	Pre Compile parameter for all Variants of Configuration	Pre compile
	CanControllerRxFifoEnable	Pre Compile parameter for all Variants of Configuration	Pre compile
	CanRxFifoWarningNotification	Pre Compile parameter for all Variants of Configuration	Pre compile
	CanRxFifoOverflowNotificatio n	Pre Compile parameter for all Variants of Configuration	Pre compile
	CanErrorControllerNotifEn	Pre Compile parameter for all Variants of Configuration	Pre compile
	CanErrorControllerNotification	Pre Compile parameter for all Variants of Configuration	Pre compile
	CanControllerDefaultBaudrate	Pre Compile parameter for all Variants of Configuration	Pre compile
CanController \CanControllerBaudRate			
	CanControllerBaudRate	VariantPC or VariantPB	VariantPC or VariantPB
	CanControllerCheckCanStand ard	VariantPC or VariantPB	VariantPC or VariantPB
	CanControllerPropSeg	VariantPC or VariantPB	VariantPC or VariantPB
	CanControllerSeg1	VariantPC or VariantPB	VariantPC or VariantPB
	CanControllerSeg2	VariantPC or VariantPB	VariantPC or VariantPB
	CanSyncJumpWidth	VariantPC or VariantPB	VariantPC or VariantPB
	CanAdvancedSetting	VariantPC or VariantPB	VariantPC or VariantPB
	CanBusLength	VariantPC or VariantPB	VariantPC or VariantPB
	CanPropDelayOfTranceiver	VariantPC or VariantPB	VariantPC or VariantPB
	CanControllerPrescaller	VariantPC or VariantPB	VariantPC or VariantPB
	CanControllerPrescaller_Alt	VariantPC or VariantPB	VariantPC or VariantPB
CanController\CanRxFifo			
	CanControllerIDAcceptanceM ode	VariantPC or VariantPB	VariantPC or VariantPB
	CanIDValue0	VariantPC or VariantPB	VariantPC or VariantPB
	CanIDValue1	VariantPC or VariantPB	VariantPC or VariantPB
	CanIDValue2	VariantPC or VariantPB	VariantPC or VariantPB
	CanIDValue3	VariantPC or VariantPB	VariantPC or VariantPB

Table continues on the next page...

### **Configuration Parameters**

**Table 8-1. Configuration Parameters (continued)** 

		г	1
Configuration Container	Configuration Parameters	Configuration Variant	Current Implementation
	CanTableIDType	VariantPC or VariantPB	VariantPC or VariantPB
	CanMBFilterMaskValue	VariantPC or VariantPB	VariantPC or VariantPB
	CanRxFifoFiltersNumber	VariantPC or VariantPB	VariantPC or VariantPB
	CanRxFifoGlobalMaskValue	VariantPC or VariantPB	VariantPC or VariantPB
CanController\CanFilterMask			
	CanFilterMaskValue	VariantPC or VariantPB	VariantPC or VariantPB
CanController CanHardwareObject			
	CanHandleType	VariantPC or VariantPB	VariantPC or VariantPB
	CanldType	VariantPC or VariantPB	VariantPC or VariantPB
	CanldValue	VariantPC or VariantPB	VariantPC or VariantPB
	CanMBPrio	VariantPC or VariantPB	VariantPC or VariantPB
	CanObjectId	VariantPC or VariantPB	VariantPC or VariantPB
	CanObjectType	VariantPC or VariantPB	VariantPC or VariantPB
	CanControllerRef	VariantPC or VariantPB	VariantPC or VariantPB
	CanFilterMaskRef	VariantPC or VariantPB	VariantPC or VariantPB
	CanMainFunctionRWPeriodR ef	VariantPC or VariantPB	VariantPC or VariantPB
CanController CanTTController			
	CanTTControllerApplWatchdo gLimit	VariantPC or VariantPB	VariantPC or VariantPB
	CanTTControllerCycleCountM ax	VariantPC or VariantPB	VariantPC or VariantPB
	CanTTControllerExpectedTxT rigger	VariantPC or VariantPB	VariantPC or VariantPB
	CanTTControllerExternalCloc kSynchronisation	VariantPC or VariantPB	VariantPC or VariantPB
	CanTTControllerGlobalTimeFi Itering	VariantPC or VariantPB	VariantPC or VariantPB
	CanTTControllerInitialRefOffs et	VariantPC or VariantPB	VariantPC or VariantPB
	CanTTControllerInterruptEnab le	VariantPC or VariantPB	VariantPC or VariantPB
	CanTTControllerLevel2	VariantPC or VariantPB	VariantPC or VariantPB
	CanTTControllerNTUConfig	VariantPC or VariantPB	VariantPC or VariantPB
	CanTTControllerOperationMo de	VariantPC or VariantPB	VariantPC or VariantPB
	CanTTControllerSyncDeviation	VariantPC or VariantPB	VariantPC or VariantPB
	CanTTControllerTURRestore	VariantPC or VariantPB	VariantPC or VariantPB
	CanTTControllerTimeMaster	VariantPC or VariantPB	VariantPC or VariantPB

Table continues on the next page...

### **Chapter 8 Configuration parameters considerations**

# **Table 8-1. Configuration Parameters (continued)**

Configuration Container	Configuration Parameters	Configuration Variant	Current Implementation
	CanTTControllerTimeMasterP riority	VariantPC or VariantPB	VariantPC or VariantPB
	CanTTControllerTxEnableWin dowLength	VariantPC or VariantPB	VariantPC or VariantPB
	CanTTControllerWatchTrigger GapTimeMark	VariantPC or VariantPB	VariantPC or VariantPB
	CanTTControllerWatchTrigger TimeMark	VariantPC or VariantPB	VariantPC or VariantPB
	CanTTIRQProcessing	VariantPC or VariantPB	VariantPC or VariantPB

**Configuration Parameters** 

# **Chapter 9 Integration Steps**

This section gives a brief overview of the steps needed for integrating Controller Area Network:

- Generate the required CAN configurations. For more details refer to section
- Allocate proper memory sections in CAN\_MemMap.h and linker command file. For more details refer to section
- Compile & build the CAN with all the dependent modules. For more details refer to section Building the Driver

# **Chapter 10 External Assumptions for CAN driver**

The section presents requirements that must be complied with when integrating CAN driver into the application.

#### [SMCAL\_CPR\_EXT4]

<< The external application shall call Can\_Init function only when the driver state is CAN\_UNINIT and the state of all controllers is UNINIT. >>

#### [SMCAL\_CPR\_EXT5]

<< The external application shall call Can\_MainFunction\_Write only after driver initialization. >>

# [SMCAL\_CPR\_EXT6]

<< The external application shall call Can\_MainFunction\_Read only after driver initialization. >>

# [SMCAL\_CPR\_EXT7]

<< The external application shall call Can\_MainFunction\_BusOff only after driver initialization. >>

# [SMCAL\_CPR\_EXT8]

<< The external application shall call Can\_MainFunction\_Wakeup only after driver initialization. >>

# [SMCAL\_CPR\_EXT9]

<< The external application shall call Can\_SetControllerMode only after driver initialization >>

#### [SMCAL\_CPR\_EXT10]

<< The external application shall call Can\_DisableControllerInterrupts function only after driver initialization. >>

#### [SMCAL\_CPR\_EXT11]

<< The external application shall call Can\_EnableControllerInterrupts function only after driver initialization. >>

#### [SMCAL\_CPR\_EXT12]

<< The external application shall call Can\_Write function only after driver initialization. >>

#### [SMCAL\_CPR\_EXT13]

<< The external application shall assure that Can\_Init does not preempt and is not preempted by any other CAN driver API excepting Can\_GetVersionInfo.

The external application shall assure that Can\_Init does not preempt itself. >>

# [SMCAL\_CPR\_EXT14]

<< The external application shall assure that Can\_MainFunction\_Write does not preempt and is not preempted by any other CAN driver API exceptin Can\_GetVersionInfo.The external application shall assure that Can\_MainFunction\_Write does not preempt itself. >>

# [SMCAL\_CPR\_EXT15]

<< The external application shall assure that Can\_MainFunction\_Read does not preempt and is not preempted by any other CAN driver API exceptin Can\_GetVersionInfo.The external application shall assure that Can\_MainFunction\_Read does not preempt itself. >>

# [SMCAL\_CPR\_EXT16]

<< The external application shall assure that Can\_MainFunction\_BusOff does not preempt and is not preempted by any other CAN driver API exceptin Can\_GetVersionInfo.The external application shall assure that Can\_MainFunction\_BusOff does not preempt itself. >>

#### [SMCAL\_CPR\_EXT17]

<< The external application shall assure that Can\_SetControllerMode does not preempt and is not preempted by any other CAN driver API using the same controller parameter.

The external application shall assure that Can\_SetControllerMode does not preempt itself. >>

#### [SMCAL\_CPR\_EXT18]

<< The external application shall assure that Can\_DisableControllerInterrupts does not preempt and is not preempted by any other CAN driver API using the same controller parameter. >>

#### [SMCAL\_CPR\_EXT19]

<< The external application shall assure that Can\_EnableControllerInterrupts does not preempt and is not preempted by any other CAN driver API using the same controller parameter. >>

# [SMCAL\_CPR\_EXT20]

<< The external application shall assure that Can\_Write does not preempt and is not preempted by any other CAN driver API using the same controller as the hardware handle parameter. The external application shall assure that Can\_Write does not preempt itself for the same hardware handle parameter. >>

# [SMCAL CPR EXT21]

<< The external application shall call Can\_ChangeBaudrate only when the CAN controller is in state STOPPED. >>

# [SMCAL\_CPR\_EXT22]

<< The external application shall call Can\_Init function only when the driver state is CAN\_UNINIT and the state of all controllers is UNINIT. >>

#### [SMCAL\_CPR\_EXT23]

<< The external application shall assure that Can\_CheckWakeup does not preempt and is not preempted by any other CAN driver API using the same controller parameter. The external application shall assure that Can\_CheckWakeup does not preempt itself. >>

#### [SMCAL\_CPR\_EXT24]

<< The external application shall call Can\_CheckWakeup function only after driver initialization. >>

#### [SMCAL\_CPR\_EXT25]

<< The external application shall call Can\_MainFunction\_Mode function only after driver initialization. >>

#### [SMCAL\_CPR\_EXT26]

<< The external application shall call Can\_Init function only when the driver state is CAN\_UNINIT and the state of all controllers is UNINIT. >>

# [SMCAL\_CPR\_EXT27]

<< The external application shall call Can\_SetControllerMode(CAN\_T\_START) only when the CAN controller is in state STOPPED. >>

# [SMCAL\_CPR\_EXT28]

<< The external application shall call Can\_SetControllerMode(CAN\_T\_STOP) only when the CAN controller is in state STARTED or STOPPED. >>

# [SMCAL\_CPR\_EXT29]

<< The external application shall call Can\_SetControllerMode(CAN\_T\_SLEEP) only when the CAN controller is in state SLEEP or STOPPED. >>

# [SMCAL\_CPR\_EXT30]

<< The external application shall call Can\_SetControllerMode(CAN\_T\_WAKEUP) only when the CAN controller is in state SLEEP or STOPPED. >>

#### [SMCAL\_CPR\_EXT31]

<< The external application shall assure that Can\_ChangeBaudrate does not preempt and is not preempted by any other CAN driver API using the same controller parameter. The external application shall assure that Can\_ChangeBaudrate does not preempt itself. The external application shall call Can\_ChangeBaudrate only when the controller parameter is STOPPED. >>

#### [SMCAL\_CPR\_EXT32]

<< The external application shall call Can\_ChangeBaudrate only after driver initialization and when the configured controller is in the STOPPED state. >>

#### [SMCAL\_CPR\_EXT33]

<< The external application shall assure that Can\_CheckBaudrate does not preempt and is not preempted by any other CAN driver API using the same controller parameter. >>

#### [SMCAL\_CPR\_EXT34]

<< The external application shall call Can\_CheckBaudrate only after driver initialization. >>

# [SMCAL\_CPR\_EXT163]

<< If interrupts are locked a centralized function pair to lock and unlock interrupts shall be used. >>

# [CAN023]

<< The consistency of the configuration must be checked by the configuration tool(s). >>

# [CAN024]

<< The valid values that can be configured are hardware dependent. Therefore the rules and constraints can't be given in the standard. The configuration tool is responsible to do a static configuration checking, also regarding dependencies between modules (i.e. Port driver, MCU driver etc.) >>

# [CAN039]

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<< Can\_ReturnType

Return values of CAN driver API

CAN\_OK

CAN\_NOT\_OK

CAN\_BUSY >>

#### **NOTE**

defined into Can\_GeneralTypes.h included into Base module

#### [CAN240]

<< The Mcu module (SPAL see [8]) shall configure register settings that are 'shared' with other modules. >>

#### NOTE

not a requirement for CAN module

#### [CAN285]

<< Transmit cancellation may only be used when transmit buffers are enabled inside the CanIf module. >>

#### NOTE

This is a specification for CanIf module.Safety manual

# [CAN288]

<< The TX request for the new L-PDU shall be repeated by the CanIf module, inside the notification function CanIf CancelTxConfirmation.

Implementation note:

For sequence relevant streams the sender must assure that the next transmit request for the same CAN ID is only initiated after the last request was confirmed. >>

#### NOTE

This is a specification for CanIf module. Safety manual.

# [CAN415]

<< Can\_PduType

This type is used to provide ID, DLC and SDU from CAN interface to CAN driver. >>

#### **NOTE**

defined into Can\_GeneralTypes.h included into Base module

#### [CAN416]

<< Can\_IdType

Represents the Identifier of an L-PDU. For extended IDs the most significant bit is set. >>

#### **NOTE**

defined into Can\_GeneralTypes.h included into Base module

#### [CAN417]

<< Can\_StateTransitionType

CAN\_T\_START: CAN controller transition value to request state STARTED.

CAN\_T\_STOP: CAN controller transition value to request state STOPPED.

CAN\_T\_SLEEP: CAN controller transition value to request state SLEEP.

CAN\_T\_WAKEUP: CAN controller transition value to request state STOPPED from state SLEEP.

State transitions that are used by the function CAN\_SetControllerMode >>

#### **NOTE**

defined into Can\_GeneralTypes.h included into Base module

# [CAN429]

<< Can\_HwHandleType

Represents the hardware object handles of a CAN hardware unit. For CAN hardware units with more than 255 HW objects use extended range. >>

#### NOTE

defined into Can\_GeneralTypes.h included into Base module

# [CAN436]

Integration Manual, Rev. 1.0

<< Can\_GeneralTypes.h shall contain all types and constants that are shared among the AUTOSAR CAN modules Can, CanIf and CanTrev. >>

#### **NOTE**

Implemented in base

# [CAN437]

<< The integrator of the Can modules shall provide the file Can\_GeneralTypes.h. >>

# [CAN438]

<< The content of Can\_GeneralTypes.h consists of types specified within [5] and the following type specifications within this document except Can\_ConfigType. >>

#### **NOTE**

Implemented in base

# [CAN455]

<< The service Can\_CheckBaudrate(Controller, Baudrate) shall be called by CanIf\_CheckBaudrate() for the requested CAN controller. >>

#### NOTE

This is a requirement for CanIf

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Document Number IM2CANASR4.2 Rev0002R1.0.1 Revision 1.0



