

[illegible]

The diagram shows two NPN transistors, Q1 and Q2, both labeled MMBT2222A. The base of Q1 is connected to the DTR pin through a 4.7k resistor (R8). The base of Q2 is connected to the RTS pin through a 4.7k resistor (R7). The emitter of both transistors is connected to ground. The collector of Q1 is connected to the ESP32_EN pin, and the collector of Q2 is connected to the ESP32_BOOT pin. The pins are numbered 1, 2, and 3 for each transistor.

The diagram shows a 3.3V supply connected to a J2 connector. The pins are labeled as follows: 1 (+3.3V), 2 (GND1), 3 (ESP32_RX), 4 (ESP32_TX), 5 (ESP32_BOOT), and 6 (ESP32_EN). A switch SW1 is connected between pin 6 and GND1, with a 10µF capacitor C8 in parallel with the switch.

Rev: v1
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