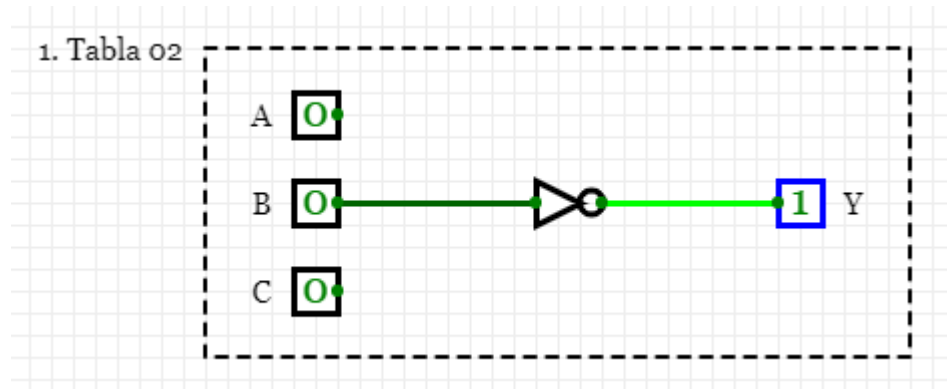
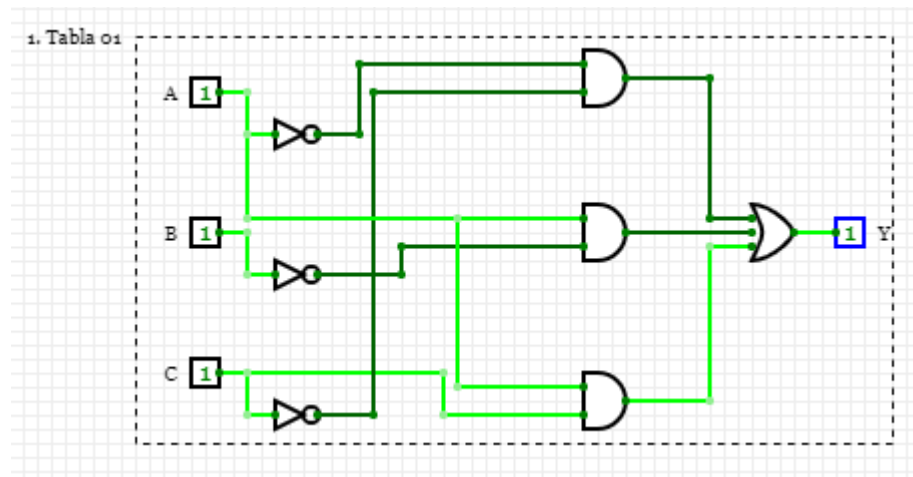
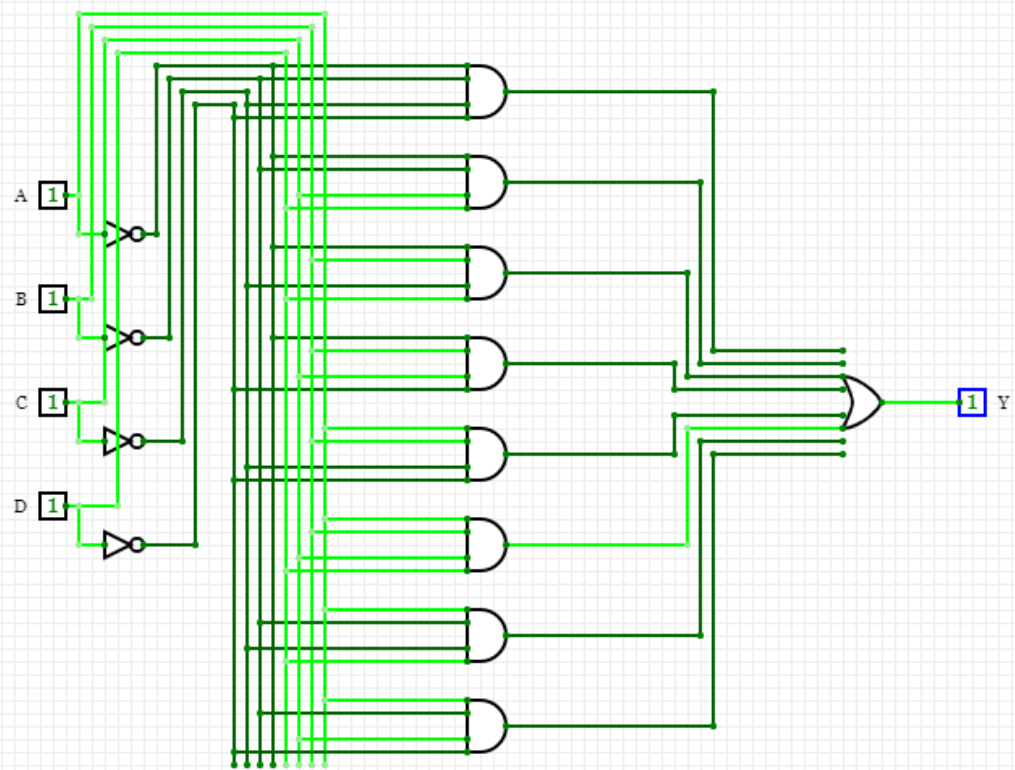


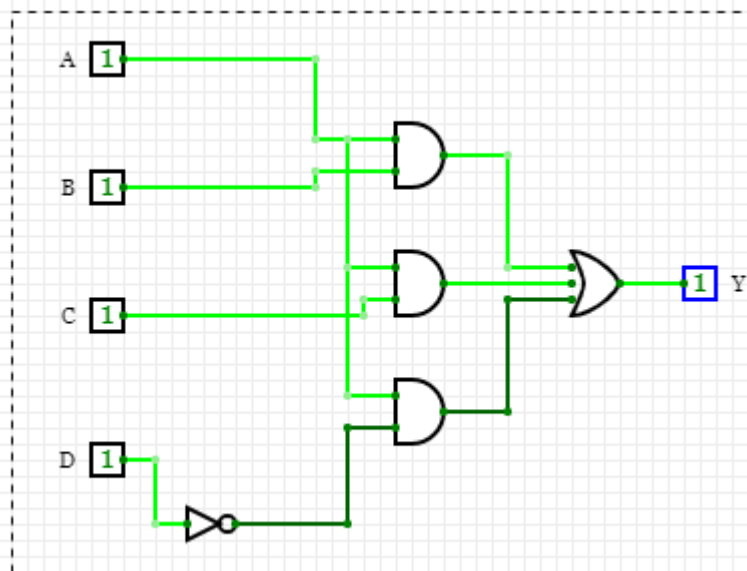
Circuitos Ejercicios 1, 2 y 5



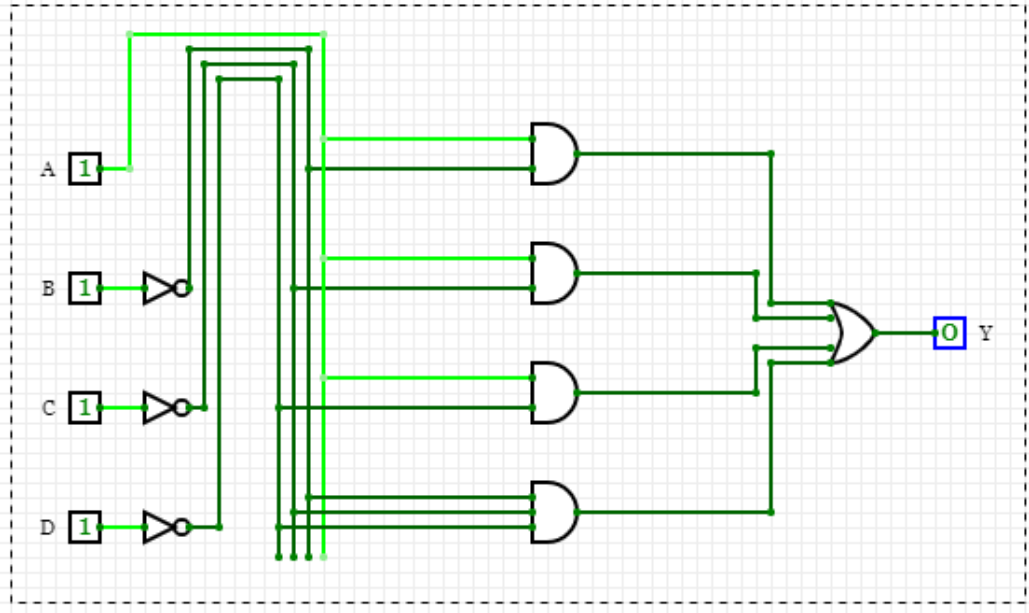
1. Tabla 03



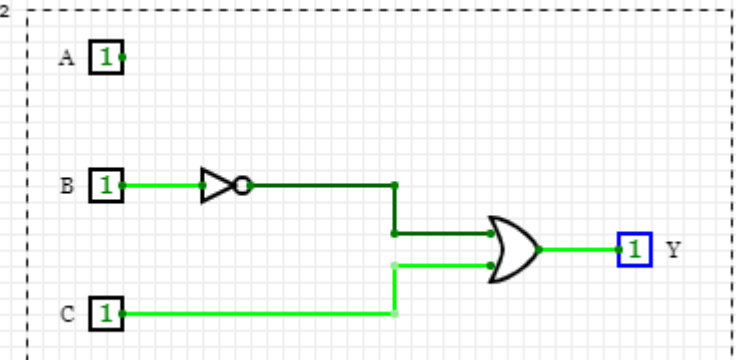
1. Tabla 04



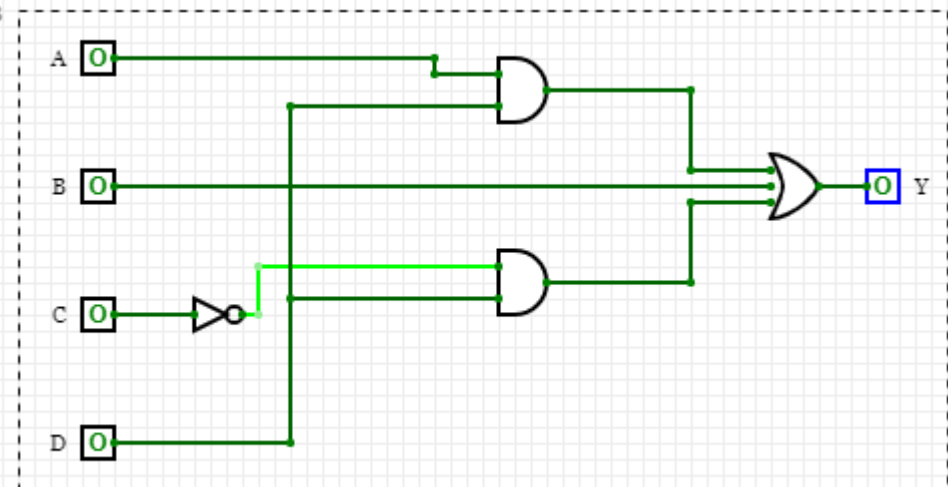
2. Tabla 01



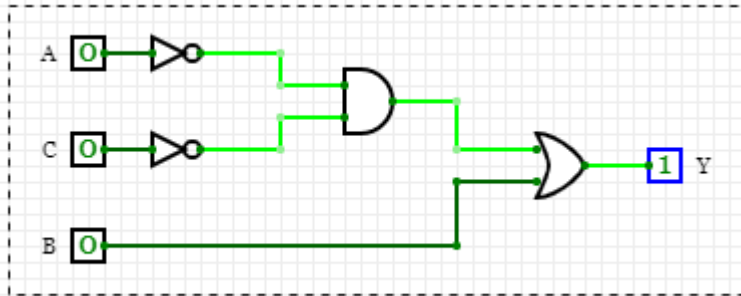
2. Tabla 02



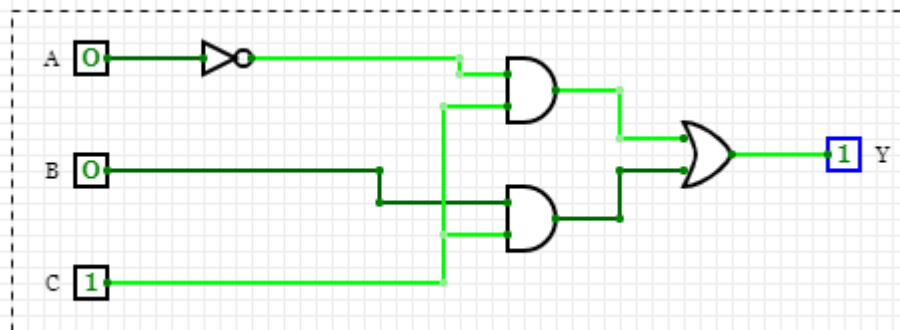
2. Tabla 03



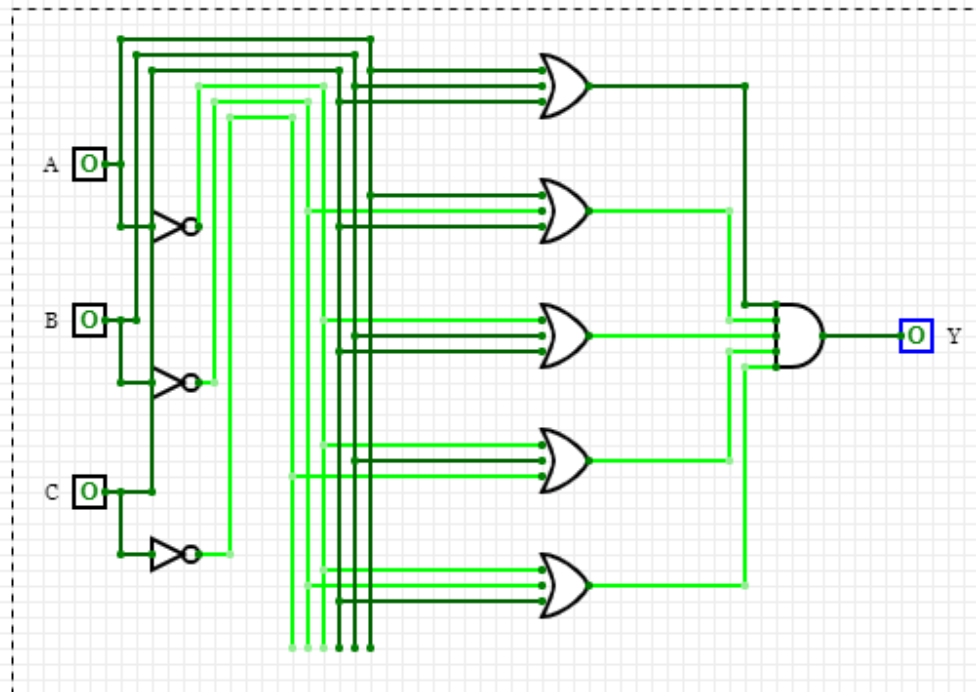
2. Tabla 04



Ejercicio 5 k-map



Ejercicio 5 POS



Ejercicio 5 SOP

