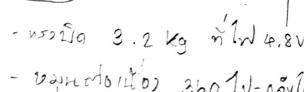


Module Design



- max. 14.75V 5V
- max. Input $\approx 12V$ (max. 5V) / $\approx 2.0V$ (limits)
- Digital I/O 54 pins
- Analog Input 16 pins
- DC Current / I/O pin 40 mA
- DC Current for 3.3V pin 50 mA
- Flash memory 256 KB
- SRAM 8 KB
- EEPROM 4 KB
- Clock speed 16 MHz