6   Pin Configuration and Functions

D, N, NS, J, DB, or PW Package
16-Pin SOIC, PDIP, SO, CDIP, SSOP, or TSSOP
Top View

```
Q_B  [ 1      16 ]  V_CC
Q_C  [ 2      15 ]  Q_A
Q_D  [ 3      14 ]  SER
Q_E  [ 4      13 ]  OE
Q_F  [ 5      12 ]  RCLK
Q_G  [ 6      11 ]  SRCLK
Q_H  [ 7      10 ]  SRCLR
GND  [ 8       9 ]  Q_H'
```
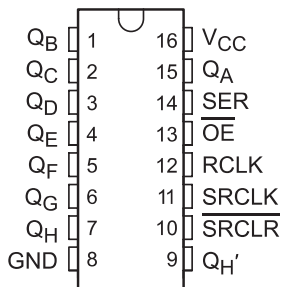
FK Package
20-Pin LCCC
Top View

```
        Q_C  Q_B  NC  V_CC  Q_A
         3    2    1   20   19
Q_D   4                         18   SER
Q_E   5                         17   OE
NC    6                         16   NC
Q_F   7                         15   RCLK
Q_G   8                         14   SRCLK
         9   10   11   12   13
        Q_H  GND  NC  Q_H'  SRCLR
```
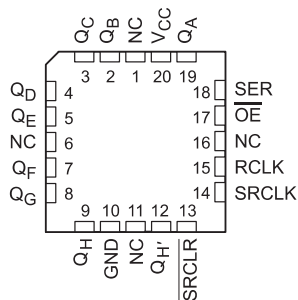
Pin Functions

PIN			I/O	DESCRIPTION
NAME	SOIC, PDIP, SO, CDIP, SSOP, or TSSOP	LCCC		
GND	8	10	—	Ground Pin
OE	13	17	I	Output Enable
Q_A	15	19	O	Q_A Output
Q_B	1	2	O	Q_B Output
Q_C	2	3	O	Q_C Output
Q_D	3	4	O	Q_D Output
Q_E	4	5	O	Q_E Output
Q_F	5	7	O	Q_F Output
Q_G	6	8	O	Q_G Output
Q_H	7	9	O	Q_H Output
Q_H'	9	12	O	Q_H' Output
RCLK	12	14	I	RCLK Input
SER	14	18	I	SER Input
SRCLK	11	14	I	SRCLK Input
SRCLR	10	13	I	SRCLR Input
NC	—	1	—	No Connection
		16		
		11		
		16		
V_CC	—	20	—	Power Pin