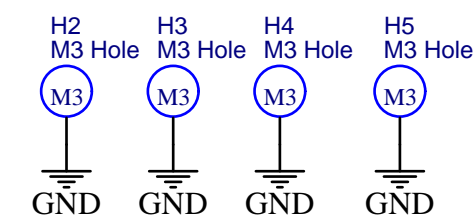
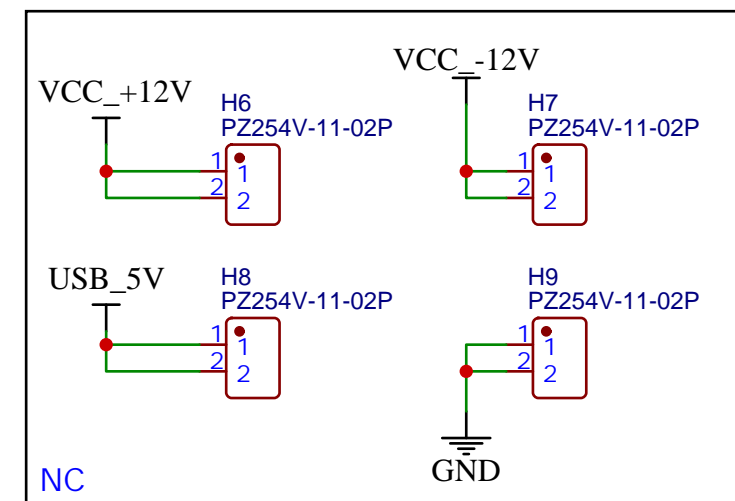
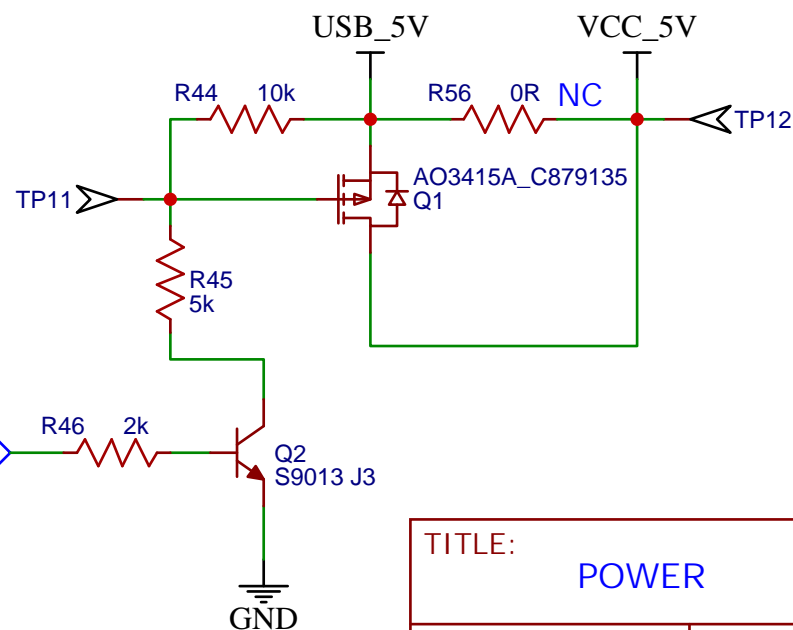
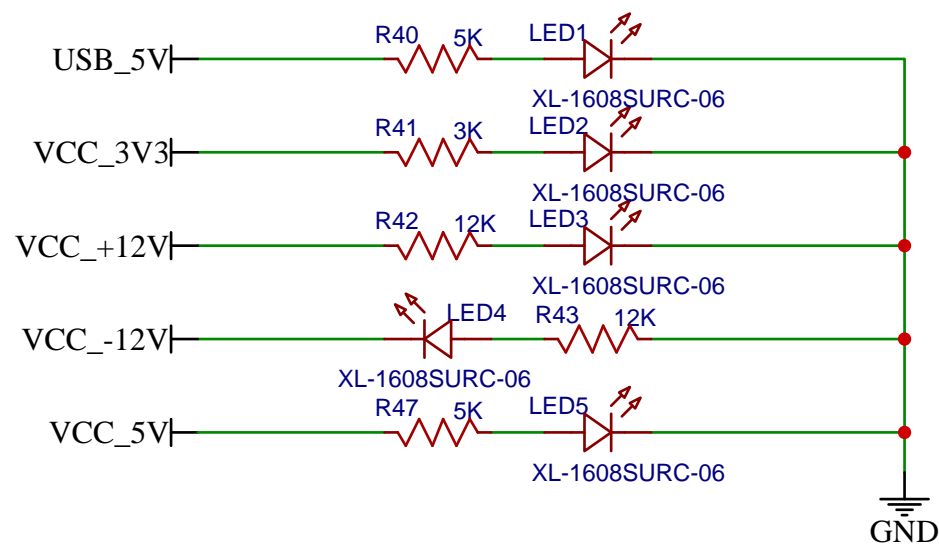
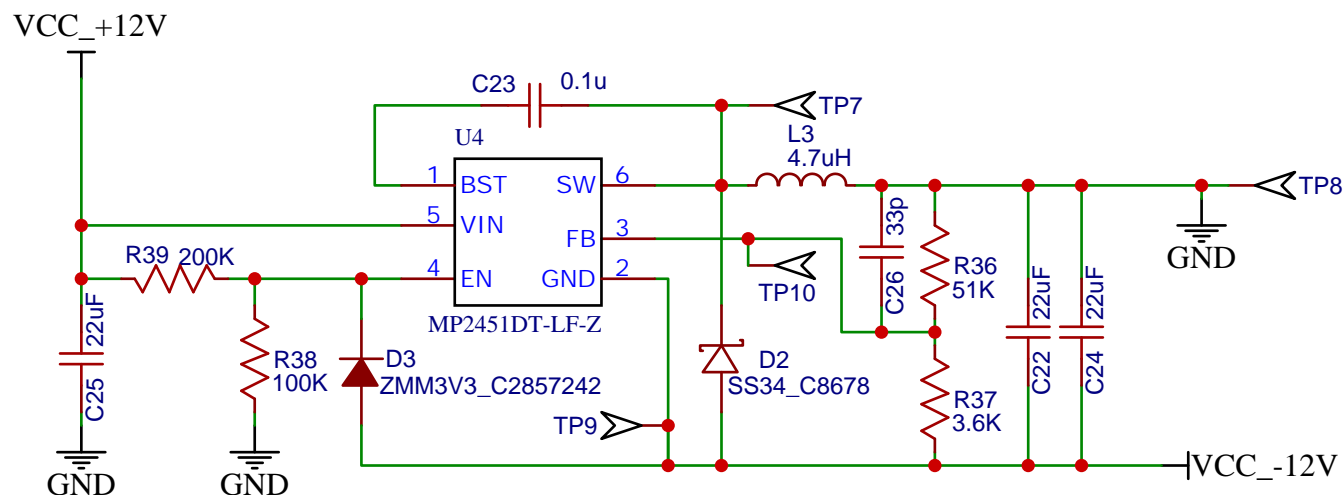
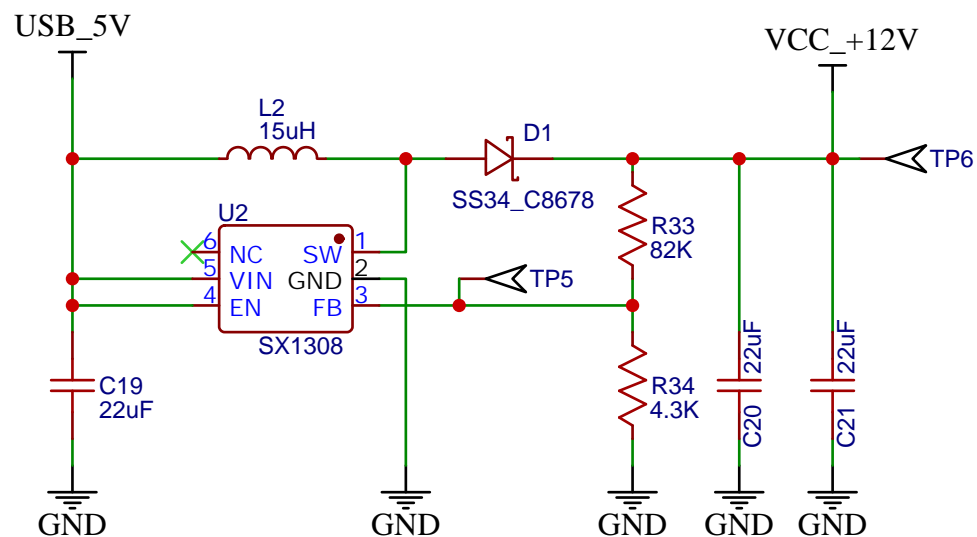
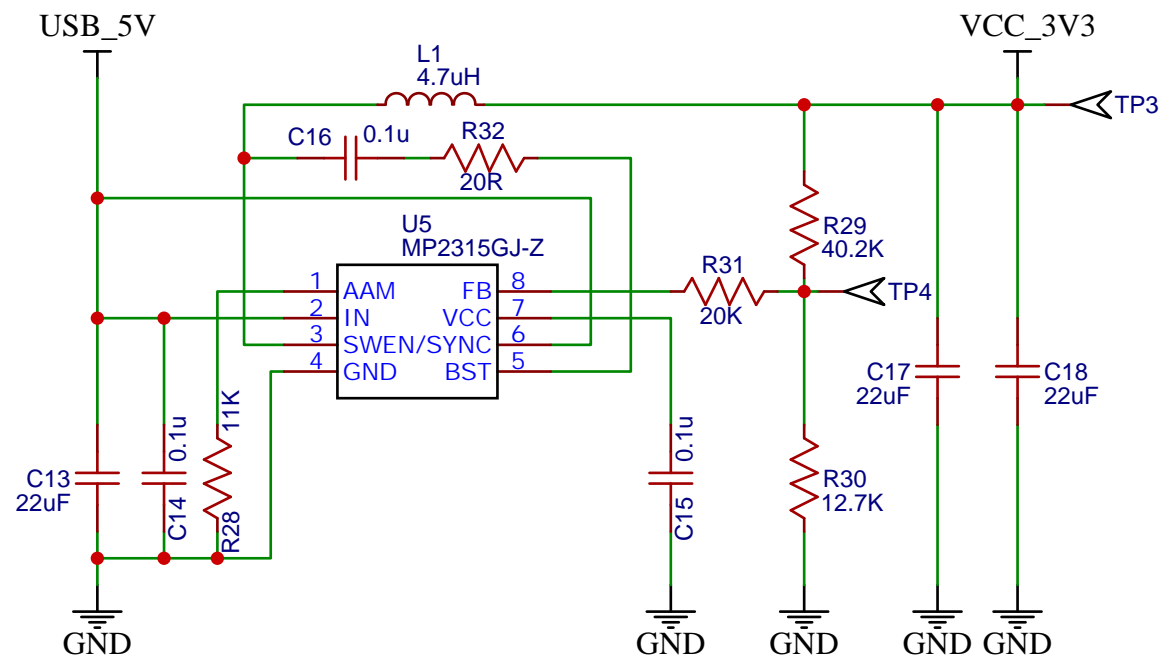
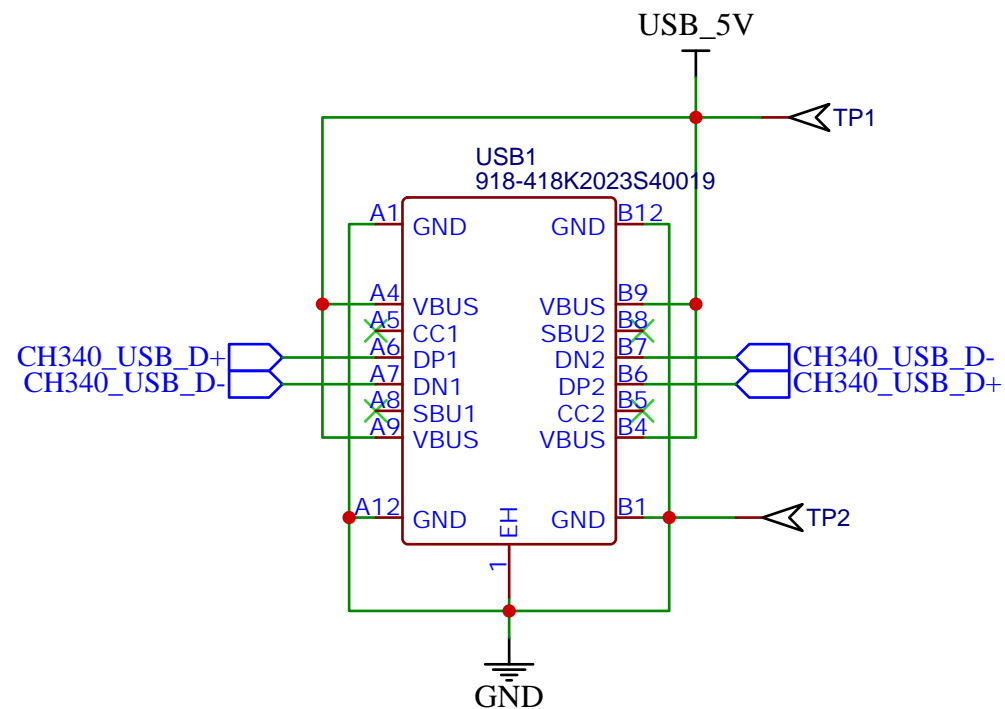


V1.1 (SCH、PCB均已更新)

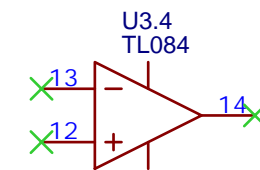
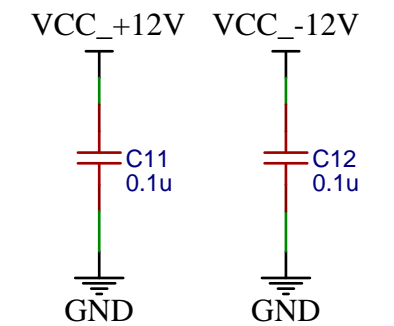
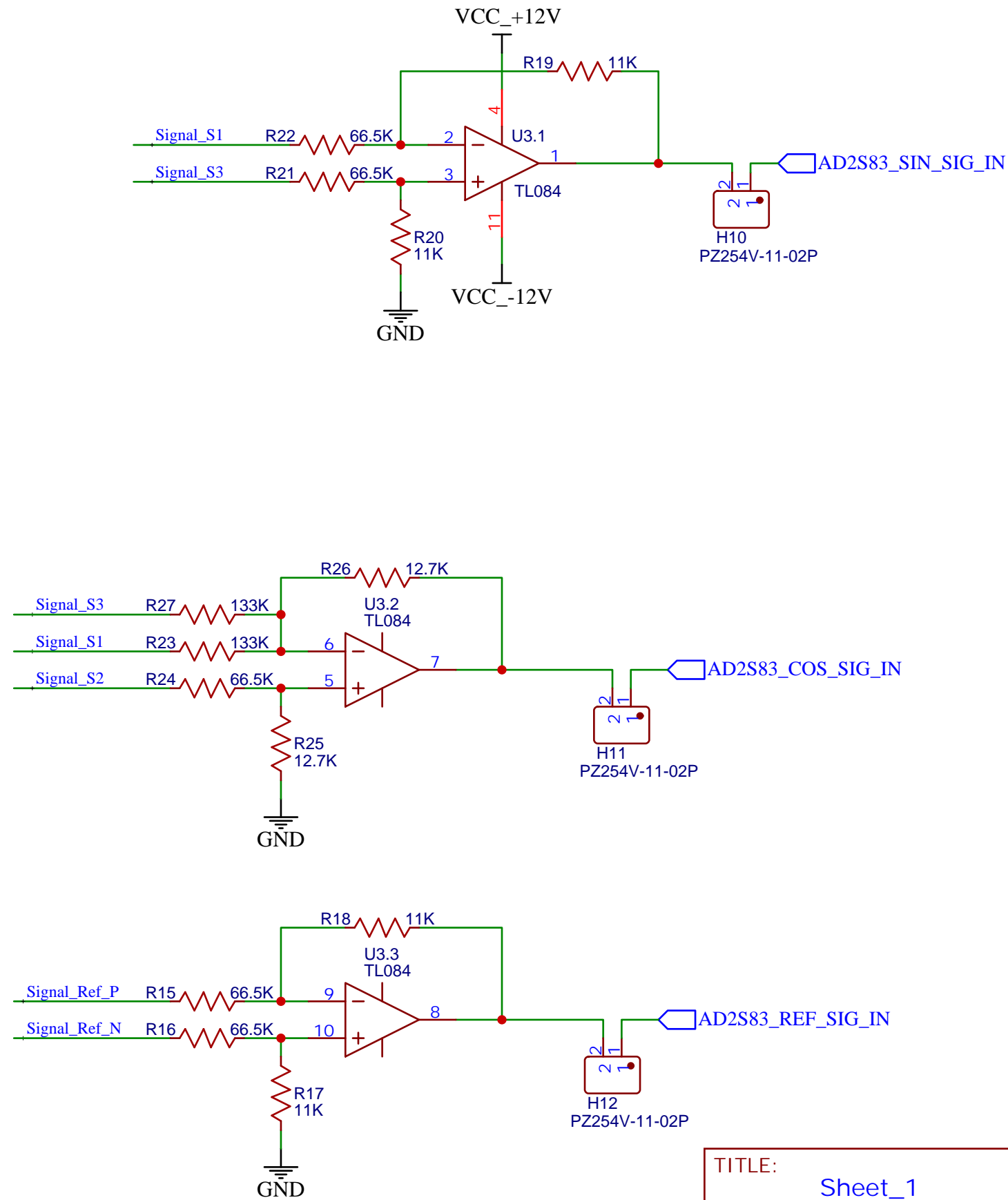
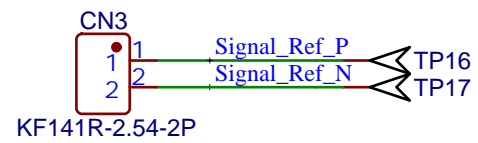
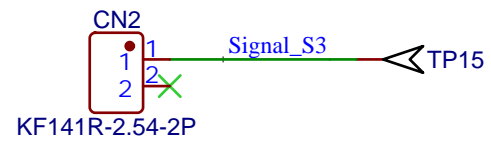
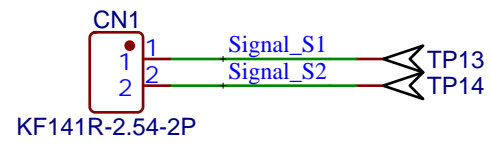
- 1、R30更改为12.7KOhm
- 2、删掉SX1308的使能引脚
- 3、MP2451DT-LF-Z的使能引脚添加稳压二极管
- 4、删掉AD2S83_SC1/2引脚的上拉电阻，内部有上拉
- 5、R11由130K更改为10K，根据数据位宽（16Bits）确定
- 6、将AD2S83的BYTESELECT连接到VCC5V。
- 7、C1和C5、C6更改为22nF，R11更改为100K



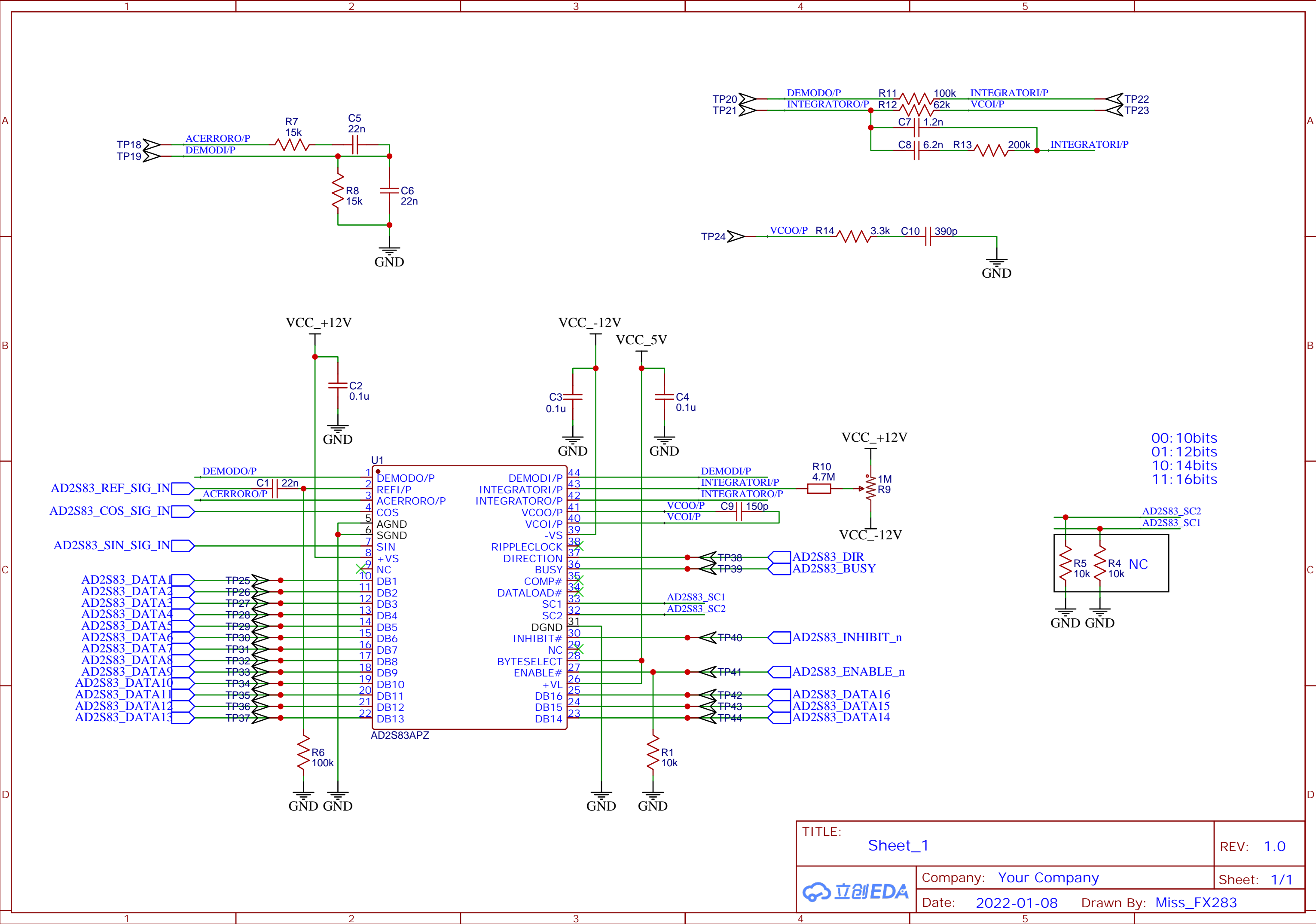
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立创EDA	Company: Your Company	Sheet: 1/1
	Date: 2022-01-08	Drawn By: Miss_FX283

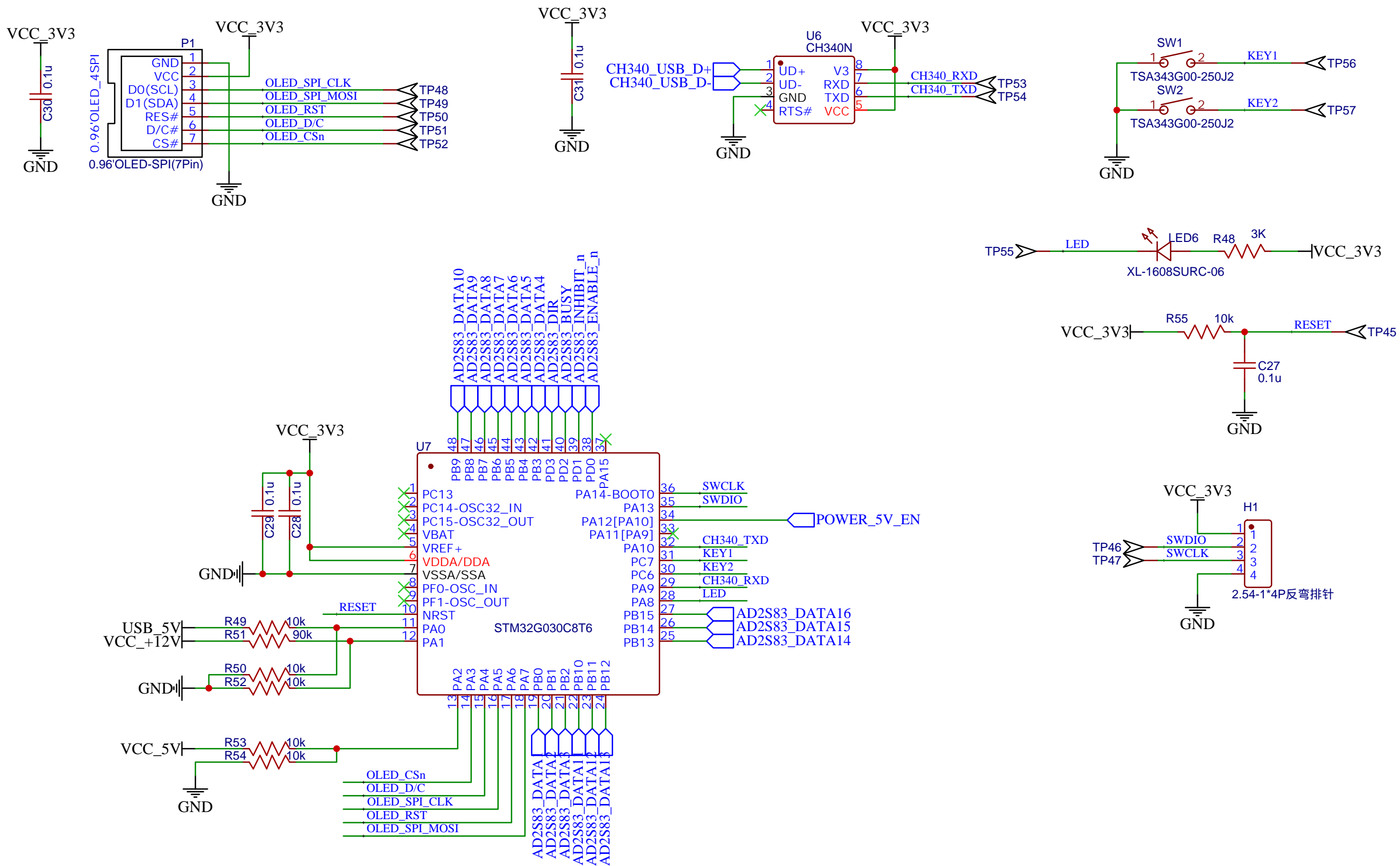


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Company: Your Company		Sheet: 1/1
Date: 2022-01-08		Drawn By: Miss_FX283



TITLE: Sheet_1		REV: 1.0
立创EDA	Company: Your Company	Sheet: 1/1
	Date: 2022-01-07	Drawn By: Miss_FX283





TITLE: Sheet_1		REV: 1.0
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	Date: 2022-01-08	Drawn By: Miss_FX283