

**J1 Left**

GND	1
1.10	2
1.13	3
1.15	4
0.02	5
VDD	6
0.29	7
0.31	7
GND	8
OUT	9
VBUS	10

**J2 Right**

GND	1
0.10	2
0.09	3
1.00	4
0.24	5
0.22	6
0.20	7
0.17	8
0.15	9
0.13	10

**J8 C12880MA Breakboard v2**

Pin	Signal
1	+5V
2	3V3
3	GND
4	GND
5	EOS
6	TRG
7	(Unlabeled)
8	START
9	CLK
10	VIDEO
11	LED
12	(Unlabeled)
13	(Unlabeled)
14	GND

1.10	STEMMA_SDA	0.10	RX
1.13	STEMMA_SCL	0.09	TX
1.15	AS7341_EN	1.00	LS_A1
0.02	AS7341_GPIO	0.24	LS_A2
0.29	AS7341_INT	0.22	LS_A3
0.31	BUTTON	0.20	LS_A4
		0.17	ADC_DRDY
		0.15	SDA
		0.13	SCL

AS7341 I2C address: 0x39

U1  
ADS1219PWR

VDD

AVDD 12 +5V VDD

DVDD 13 +5V VDD

DRDY 14 +5V VDD

RESET 3

A0 2

A1 1

AIN0

AIN1

AIN2

AIN3

REFN

REFP

SCL

SDA

AGND 5

DGND 4

GND

ADC\_ADDR0 1

ADC\_ADDR1 2

VIDEO 11

ADC\_REFN

ADC\_REFP

SCL 16

SDA 15

+5V

C1  
0.1uF

Default I2c address: 0x40

VDD

JP1

1

2

ADC\_ADDR0

ADC\_ADDR1

I2C address: 0x40 || 0x44

GND

U3  
LSF0204DPWR

VDD  
C6 0.1uF  
GND

VDD  
1 VREF\_A  
8 EN  
2 A1  
3 A2  
4 A3  
5 A4

LS\_A1  
LS\_A2  
LS\_A3  
LS\_A4

+5V  
14 VREF\_B  
13 CLK  
12 START  
11 TRG  
10 EOS  
6 NC\_6  
9 NC\_9  
7 GND  
GND

C7 0.1uF  
+5V  
GND