

Typographical Conventions for Makefile

Normal	Keyword	Comment	String	Variable	Target	Section
Prereq	FuncParam	Operator	Silent	Special	Error	Builtin
Number	Include	ControlFlow	VarModifier	RealOperator	SpecialTarget	SpecialPrereq

Alerts text

Normal Text **Alert Level 1** **Alert Level 2** **Alert Level 3** **Region Marker**

Modelines text

Comment Keyword Variable Number String Value Option ON
Option OFF

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1 # PRU_CGT environment variable must point to the TI PRU code gen tools directory. E.g.:
2 #(Desktop Linux) export PRU_CGT=/path/to/pru/code/gen/tools/ti-cgt-pru_2.1.2
3 #(Windows) set PRU_CGT=C:/path/to/pru/code/gen/tools/ti-cgt-pru_2.1.2
4 #(ARM Linux*) export PRU_CGT=/usr/share/ti/cgt-pru
5 #
6 # *ARM Linux also needs to create a symbolic link to the /usr/bin/ directory in
7 # order to use the same Makefile
8 #(ARM Linux) ln -s /usr/bin/ /usr/share/ti/cgt-pru/bin
9
10 ifndef PRU_CGT
11 define ERROR_BODY
12
13 *****
14 PRU_CGT environment variable is not set. Examples given:
15 (Desktop Linux) export PRU_CGT=/path/to/pru/code/gen/tools/ti-cgt-pru_2.1.2
16 (Windows) set PRU_CGT=C:/path/to/pru/code/gen/tools/ti-cgt-pru_2.1.2
17 (ARM Linux*) export PRU_CGT=/usr/share/ti/cgt-pru
18
19 *ARM Linux also needs to create a symbolic link to the /usr/bin/ directory in
20 order to use the same Makefile
21 (ARM Linux) ln -s /usr/bin/ /usr/share/ti/cgt-pru/bin
22 *****
23
24 endif
25 $(error $(ERROR_BODY))
26 endif
27
28 MKFILE_PATH := $(abspath $(lastword $(MAKEFILE_LIST)))
29 CURRENT_DIR := $(notdir $(patsubst %/,%, $(dir $(MKFILE_PATH))))
30 PROJ_NAME=$(CURRENT_DIR)
31 INCLUDE=--include_path=../../../include
32 GEN_DIR=gen
33
34 #Common compiler flags (Defined in 'PRU Optimizing C/C++ Compiler User's Guide)
35 CFLAGS=-v3 -O2 --display_error_number --endian=little --hardware_mac=on --obj_directory=$
36 (GEN_DIR) --pp_directory=$(GEN_DIR) -ppd -ppa
37
37 TARGET=$(GEN_DIR)/$(PROJ_NAME).lib
38 SOURCES=$(wildcard *.c)
39 #Using .object instead of .obj in order to not conflict with the CCS build process
40 OBJECTS=$(patsubst %, $(GEN_DIR)/%, $(SOURCES:.c=.object))
41
42 all: printStart $(TARGET) printEnd
43
44 printStart:
45     @echo ''
46     @echo '*****'
47     @echo 'Building project: $(PROJ_NAME)'
48

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49 printEnd:
50     @echo ''
51     @echo 'Finished building project: $(PROJ_NAME)'
52     @echo '*****'
53     @echo ''
54
55 # Invokes the archiver to make the .lib file
56 $(TARGET): $(OBJECTS)
57     @echo ''
58     @echo 'Building target: $@'
59     @echo 'Invoking: PRU Archiver'
60     $(PRU_CGT)/bin/arpru r $(TARGET) $(OBJECTS)
61     @echo 'Finished building target: $@'
62     @echo ''
63     @echo 'Output files can be found in the "$(GEN_DIR)" directory'
64
65 # Invokes the compiler on all c files in the directory to create the object files
66 $(GEN_DIR)/%.object: %.c
67     @mkdir -p $(GEN_DIR)
68     @echo ''
69     @echo 'Building file: $<'
70     @echo 'Invoking: PRU Compiler'
71     $(PRU_CGT)/bin/clpru --include_path=$(PRU_CGT)/include $(INCLUDE) $(CFLAGS) -fe $@ $<
72
73 .PHONY: all clean
74
75 # Remove the $(GEN_DIR) directory
76 clean:
77     @echo ''
78     @echo '*****'
79     @echo 'Cleaning project: $(PROJ_NAME)'
80     @echo ''
81     @echo 'Removing files in the "$(GEN_DIR)" directory'
82     @rm -rf $(GEN_DIR)
83     @echo ''
84     @echo 'Finished cleaning project: $(PROJ_NAME)'
85     @echo '*****'
86     @echo ''
87
88 # Includes the dependencies that the compiler creates (-ppd and -ppa flags)
89 -include $(OBJECTS:%.object=%.pp)
90
91

```