

Pixy progress report

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1 Introduction

Our initial goal was to adapt the Lucid programming language to have first-class support for realtime programming, while retaining its nature as a general purpose language.

Our progress so far is that we have redesigned the language's evaluation semantics from the ground up to create Pixy, a language that shares a lot of syntax and surface-level semantics with Lucid. Pixy is however at its core a very different language.

2 Pixy's programming model

We have defined Pixy's operational semantics in terms of nested state machines - any Pixy expression will evaluate to a state machine that will operate asynchronously, yielding a value at each timestep $t \in \mathbb{N}$. This approach allows for a much finer-grained control of the asynchronous relationships between dataflow nodes than Lucid's original lazy computation model, at the cost of some flexibility.

Specifically, Pixy does not support nested iteration in that same way that Lucid does, nor do custom operators enjoy the same degree of freedom as they did in Lucid.

To introduce Pixy's supported subset of Lucid, here is Pixy's grammar:

```

⟨expr⟩ ::= ⟨number⟩
| ⟨var⟩
| ⟨bool⟩
| nil
| ? ⟨expr⟩
| if ⟨expr⟩ then ⟨expr⟩ else ⟨expr⟩
| ⟨expr⟩ fby ⟨expr⟩
| ⟨expr⟩ where { ⟨wheredecls⟩ }
| next ⟨expr⟩
| ⟨var⟩ ( ⟨exprlist⟩ )
| ⟨expr⟩ + ⟨expr⟩
| ⟨expr⟩ - ⟨expr⟩
| ⟨expr⟩ * ⟨expr⟩
| ⟨expr⟩ / ⟨expr⟩
| ⟨exprtuplet⟩

⟨exprtuplet⟩ ::= ( ⟨exprlist⟩ ( | ⟨expr⟩ )? )

⟨bool⟩ ::= true | false

⟨exprlist⟩ ::= ⟨expr⟩ , ⟨exprlist⟩ | ⟨expr⟩

⟨varlist⟩ ::= ⟨var⟩ , ⟨varlist⟩ | ⟨var⟩

⟨wheredecl⟩ ::= ⟨var⟩ = ⟨expr⟩
| ⟨var⟩ ( ⟨varlist⟩ ) = ⟨expr⟩
| ( ⟨varlist⟩ ( | ⟨var⟩ )? ) = ⟨expr⟩

⟨wheredecls⟩ ::= ⟨wheredecl⟩ ; ⟨wheredecls⟩ | ⟨wheredecl⟩

```

In principle, any Pixy expression with no free variables can be viewed as a self-container program. Our current implementation reads a series of wheredecls and executes a main operator for the sake of ease of use.

We support 3 common datatypes: booleans, natural numbers and tuples.

2.1 Operational semantics

Pixy's operational semantics have several stages in order to model the ahead-of-time requirements of the language. This helps to prove by construction that Pixy's key properties hold.

The first stage is used to flatten out any operator calls. Since recursive operator calls could lead to unbounded memory usage during evaluation, Pixy requires all operator

calls including recursive ones to be flattened out before evaluation. In order to predict the maximum possible recursion depth and allocate memory for it ahead of time, we use a technique based on sized types.

The second stage is the allocation of initial state for all builtin operators - this was split off from the first stage due to complexity concerns.

The last stage is the evaluation derivations themselves, which operate on the modified AST and initial state defined in the prior stages. This stage models the lifecycle of the program and repeats indefinitely.

2.1.1 Operator calls

To begin with, we flatten out all operator calls via substitution. This is both to ensure that there is a clear bound on the time a Pixy expression can take to perform one time step and to simplify the formulation of any further stages.

We have an idea of how to deal with recursive operator application, but it is incomplete. Similar to [], we expect to reason about recursion depth using sized types. Specifically, we hope to generate upper and lower bounds for all operator arguments and enforce that the range of possible inputs to a recursive operator strictly decreases as recursion depth increases. Thus, we should be able to either generate a series of substitutions equivalent to the deepest recursion depth possible or reject an expression as ill-formed.

Our problem at the moment is that we do not have a precise enough representation of numbers in Pixy. While it is simple enough to calculate upper and lower bounds for an expression given its type, performing operations on these bounds requires a precise definition of numerical edge cases that we lack. We leave to future work exactly how to do this, but expect that once a more precise set of numerical semantics are derived the issue will be simpler.

For the simple case where we assume no recursion occurs, operator call flattening can be expressed using the syntax $\Gamma \vdash E \xRightarrow{\text{flatten}} E'$ where Γ represents the current scope, E represents the initial expression and E' represents the modified result with no operator calls.

$$\frac{\begin{array}{l} \Gamma \vdash B \xRightarrow{\text{flatten}}_{W1} B'; \Gamma' \\ \Gamma, \Gamma' \vdash B' \xRightarrow{\text{flatten}}_{W2} B'' \\ \Gamma, \Gamma' \vdash E \xRightarrow{\text{flatten}} E' \end{array}}{\Gamma \vdash E \text{ where } \{B\} \xRightarrow{\text{flatten}} E' \text{ where } B''} [\text{Flatten-where}]$$

Since order inside a where clause is arbitrary, we have to be careful how we treat scoping. First we pass through and collect all the operator definitions, then we perform a second pass while applying the operators wherever necessary.

$$\frac{\Gamma \vdash R \xRightarrow{\text{flatten}}_{W1} R'; \Gamma'}{\Gamma \vdash F(A...) = E; R \xRightarrow{\text{flatten}}_{W1} R'; F(A...) = E, \Gamma'} [\text{Flatten-where-1-operator}]$$

$$\frac{\Gamma \vdash R \xRightarrow{\text{flatten}}_{W1} R'; \Gamma'}{\Gamma \vdash V = E; R \xRightarrow{\text{flatten}}_{W1} (V = E; R'); \Gamma'} [\text{Flatten-where-1-variable}]$$

$$\frac{}{\Gamma \vdash \xRightarrow{\text{flatten}}_{W1} \emptyset} [\text{Flatten-where-1-empty}]$$

Once we've collected all the operator definitions, we traverse all the subexpressions and substitute them in when necessary:

$$\frac{\Gamma \vdash E \xRightarrow{\text{flatten}} E' \quad \Gamma \vdash R \xRightarrow{\text{flatten}}_{W2} R'}{\Gamma \vdash V = E; R \xRightarrow{\text{flatten}}_{W2} V = E'; R'} [\text{Flatten-where-2-variable}]$$

$$\frac{}{\Gamma \vdash \xRightarrow{\text{flatten}}_{W2}} [\text{Flatten-where-2-empty}]$$

When we encounter an operator application, we replace it the operator's body and a scope modifier to avoid name conflicts:

$$\frac{\Gamma(F(A...) = E) \quad \Gamma \vdash E \xRightarrow{\text{flatten}} E'}{\Gamma \vdash F(V...) \xRightarrow{\text{flatten}} [A = V...] E'} [\text{Flatten-apply}]$$

Note: the scope modifier pictured here is not a substitution - it will be interpreted specially by later stages.

2.1.2 init stage

Pixy has an initialisation stage during which all the state needed for any later computation will be initialised and buffers are put in place to implement delay compensation - this is the only stage that controls the allocation of memory.

To give a brief rationale for delay compensation, consider the following Pixy snippet:

```
x = 1;
y = x + next x;
```

Following the basic semantics, at t_1 x will be 1, and when computing y we will take 1 and try to add it to next x , which dropped the value 1 and is equal to *nil* in this case. This is counter-intuitive since $x + \text{next } x$ looks like it means "wait for next x " then add x to it. When considering this issue we found that it was possible to reimplement the intuitive interpretation using implicit buffering, or delay compensation.

The syntax for derivations of this stage looks like this:

$$d \vdash E \overset{\text{init}}{\Rightarrow} S; \delta; \Delta$$

d is the number of nested fby expressions containing E (initially 0)

S is the starting state for expression E

δ is the pre-delay of expression E , that is, the number of timesteps before E will yield values.

Δ is the set of constraints on pre-delay time. These are to be solved by a constraint solver in order to derive the appropriate pre-delays of any expression.

In order to perform delay compensation, some initial state will use a notation $Q[x]$ as a shorthand for creating a queue of length x . This queue will initially be empty, and should be filled at maximum to capacity x .

$$\frac{\begin{array}{c} d \vdash A \overset{\text{init}}{\Rightarrow} S_a; \delta_a; \Delta_a \\ d \vdash B \overset{\text{init}}{\Rightarrow} S_b; \delta_b; \Delta_b \end{array}}{d \vdash A \text{ op } B \overset{\text{init}}{\Rightarrow} (S_a, Q[\max(\delta_a, \delta_b) - \delta_a], S_b, Q[\max(\delta_a, \delta_b) - \delta_b]); \max(\delta_a, \delta_b); \Delta_a, \Delta_b} \text{[Init-binop]}$$

$$\frac{\begin{array}{c} d \vdash C \overset{\text{init}}{\Rightarrow} S_C; \delta_C; \Delta_C \\ d \vdash T \overset{\text{init}}{\Rightarrow} S_T; \delta_T; \Delta_T \\ d \vdash F \overset{\text{init}}{\Rightarrow} S_F; \delta_F; \Delta_F \end{array}}{d \vdash \text{if } C \text{ then } T \text{ else } F \overset{\text{init}}{\Rightarrow} (S_C, Q[m - \delta_C], S_T, Q[m - \delta_T], S_F, Q[m - \delta_F]); m; \Delta_C, \Delta_T, \Delta_F} \text{[Init-if]}$$

where $m = \max(\delta_C, \delta_T, \delta_F)$

$$\frac{d \vdash E \overset{\text{init}}{\Rightarrow} S; \delta; \Delta}{d \vdash \text{next } E \overset{\text{init}}{\Rightarrow} (\text{false}, S); \delta + 1; \Delta} \text{[Init-next]}$$

$$\frac{\begin{array}{c} d \vdash A \overset{\text{init}}{\Rightarrow} S_a; \delta_a; \Delta_a \\ d + 1 \vdash B \overset{\text{init}}{\Rightarrow} S_b; \delta_b; \Delta_b \end{array}}{d \vdash A \text{ fby } B \overset{\text{init}}{\Rightarrow} (\text{false}, \text{nil}, S_a, S_b); \delta_a; \Delta_a, \Delta_b, \delta_a \geq \delta_b - d} \text{[Init-fby]}$$

$$\begin{array}{c}
\frac{}{d \vdash I \overset{\text{init}}{\Rightarrow} (); \delta(I);} \text{[Init-id]} \\
\\
\frac{\frac{}{d \vdash V \overset{\text{init}}{\Rightarrow} S_v; \delta_v; \Delta_v} V...}{d \vdash E \overset{\text{init}}{\Rightarrow} S; \delta; \Delta} \\
\\
\frac{}{d \vdash [A = V...]E \overset{\text{init}}{\Rightarrow} (S, (S_v...), (Q[\max(\delta_v...) - \delta_v]...)); \delta; \Delta, \Delta_v..., \delta(A) = \delta_v...} \text{[Init-operator-scope]} \\
\\
\frac{\frac{}{d \vdash V \overset{\text{init}}{\Rightarrow} S_v; \delta_v; \Delta_v} V...}{d \vdash E \overset{\text{init}}{\Rightarrow} S; \delta; \Delta} \\
\\
\frac{}{d \vdash E \text{ where } \{N = V...\} \overset{\text{init}}{\Rightarrow} (S, (nil...), (S_v...)); \delta; \Delta, \Delta_v..., \delta(N) = \delta_v...} \text{[Init-where]}
\end{array}$$

2.1.3 Proof of δ correctness

TODO

2.1.4 Runtime evaluation semantics

Now that we have described how to prepare a Pixy program to be run, here are the semantics of a running Pixy program.

Since a Pixy program's evaluation takes place over an infinite series of timesteps, the evaluation semantics describe a single timestep in detail.

The syntax of one evaluation step looks like this: $\Gamma; S \vdash E \Downarrow V; S'$.

Γ represents a mapping of variable names to value, that is, the current scope.

S represents the accumulated state associated with the current expression.

E represents the current expression.

V is the value that E evaluates to in the context of Γ and S at the current timestep.

S' is the state that will be used to evaluate E in the next timestep.

Conditionals We haven't considered if statements in great detail so far - they pose a peculiar problem. Unlike typical programming languages, if statements in Pixy do not short-circuit. That is, we must ensure that concurrent events continue uninterrupted in both the taken and non-taken branches of the if statement.

To achieve this we introduce a second mode of evaluation, choke-evaluation. It is notated \P and aside from its special implementation acts the same as normal evaluation. Specifically, if an expression if choke-evaluated then it should not appear to perform any computations. It may however update any of its non-visible state as normal.

$$\begin{array}{c}
\Gamma; S_c \vdash C \Downarrow V_c; S'_c \\
\Gamma; S_t \vdash T \Downarrow V; S'_t \\
\Gamma; S_f \vdash F \Downarrow nil; S'_f \\
\mathbf{buffer}(Q_c, V_c) = Q'_c, true \\
\mathbf{buffer}(Q_t, V) = Q'_t, D \\
\mathbf{buffer}(Q_f, nil) = Q'_f, nil \\
\hline
\Gamma; (S_c, Q_c, S_t, Q_t, S_f, Q_f) \vdash \text{if } C \text{ then } T \text{ else } F \Downarrow D; (S'_c, Q'_c, S'_t, Q'_t, S'_f, Q'_f) \text{ [Eval-if-true]}
\end{array}$$

$$\begin{array}{c}
\Gamma; S_c \vdash C \Downarrow V_c; S'_c \\
\Gamma; S_t \vdash T \Downarrow nil; S'_t \\
\Gamma; S_f \vdash F \Downarrow V; S'_f \\
\mathbf{buffer}(Q_c, V_c) = Q'_c, false \\
\mathbf{buffer}(Q_t, nil) = Q'_t, nil \\
\mathbf{buffer}(Q_f, V) = Q'_f, D \\
\hline
\Gamma; (S_c, Q_c, S_t, Q_t, S_f, Q_f) \vdash \text{if } C \text{ then } T \text{ else } F \Downarrow D; (S'_c, Q'_c, S'_t, Q'_t, S'_f, Q'_f) \text{ [Eval-if-false]}
\end{array}$$

$$\begin{array}{c}
\Gamma; S_c \vdash C \Downarrow V_c; S'_c \\
\Gamma; S_t \vdash T \Downarrow nil; S'_t \\
\Gamma; S_f \vdash F \Downarrow nil; S'_f \\
\mathbf{buffer}(Q_c, V_c) = Q'_c, nil \\
\mathbf{buffer}(Q_t, nil) = Q'_t, nil \\
\mathbf{buffer}(Q_f, nil) = Q'_f, nil \\
\hline
\Gamma; (S_c, Q_c, S_t, Q_t, S_f, Q_f) \vdash \text{if } C \text{ then } T \text{ else } F \Downarrow nil; (S'_c, Q'_c, S'_t, Q'_t, S'_f, Q'_f) \text{ [Eval-if-nil]}
\end{array}$$

$$\begin{array}{c}
\Gamma; S_c \vdash C \Downarrow nil; S'_c \\
\Gamma; S_t \vdash T \Downarrow nil; S'_t \\
\Gamma; S_f \vdash F \Downarrow nil; S'_f \\
\mathbf{buffer}(Q_c, nil) = Q'_c, nil \\
\mathbf{buffer}(Q_t, nil) = Q'_t, nil \\
\mathbf{buffer}(Q_f, nil) = Q'_f, nil \\
\hline
\Gamma; (S_c, Q_c, S_t, Q_t, S_f, Q_f) \vdash \text{if } C \text{ then } T \text{ else } F \Downarrow nil; (S'_c, Q'_c, S'_t, Q'_t, S'_f, Q'_f) \text{ [Eval-if-C]}
\end{array}$$

Binop This is a catch-all derivation for any binary operator in Pixy - the main point of interest here is that binary operators synchronise their inputs using buffers allocated in the init stage.

$\mathbf{buffer}(Q, V)$ in this case is a shorthand for pushing V onto the queue Q , returning the element at the front of the queue is any, nil otherwise.

$$\begin{array}{c}
\Gamma; S_l \vdash L \Downarrow V_l; S'_l \\
\Gamma; S_r \vdash R \Downarrow V_r; S'_r \\
\mathbf{buffer}(Q_l, V_l) = Q'_l, nil \\
\mathbf{buffer}(Q_r, V_r) = Q'_r, nil \\
\hline
\Gamma; (S_l, Q_l, S_r, Q_r) \vdash L \text{ op } R \Downarrow nil; (S'_l, Q'_l, S'_r, Q'_r) \text{ [Eval-binop-skip]}
\end{array}$$

$$\begin{array}{c}
\Gamma; S_l \vdash L \Downarrow V_l; S'_l \\
\Gamma; S_r \vdash R \Downarrow V_r; S'_r \\
\mathbf{buffer}(Q_l, V_l) = Q'_l, D_l \\
\mathbf{buffer}(Q_r, V_r) = Q'_r, D_r \\
D_l \neq nil \\
D_r \neq nil \\
\hline
\Gamma; (S_l, Q_l, S_r, Q_r) \vdash L \text{ op } R \Downarrow D_l \text{ op } D_r; (S'_l, Q'_l, S'_r, Q'_r) \text{ [Eval-binop]}
\end{array}$$

$$\begin{array}{c}
\Gamma; S_l \vdash L \Downarrow nil; S'_l \\
\Gamma; S_r \vdash R \Downarrow nil; S'_r \\
\mathbf{buffer}(Q_l, nil) = Q'_l, nil \\
\mathbf{buffer}(Q_r, nil) = Q'_r, nil \\
\hline
\Gamma; (S_l, Q_l, S_r, Q_r) \vdash L \text{ op } R \Downarrow nil; (S'_l, Q'_l, S'_r, Q'_r) \text{ [Eval-binop-C]}
\end{array}$$

Where

$$\begin{array}{c}
\hline \hline
\Gamma, \overline{N = S_v \dots}^{S_v \dots}; S_s \vdash E_v \Downarrow V_v; S'_s \\
\Gamma, N = V_v \dots; S \vdash E \Downarrow V; S' \\
\hline
\Gamma; (S, (S_v \dots), (S_s \dots)) \vdash E \text{ where } \{N = E_v \dots\} \Downarrow V; (S', V_v \dots, S'_s \dots) \text{ [Eval-where]}
\end{array}$$

An interesting observation here is that even under choke-evaluation where expression still evaluate their internal state. This is because the where's internal state is not directly visible - hidden computation can occur as long as it is not returned from the main body of the where.

$$\begin{array}{c}
\hline \hline
\Gamma, \overline{N = S_v \dots}^{S_v \dots}; S_s \vdash E_v \Downarrow V_v; S'_s \\
\Gamma, N = V_v \dots; S \vdash E \Downarrow nil; S' \\
\hline
\Gamma; (S, (S_v \dots), (S_s \dots)) \vdash E \text{ where } \{N = E_v \dots\} \Downarrow nil; (S', V_v \dots, S'_s \dots) \text{ [Eval-where-C]}
\end{array}$$

Operator scope Even though operator application will have been flattened in stage 1, we still need to apply the proper scoping rules and synchronisation here.

Specifically, we ensure all the arguments have their pre-delays synchronised and that the variable scope contains only the arguments to the operator.

$$\frac{\frac{\overline{\Gamma; S_v \vdash E_v \Downarrow V_v; S'_v}^{E_v \dots}}{\mathbf{buffer}(Q_v, V_v) = Q'_v, D_v}^{Q_v \dots}}{A = D_v \dots; S \vdash E \Downarrow V; S'} \text{[Eval-apply]}$$

$$\frac{\frac{\overline{\Gamma; S_v \vdash E_v \Downarrow nil; S'_v}^{E_v \dots}}{\mathbf{buffer}(Q_v, nil) = Q'_v, D_v}^{Q_v \dots}}{A = D_v \dots; S \vdash E \Downarrow nil; S'} \text{[Eval-apply-C]}$$

fby

$$\frac{\begin{array}{l} \Gamma; S_a \vdash A \Downarrow V_a; S'_a \\ \Gamma; S_b \vdash B \Downarrow V_b; S'_b \\ V_b \neq nil \end{array}}{\Gamma; (false, U, S_a, S_b) \vdash A \text{ fby } B \Downarrow V_a; (V_a \neq nil, V_b, S'_a, S'_b)} \text{[Eval-fby-A-buffer]}$$

$$\frac{\begin{array}{l} \Gamma; S_a \vdash A \Downarrow V_a; S'_a \\ \Gamma; S_b \vdash B \Downarrow V_b; S'_b \\ V_b = nil \end{array}}{\Gamma; (false, U, S_a, S_b) \vdash A \text{ fby } B \Downarrow V_a; (V_a \neq nil, U, S'_a, S'_b)} \text{[Eval-fby-A-nobuffer]}$$

$$\frac{\begin{array}{l} \Gamma; S_a \vdash A \Downarrow nil; S'_a \\ \Gamma; S_b \vdash B \Downarrow V_b; S'_b \\ U \neq nil \end{array}}{\Gamma; (true, U, S_a, S_b) \vdash A \text{ fby } B \Downarrow U; (true, V_b, S'_a, S'_b)} \text{[Eval-fby-B-buffer]}$$

$$\frac{\begin{array}{l} \Gamma; S_a \vdash A \Downarrow nil; S'_a \\ \Gamma; S_b \vdash B \Downarrow V_b; S'_b \\ U = nil \end{array}}{\Gamma; (true, U, S_a, S_b) \vdash A \text{ fby } B \Downarrow V_b; (true, nil, S'_a, S'_b)} \text{[Eval-fby-B-nobuffer]}$$

$$\frac{\begin{array}{l} \Gamma; S_a \vdash A \Downarrow nil; S'_a \\ \Gamma; S_b \vdash B \Downarrow nil; S'_b \end{array}}{\Gamma; (C, U, S_a, S_b) \vdash A \text{ fby } B \Downarrow nil; (C, U, S'_a, S'_b)} \text{[Eval-fby-C]}$$

next

$$\frac{\Gamma; S \vdash E \Downarrow V; S' \quad V = nil}{\Gamma; (false, S) \vdash next E \Downarrow nil; (false, S')} [Eval-next-skip-nil]$$

$$\frac{\Gamma; S \vdash E \Downarrow V; S' \quad V \neq nil}{\Gamma; (false, S) \vdash next E \Downarrow nil; (true, S')} [Eval-next-skip-first]$$

$$\frac{\Gamma; S \vdash E \Downarrow V; S'}{\Gamma; (true, S) \vdash next E \Downarrow V; (true, S')} [Eval-next-forward]$$

$$\frac{\Gamma; S \vdash E \Downarrow nil; S'}{\Gamma; (C, S) \vdash next E \Downarrow nil; (C, S')} [Eval-next-C]$$

Id

$$\frac{\Gamma(I) = V}{\Gamma, () \vdash I \Downarrow V; ()} [Eval-id]$$

$$\frac{}{\Gamma, () \vdash I \Downarrow nil; ()} [Eval-id-C]$$

3 Comments and future work

There is clearly a lot more to be done to the language. At the evaluation level we need to properly refine recursive operator application, and most of our ideas for a type system are discussed but not fully explored. While we have described some of these ideas inline, here we will present a set of possible future directions and our thoughts on them.

3.1 Circuits and lowest common denominator of computation

As we worked more on the language, we discovered that Pixy is not very powerful compared to most languages. In its current form it isn't even Turing complete.

While on its own this can be seen as a weakness in the language, we noticed at the same time that due to its fully static control flow Pixy can be used to describe algorithms that would work on more unusual platforms such as field programmable gate arrays - since it derives from Lucid, which had both a text and a diagram representation, and removes most of Lucid's dynamicism, Pixy should be able to express algorithms targeted at the lowest common denominator of computation: digital circuits. Since we also envisage interesting implementation strategies for commodity hardware, once we build a better base for the language it may be interesting to investigate what happens if you apply Pixy to circuit design.

3.2 State of the type system(s)

Current discussion places Pixy type system as 2-dimensional along the data and control axes. On the one hand we envisage a traditional type system similar to that used in typical functional programming languages that would be used to make sure basic data is coherent across all program timesteps, and on the other we hope to experiment with a temporal logic-based system that tries to reason about tricky corner cases and synchronisation problems that span multiple timesteps.

Part of our work on synchronising pre-delays might be generalisable to become a part of the temporal type system, and can already catch some basic programming errors.

3.3 Expressing iteration

One notable missing feature from Lucid is support for nested iteration. Here we explore why this feature disappeared and how we expect to replace it.

In Lucid one would have been able to write something like this:

```
c until c eq N where
  N is current n;
  c = 1 fby c+1;
end
```

Given some external variable n , for each value of n this would yield the sequence $1 \dots n-1$. While this is a reasonable program to write, within the context of Pixy's state machine-based semantics one might ask the question: if used within another program, when will these values be yielded? For this program, the question does not have an answer. Since for every timestep a new value of n may be presented, there can exist no state machine that will yield more than one value in a single timestep.

While this can be seen as a limitation of Pixy relative to Lucid's expressiveness, this also demonstrates that Pixy's state machine model enforces that each timestep of a Pixy program will take a known amount of time - it is impossible to write a program whose timesteps take an unbounded amount of time to execute.

The closest alternative we imagine is Turing-incomplete synchronous recursion, also referred to as recursive operator application earlier in this document. This would allow processing batches of data in one timestep, but using a mechanism more similar to C++'s templates. All calls would be expanded at compile time and the maximum recursion depth would be checked in order to ensure that a deadline can be given and met for the completion of each program timestep.

For truly infinite computation, the programmer would have to express it over multiple timesteps. This is reasonable, since Pixy specifically prioritises liveness and concurrency over expressiveness. It also encourages developers to think about the liveness of their systems - can the system afford to wait? What other components might be affected?