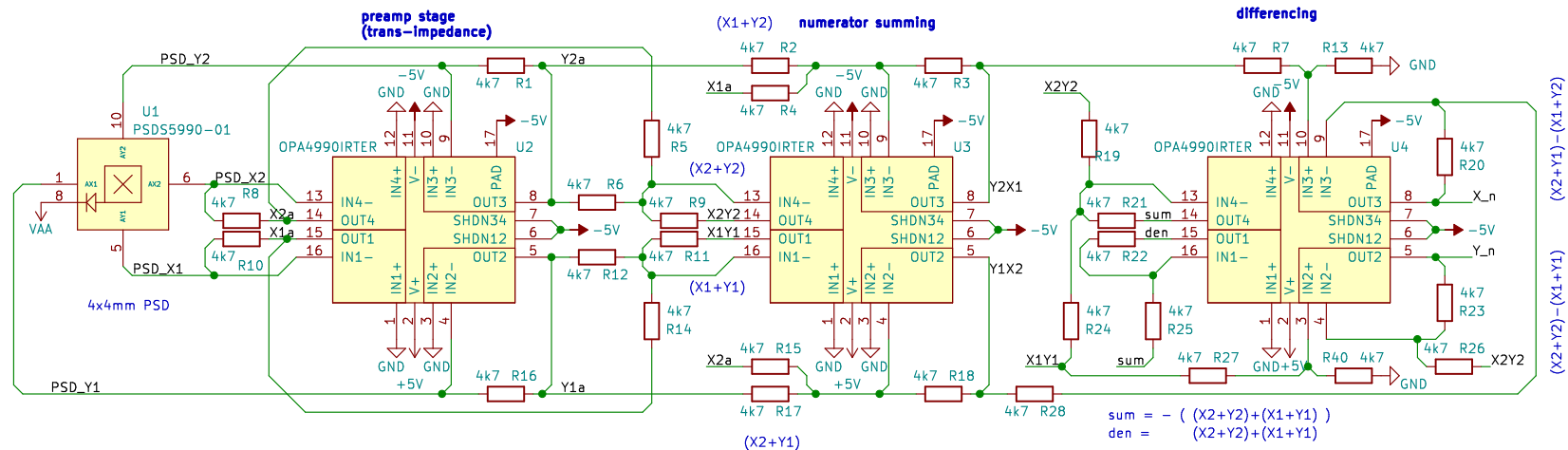
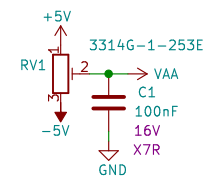
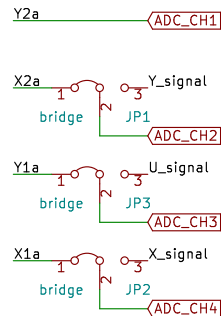


Vr reverse voltage bias



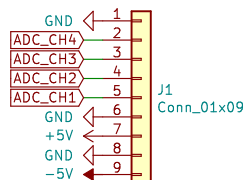
Output

output selection



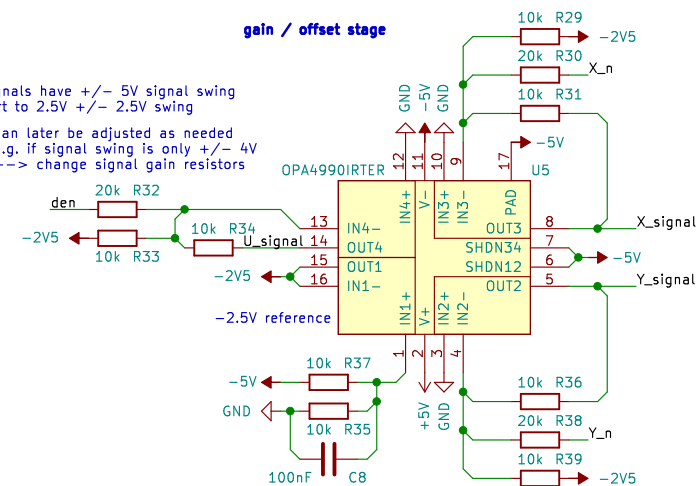
these signals do not make sense to directly go to the ADC because they may have negative output swing

Flatband Cable 9P



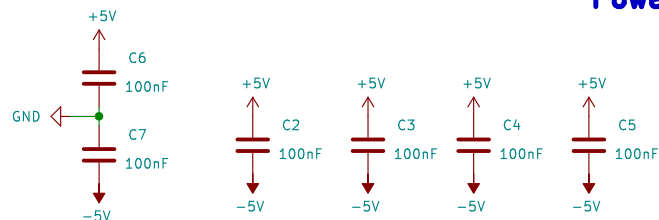
gain / offset stage

all signals have +/- 5V signal swing
convert to 2.5V +/- 2.5V swing
can later be adjusted as needed
e.g. if signal swing is only +/- 4V
--> change signal gain resistors

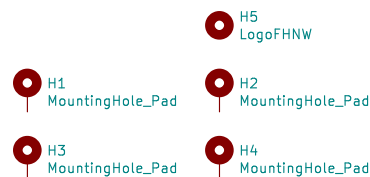


- use precision resistors
- calibrate the DSP

Power



Mechanical



Analogue



Simon Burkhardt
Université de Genève
FHNW ISE

Sheet: /
File: QSIT_PayloadPSDmodule.sch

Title: PSD analog front end for QSIT payload

Size: A4 Date: 2021-12-20
KiCad E.D.A. kicad (5.1.12)-1

Rev: 1.0
Id: 1/1