

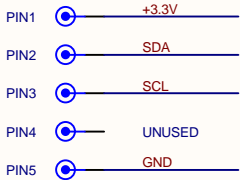
NERVESKEY

VERSION 1901

CRYPTOGRAPHIC CHIP BREAKOUT FOR BREADBOARD AND RASPBERRY PI
A COLLABORATIVE PROJECT SUPPORTED BY THE MOCOMAKERS COMMUNITY
WWW.MOCOMAKERS.COM

DESIGNED AND MANUFACTURED AT HOME IN
GAITHERSBURG, MD USA

SINGLE IN-LINE PIN LAYOUT (0.1 INCH SPACING)

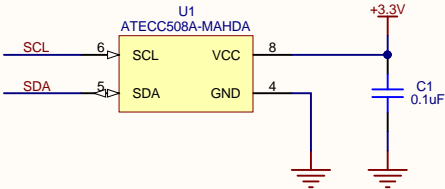


MECHANICAL FEATURES SPECIFICALLY FOR SHIM MOUNTING TO A RASPBERRY PI

(NO CONNECTION, NOT USED FOR ANY ELECTRICAL FUNCTION)




CRYPTOGRAPHIC CHIP FROM MICROCHIP TECHNOLOGY



- NOTES -
- 1) I2C PULL UP RESISTORS MUST BE PROVIDED BY THE HOST DEVICE, AND THEY TYPICALLY ARE ALREADY ON A RASPBERRY PI
 - 2) THE UDFN PACKAGE IS EMPLOYED FOR U1 SO THAT THE BOARD CAN BE MOUNTED PART SIDE DOWN (TEXT SIDE UP) ON THE TOP SIDE OF A RASPBERRY PI WITHOUT INTERFERING WITH OTHER PARTS ON THE RASPBERRY PI
 - 3) THE MODULE IS TYPICALLY CONNECTED IN ONE OF TWO WAYS -
 - A) PUSH ONTO A RASPBERRY PI EXPANSION HEADER AT THE PIN 1 END WITH THE CHIP FACING THE RASPBERRY PI BOARD
 - B) INSTALL A 4-PIN HEADER TO ALLOW MOUNTING ON A MODULAR BREADBOARD
 - 4) THE MODULE IS GENERALLY NOT EXPECTED TO BE PHYSICALLY SERVICEABLE

THANKS TO OUR LOCAL COMMERCIAL SPONSORS

NERVESKEY IS AN OPEN HARDWARE DESIGN
MANUFACTURED BY ALLIED COMPONENT WORKS, LLC
WWW.ALLIEDCOMPONENTWORKS.COM
IN COOPERATION WITH CUSTOM ELECTRONICS COMPANY, INC.
WWW.CUSTOMELECTRONICSCO.COM

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