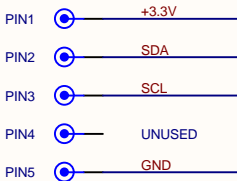


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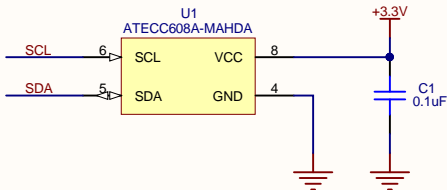
VERSION 1902

CRYPTOGRAPHIC CHIP BREAKOUT FOR BREADBOARD AND RASPBERRY PI
A PROJECT SUPPORTED BY THE NERVES SOFTWARE COMMUNITY
WITH ORGANIZATION BY NERVES PROJECT COLLABORATOR TROODON SOFTWARE, LLC
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SINGLE IN-LINE PIN LAYOUT (0.1 INCH SPACING)



CRYPTOGRAPHIC CHIP FROM MICROCHIP TECHNOLOGY



ASSEMBLED BOARDS

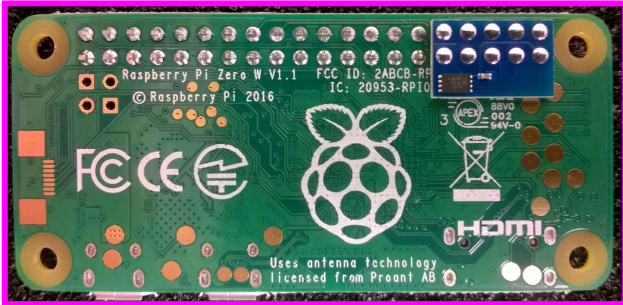


SECOND ROW SINGLE IN-LINE MECHANICAL FEATURES
SPECIFICALLY TO SUPPORT MOUNTING TO A RASPBERRY PI
(NO CONNECTION, NOT USED FOR ANY ELECTRICAL FUNCTION)

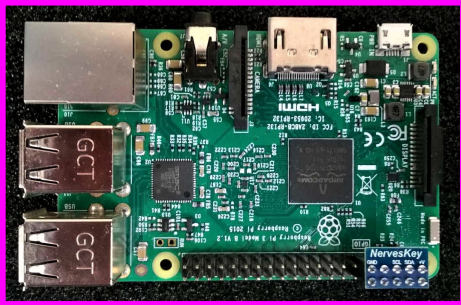


- NOTES -
- 1) I2C PULL-UP RESISTORS MUST BE PROVIDED BY THE HOST DEVICE, AND THEY TYPICALLY ARE ALREADY ON A RASPBERRY PI
 - 2) A UDFN PACKAGE IS EMPLOYED FOR U1 SO THAT THE BOARD CAN BE EASILY MOUNTED ON EITHER SIDE OF THE RASPBERRY PI BOARD AT ONE END OF THE EXPANSION HEADER AND STILL ALLOW THE ASSEMBLY TO FIT IN MOST ENCLOSURES
 - 3) INSTALLING A 5-PIN SINGLE ROW HEADER ALLOWS THE BOARD TO BE CONNECTED VIA A MODULAR BREADBOARD (NOTE THAT BECAUSE THE SECOND ROW IS NOT CONNECTED, A DUAL ROW HEADER CAN ALSO BE USED IN THIS APPLICATION)
 - 4) ALL 4 CONNECTED SIGNALS ARE PRINTED ON THE BOARD

EXAMPLES OF INTENDED APPLICATIONS



DIRECT SOLDER TO THE BOTTOM OF A RASPBERRY PI



CONNECT ON TOP SIDE USING A SOCKET HEADER




SINGLE OR DUAL ROW HEADERS CAN SERVE A BREADBOARD EQUALLY



THANKS TO OUR ELECTRONICS MANUFACTURING COLLABORATORS

NERVESKEY IS AN OPEN HARDWARE DESIGN
MANUFACTURED BY ALLIED COMPONENT WORKS, LLC
WWW.ALLIEDCOMPONENTWORKS.COM
IN COOPERATION WITH CUSTOM ELECTRONICS COMPANY, INC.
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