Assignment

Subject:

DLD

Submitted by:

Fiaz Ahmad

Submitted to:

Syeda Zoupash Zahra

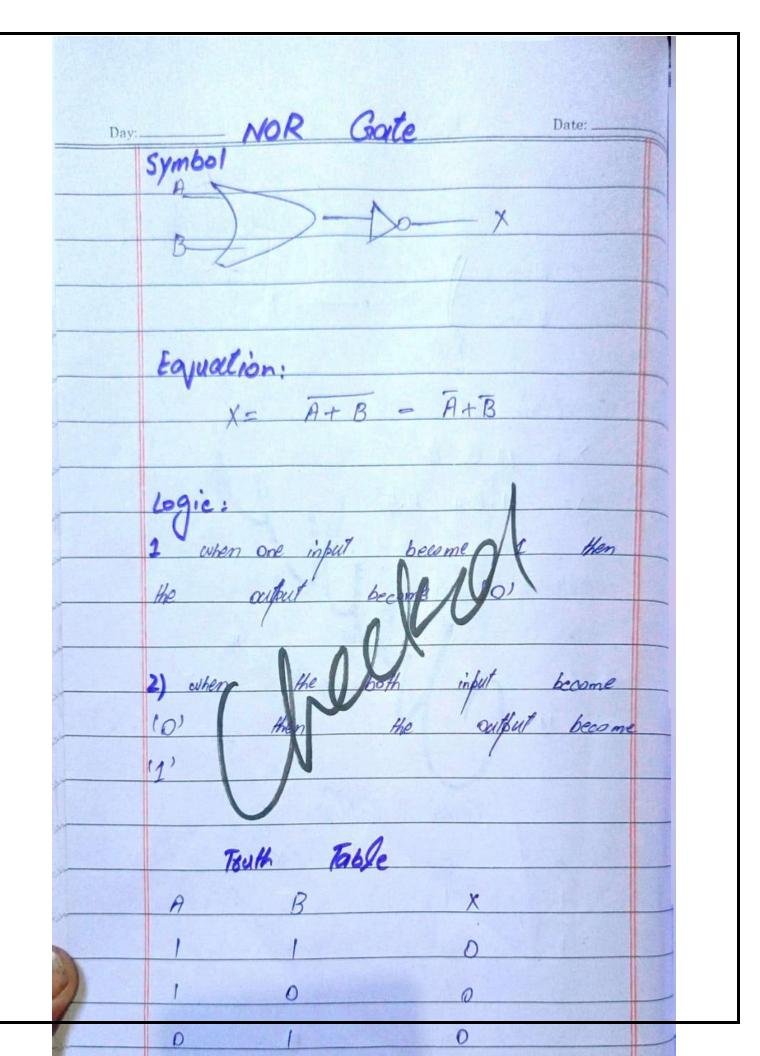
Reg NO:

Fa24-Bse-127

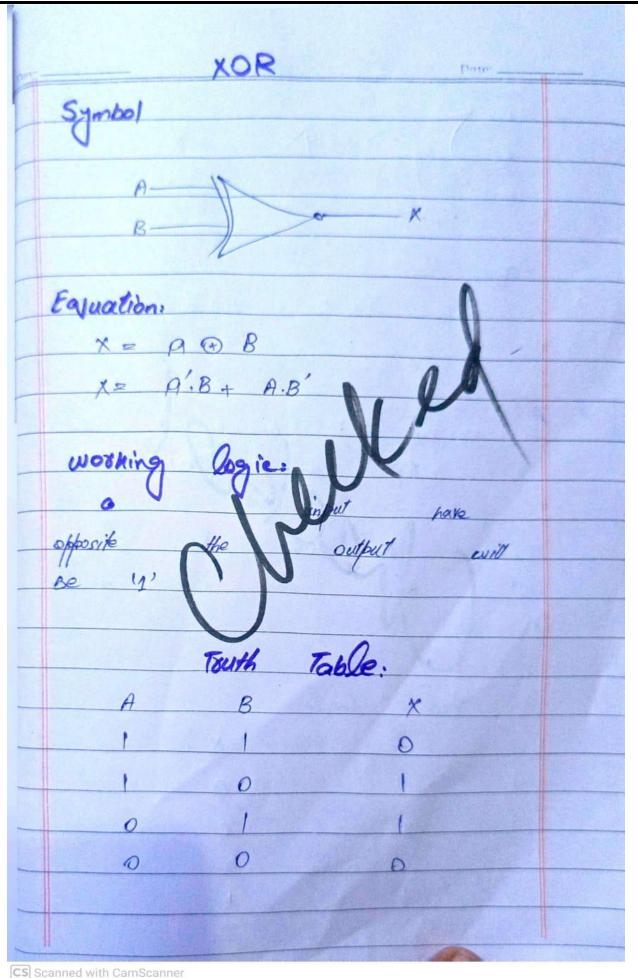
Department:

Computer Science

Comsats University Islamabad (vehari campus)



NAND Gate: Date
Symbol:
1 / Do X
B
Equation,
1
$X = \overline{A} \cdot B = \overline{A} \cdot \overline{B} \wedge A \wedge B \wedge A \wedge A$
10/00
Logic:
input become
O' then the output become
2) when both input become 1 then the output is
when both input become 1 then the output is
10'
Truth Toble.
A B X
0 1
0 1
0 0



Day:		XNOR		Date:
	Symbo	1/1		
	A-	1		X
	8_			<i>X</i>
		16		
	Equali	ion:		
		AOB		
		A.B + A.B	1	
	X	A. D + H. C		
		1	100	
	weaking	logic:	1	
	1	Of on John	He	input
	have	0,000	the out	sut will
	be	N		
		Buth	Table.	
	A	В	X	
		1		
	,	0		
		1	0	
	P	0		
	0			





