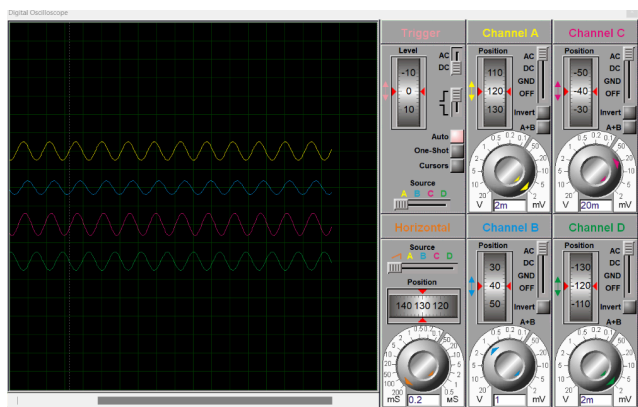
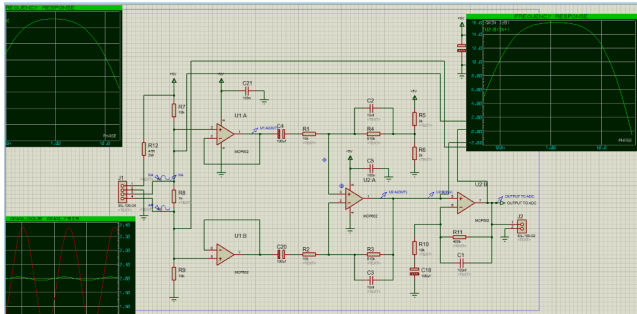


schematic and simulation

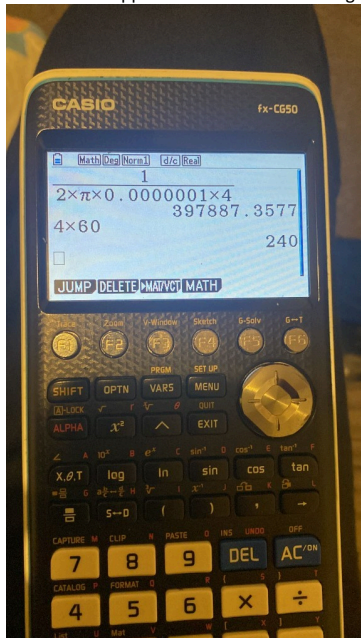
Friday, October 20, 2023 3:26 PM

Here is the GitHub link containing our protus files and our Kamban bord done in excel.
<https://github.com/UniversityOfPlymouth-Electronics/group-project-report-and-demonstration-p1groupm.git>

Here is our simulation results

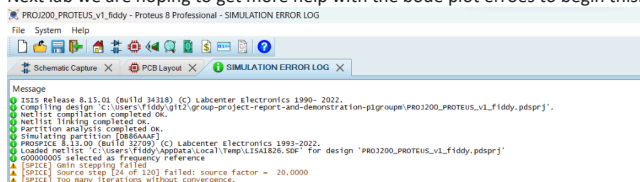


Here are our upper cut off calculations this gives us a cut off of 240bpm



Our plan is to use these numbers which are around the values we need for each resistor and capacitor. Then to observe the bode plot showing the frequency range. Then we will adjust the resistor values to change our cut off frequencies to values we are looking for.

Next lab we are hoping to get more help with the bode plot errors to begin this.



⚠ Simulation failed to start.
❌ Simulation FAILED due to fatal simulator errors.

We discovered the issues was the capacitors r1 and r2 were too low. As such we tweaked this to get acruit results.



Here is our second iteration of the circuit

