

Batch Wafer Quality Summary Report

Report Generated: 2025-12-27 12:39:14

Simulation Date: All Dates

Report Type: Summary Only (Per-Wafer Details Excluded)

Batch Summary

Total wafers: 758

Date Range: 2025-12-21 to 2025-12-27 (3 days)

PASS: 523 | **FAIL:** 235

PASS rate: 69.0%

Top 5 Highest Defect Percentages

- Electrical_ELEC_02_W0018 (Electrical): 98.04% [Near-Full]
- Electrical_ELEC_02_W0035 (Electrical): 98.03% [Near-Full]
- Mechanical_MECH_02_W0041 (Mechanical): 96.48% [Near-Full]
- Thermal_THERM_01_W0032 (Thermal): 95.53% [Near-Full]
- Thermal_THERM_01_W0022 (Thermal): 94.31% [Near-Full]

Distribution by Machine Type

- Electrical: 302 wafers
- Mechanical: 261 wafers
- Thermal: 195 wafers

Distribution by Defect Class

- Normal: 519 wafers
- Donut: 37 wafers
- Edge-Ring: 35 wafers
- Local: 34 wafers
- Edge-Loc: 33 wafers
- Random: 31 wafers
- Near-Full: 25 wafers
- Scratch: 25 wafers
- Center: 19 wafers