

Références

- **[Trama, 25]** Trama, D., Boudguiga, A., Clet, P.-E., Sirdey, R., & Ye, N. (2025). **Designing a General-Purpose 8-bit (T)FHE Processor Abstraction**. IACR Transactions on Cryptographic Hardware and Embedded Systems, 2025(2), 535-578. <https://doi.org/10.46586/tches.v2025.i2.535-578>
- **[Azogagh, 25]** Azogagh, S., Killijian, M.-O., Larose-Gervais, F. (2025). **A non-comparison oblivious sort and its application to private k-NN**. PoPETs Proceedings on Privacy Enhancing Technologies Symposium, 2025(3), 156-169. <https://doi.org/10.56553/popets-2025-0093>
- **[Guimarães, 21]** Guimarães, A., Borin, E., & Aranha, D. F. . (2021). **Revisiting the functional bootstrap in TFHE**. IACR Transactions on Cryptographic Hardware and Embedded Systems, 2021(2), 229-253. <https://doi.org/10.46586/tches.v2021.i2.229-253>

Merci!