Optimisation of a Linear Algebra Approach to OLAP

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Abstract

Online Analytical processing (OLAP) systems, perform multidimensional analysis of business data and provides the capability for complex calculations, trend analysis, and sophisticated data modeling. All the referred analysis depends on Relational Algebra, which lack algebraic properties, and qualitative and quantitative proofs for all the relational operator. The proposed solution focus on a typed linear algebra approach, encoding OLAP functionality solely in terms of Linear Algebra operations (matrices).

It has been argued that linear algebra (LA) is better suited than standard relational algebra for formalizing and implementing queries in on-line multidimensional data analysis [2] [1]. This can be achieved over a small LA sparse matrix kernel which, further to multiplication and transposition, offers the Kronecker, Khatri-Rao and Hadamard products.

I. Introduction

In a initial fase we implemented from the start a typed linear algebra solution, and evaluate its performance in a real word scenario, using different datasets. The datasets was produced with TPC-H Benchmark, was produced multiples datasets with different data sizes (1, 2, 4, 8, 16, 32 Gb) in order to obtain a realistic results.

System compute-652-1 # CPUs CPU Intel® Xeon® E5-2670v2 Ivy Bridge Architecture # Cores per CPU 10 # Threads per CPU Clock Freq. 2.5 GHz 320KB L1 Cache 32KB per core 2560KB L2 Cache 256KB per core 25600KB L3 Cache shared SSE4.2 & AVX Inst. Set Ext. #Memory Channels Vendors Announced 59.7 GB/s Peak Memory BW Measured¹ Peak 58.5GB/s Memory BW

Table 1: Architectural characteristics of the two evaluation platforms.

II. HARDWARE CHARACTERIZATION

The platform used by us for our study at Search6 is a dual-socket system equipped with two $Intel^{\circledR}$ Ivy Bridge processors. The system, referenced as compute node 652-1, has two $Intel^{\circledR}$ Xeon $^{\circledR}$ E5-2670v2 (Ivy Bridge architecture) and features 64 GB of DDR3 RAM, supported at a frequency of XXXX MHz divided in 4 memory channels.

REFERENCES

- [1] Rogério António da Costa Pontes. Benchmarking a linear algebra approach to olap master thesis. 2015.
- [2] Hugo Daniel Macedo and José Nuno Oliveira. A linear algebra approach to olap. *Formal Aspects of Computing*, 27(2):283–307, 2015.