

EE2020 FPGA Design Project

REPORT GUIDELINES

Submission Instructions :

- File Name : *Official lab day_Name of any one team member as indicated on the matriculation card_Matriculation number 1_Matriculation number 2_Report.pdf*
Eg. Monday_Harry Nyquist_A0123456Z_A0010101Z_Report.pdf
- Format : PDF

Submission Deadlines :

- Monday Lab Group : Project Report Deadline **13 Nov Monday 2359**
- Tuesday Lab Group : Project Report Deadline **14 Nov Tuesday 2359**

Include the following in your report :

- Name, Matriculation number, Lab Group (Monday / Tuesday / Thursday / Friday)
- User Guide
 - List the features that you have designed and implemented.
 - Provide a user guide on how these features can be operated by the user. (eg. Flowchart / eg Table format shown below)

Feature	Input	Description	Output Display
Hello Playback	SW[3]	Select this switch to playback "Hello" continuously.	Playback frequency
	+PB_C	Description: Pause / Resume	Pause / Resume indicator

- Describe the key features that have been designed and implemented. (append code segments to aid you in your description, if necessary).
 - Clearly indicate the designer(s) (Student 1 / Student 2 / Both Students) of each feature.
- Feedback:
 - 1) What did you like most 😊 / least ☹ about the project?
 - 2) How would you suggest the overall project assignment be improved?
 - 3) Any other constructive feedback / suggestions are welcome.

Note that feedbacks, whether positive or negative, DO NOT have any effects on your grades 😊

- References: Include references to sources that you may have obtained code / ideas from.