

host# lspci -vv -s 03:00.3

03:00.3 Ethernet controller: Intel Corporation Ethernet Controller XL710 for 40GbE QSFP+ (rev 02)

Subsystem: XXXXXX 10 Gb/40 Gb Ethernet Adapter

Physical Slot: 3

Control: I/O- Mem+ BusMaster+ SpecCycle- MemWINV- VGASnoop- ParErr- Stepping- SERR+ FastB2B- DisINTx+

Status: Cap+ 66MHz- UDF- FastB2B- ParErr- DEVSEL=fast >TAbort- <TAbort- <MAbort- >SERR- <PERR- INTx-

Latency: 0, Cache Line Size: 32 bytes

Interrupt: pin A routed to IRQ 24

NUMA node: 0

Region 0: Memory at c1800000 (64-bit, prefetchable) [size=8M]

—————→ Bar 0 is MMIO

Region 3: Memory at c4000000 (64-bit, prefetchable) [size=32K]

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Capabilities: [70] MSI-X: Enable+ Count=129 Masked-

Vector table: BAR=3 offset=00000000 —————→

BAR 3 MSI-X vector table is IO  
(trap/emulate)

PBA: BAR=3 offset=00001000 ↘

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BAR 3 MSI-X PBA table is MMIO