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**CPU Guest
Mode**

IO instruction to
access
pci config

MMIO to access
BAR 1 to register
DMA ring buffer

MMIO to access BAR 2
to kick doorbell

**CPU Host
(regular) Mode**

IO is trapped
and emulated
by hypervisor

MMIO is trapped and
emulated by hypervisor
as based on EPT config

MMIO is trapped and
emulated by hypervisor
as based on EPT config

