

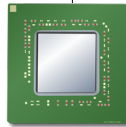
Domain 0

PVM

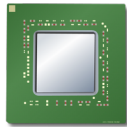
HVM

PVHVM

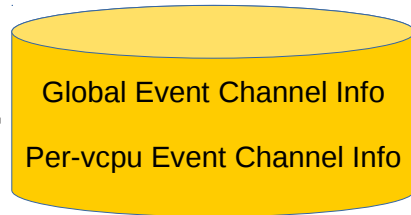
*xen_evtchn_do_upcall
will traverse and handle
each pending event*



vcpu

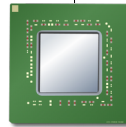


vcpu

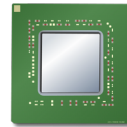


*set eip to
xen_hypervisor_callback
during scheduling
if vcpu has pending event*

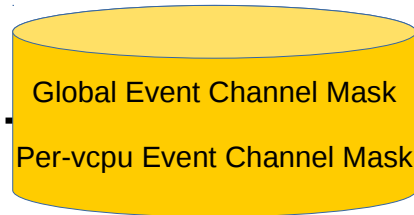
*xen_evtchn_do_upcall
will traverse and handle
each pending event*



vcpu

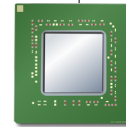


vcpu

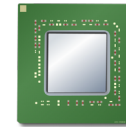


*set eip to
xen_hypervisor_callback
during scheduling
if vcpu has pending event*

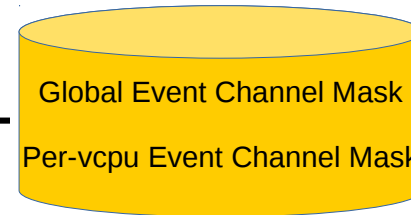
*Guest will handle interrupt
as native machine*



vcpu

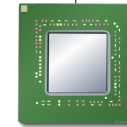


vcpu

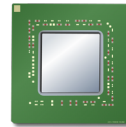


*Intel-vt based
interrupt injection and
one vector for each irq*

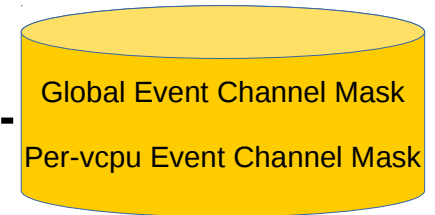
*xen_evtchn_do_upcall
will traverse and handle
each pending event*



vcpu



vcpu



IRQ handler for vector 0xf3 is called

*Intel-vt based
interrupt injection and
vector 0xf3 for each event*

Xen Hypervisor