

Arithmetic				
<b>add</b>	<b>r s t</b>	Add	$r = s + t$	<i>e</i>
<b>addi</b>	<b>r s i</b>	Add immediate	$r = s + \text{SignExt}(i)$	<i>e</i>
<b>addiu</b>	<b>r s i</b>	Add immediate unsigned	$r = s + \text{SignExt}(i)$	
<b>addu</b>	<b>r s t</b>	Add unsigned	$r = s + t$	
<b>div</b>	<b>r s t</b>	Divide	$r = s / t$	<i>p</i>
<b>divu</b>	<b>r s t</b>	Divide unsigned	$r = s / t$	<i>p</i>
<b>mul</b>	<b>r s t</b>	Multiply	$r = s * t$	
<b>neg</b>	<b>r s</b>	Negate	$r = -s$	<i>e p</i>
<b>negu</b>	<b>r s</b>	Negate unsigned	$r = -s$	<i>p</i>
<b>sub</b>	<b>r s t</b>	Subtract	$r = s - t$	<i>e</i>
<b>subu</b>	<b>r s t</b>	Subtract unsigned	$r = s - t$	
Logic				
<b>and</b>	<b>r s t</b>	And	$r = s \& t$	
<b>andi</b>	<b>r s i</b>	And immediate	$r = s \& \text{ZeroExt}(i)$	
<b>nor</b>	<b>r s t</b>	Nor	$r = \sim(s \mid t)$	
<b>not</b>	<b>r s</b>	Not	$r = \sim s$	<i>p</i>
<b>or</b>	<b>r s t</b>	Or	$r = s \mid t$	
<b>ori</b>	<b>r s i</b>	Or immediate	$r = s \mid \text{ZeroExt}(i)$	
<b>xor</b>	<b>r s t</b>	Exclusive or	$r = s \wedge t$	
<b>xori</b>	<b>r s i</b>	Exclusive or immediate	$r = s \wedge \text{ZeroExt}(i)$	
Shifting				
<b>sll</b>	<b>r s i</b>	Shift left logical	$r = s \ll i$	
<b>sllv</b>	<b>r s t</b>	Shift left logical variable	$r = s \ll t$	
<b>sra</b>	<b>r s i</b>	Shift right arithmetic	$r = \text{SignExt}(s \gg i)$	
<b>srav</b>	<b>r s t</b>	Shift right arithmetic variable	$r = \text{SignExt}(s \gg t)$	
<b>srl</b>	<b>r s i</b>	Shift right logical	$r = \text{ZeroExt}(s \gg i)$	
<b>srlv</b>	<b>r s t</b>	Shift right logical variable	$r = \text{ZeroExt}(s \gg t)$	
Loading and storing				
<b>la</b>	<b>r a</b>	Load address	$r = a$	<i>p</i>
<b>lb</b>	<b>r a</b>	Load byte	$r = \text{SignExt}(*(int8*)a)$	
<b>lbu</b>	<b>r a</b>	Load byte unsigned	$r = \text{ZeroExt}(*(int8*)a)$	
<b>lh</b>	<b>r a</b>	Load halfword	$r = \text{SignExt}(*(int16*)a)$	
<b>lhu</b>	<b>r a</b>	Load halfword unsigned	$r = \text{ZeroExt}(*(int16*)a)$	
<b>li</b>	<b>r i</b>	Load immediate	$r = \text{SignExt}(i)$	<i>p</i>
<b>lui</b>	<b>r i</b>	Load upper immediate	$r = i \ll 16$	
<b>lw</b>	<b>r a</b>	Load word	$r = *(int32*)a$	
<b>move</b>	<b>r s</b>	Move	$r = s$	<i>p</i>
<b>sb</b>	<b>r a</b>	Store byte	$*(int8*)a = r \& 0xFF$	
<b>sh</b>	<b>r a</b>	Store halfword	$*(int16*)a = r \& 0xFFFF$	
<b>sw</b>	<b>r a</b>	Store word	$*(int32*)a = r$	

Comparison				
<b>seq</b>	<b>r s t</b>	Set equal	$r = (s == t) ? 1 : 0$	<i>p</i>
<b>sge</b>	<b>r s t</b>	Set greater than or equal	$r = (s \geq_s t) ? 1 : 0$	<i>p</i>
<b>sgeu</b>	<b>r s t</b>	Set greater than or equal unsigned	$r = (s \geq_u t) ? 1 : 0$	<i>p</i>
<b>sgt</b>	<b>r s t</b>	Set greater than	$r = (s >_s t) ? 1 : 0$	<i>p</i>
<b>sgtu</b>	<b>r s t</b>	Set greater than unsigned	$r = (s >_u t) ? 1 : 0$	<i>p</i>
<b>sle</b>	<b>r s t</b>	Set less than or equal	$r = (s \leq_s t) ? 1 : 0$	<i>p</i>
<b>sleu</b>	<b>r s t</b>	Set less than or equal unsigned	$r = (s \leq_u t) ? 1 : 0$	<i>p</i>
<b>sne</b>	<b>r s t</b>	Set not equal	$r = (s != t) ? 1 : 0$	<i>p</i>
<b>slt</b>	<b>r s t</b>	Set less than	$r = (s <_s t) ? 1 : 0$	
<b>slti</b>	<b>r s i</b>	Set less than immediate	$r = (s <_s i) ? 1 : 0$	
<b>sltiu</b>	<b>r s i</b>	Set less than immediate unsigned	$r = (s <_u i) ? 1 : 0$	
<b>sltu</b>	<b>r s t</b>	Set less than unsigned	$r = (s <_u t) ? 1 : 0$	
Branching and jumping				
<b>beq</b>	<b>r s L</b>	Branch equal	$\text{if } (r == s) \text{ goto } L$	
<b>beqz</b>	<b>r L</b>	Branch equal to zero	$\text{if } (r == 0) \text{ goto } L$	<i>p</i>
<b>bge</b>	<b>r s L</b>	Branch greater than or equal	$\text{if } (r \geq_s s) \text{ goto } L$	<i>p</i>
<b>bgeu</b>	<b>r s L</b>	Branch greater than or equal unsigned	$\text{if } (r \geq_u s) \text{ goto } L$	<i>p</i>
<b>bgez</b>	<b>r L</b>	Branch greater than or equal to zero	$\text{if } (r \geq_s 0) \text{ goto } L$	
<b>bgt</b>	<b>r s L</b>	Branch greater than	$\text{if } (r >_s s) \text{ goto } L$	<i>p</i>
<b>bgtu</b>	<b>r s L</b>	Branch greater than unsigned	$\text{if } (r >_u s) \text{ goto } L$	<i>p</i>
<b>bgtz</b>	<b>r L</b>	Branch greater than zero	$\text{if } (r >_s 0) \text{ goto } L$	
<b>ble</b>	<b>r s L</b>	Branch less than or equal	$\text{if } (r \leq_s s) \text{ goto } L$	<i>p</i>
<b>bleu</b>	<b>r s L</b>	Branch less than or equal unsigned	$\text{if } (r \leq_u s) \text{ goto } L$	<i>p</i>
<b>blez</b>	<b>r L</b>	Branch less than or equal to zero	$\text{if } (r \leq_s 0) \text{ goto } L$	
<b>blt</b>	<b>r s L</b>	Branch less than	$\text{if } (r <_s s) \text{ goto } L$	<i>p</i>
<b>bltu</b>	<b>r s L</b>	Branch less than unsigned	$\text{if } (r <_u s) \text{ goto } L$	<i>p</i>
<b>bltz</b>	<b>r L</b>	Branch less than zero	$\text{if } (r <_s 0) \text{ goto } L$	
<b>bne</b>	<b>r s L</b>	Branch not equal	$\text{if } (r != s) \text{ goto } L$	
<b>bnez</b>	<b>r L</b>	Branch not equal to zero	$\text{if } (r != 0) \text{ goto } L$	<i>p</i>
<b>j</b>	<b>L</b>	Jump	$\text{goto } L$	
<b>jal</b>	<b>L</b>	Jump and link	$\$ra = pc+4; \text{ goto } L$	
<b>jalr</b>	<b>r</b>	Jump and link register	$\$ra = pc+4; \text{ goto } r$	
<b>jr</b>	<b>r</b>	Jump register	$\text{goto } r$	

$<_s / <_u$ : signed/unsigned comparisons    *e*: may cause overflow exception    *p*: pseudoinstruction

<b>\$0</b>	Constant zero
<b>\$v0, \$v1</b>	Return values
<b>\$a0 – \$a3</b>	Arguments
<b>\$t0 – \$t9</b>	Temporaries
<b>\$s0 – \$s7</b>	Saved temporaries <i>preserved</i>

<b>\$at, \$k0, \$k1</b>	Reserved
<b>\$gp</b>	Global pointer <i>preserved</i>
<b>\$sp</b>	Stack pointer <i>preserved</i>
<b>\$fp</b>	Frame pointer <i>preserved</i>
<b>\$ra</b>	Return address <i>preserved</i>