

# Embedded Systems Praktikum 1

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## 1 Statechart

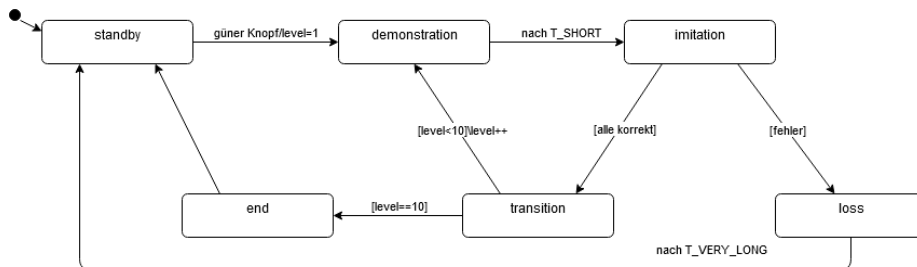


Abbildung 1: State Machine Diagramm (Simpel)

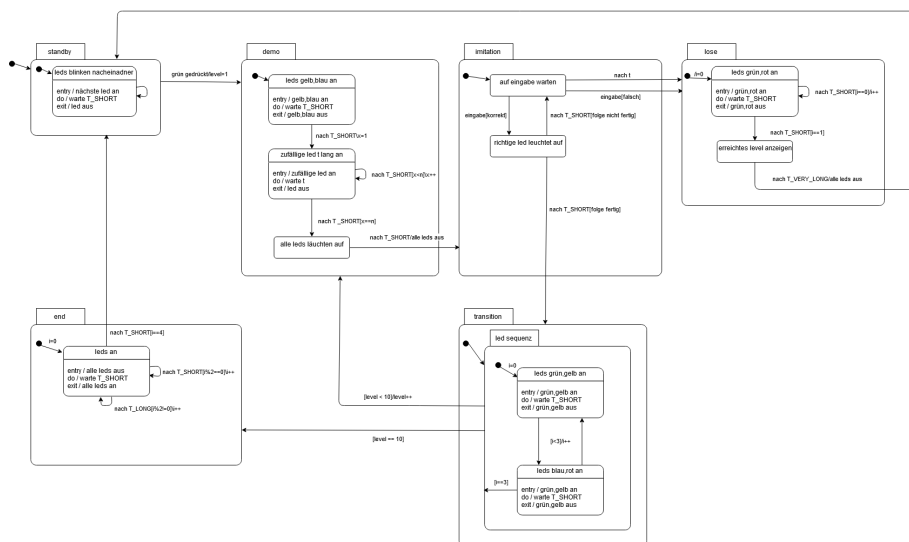


Abbildung 2: State Machine Diagramm

## 2 Schaltplan

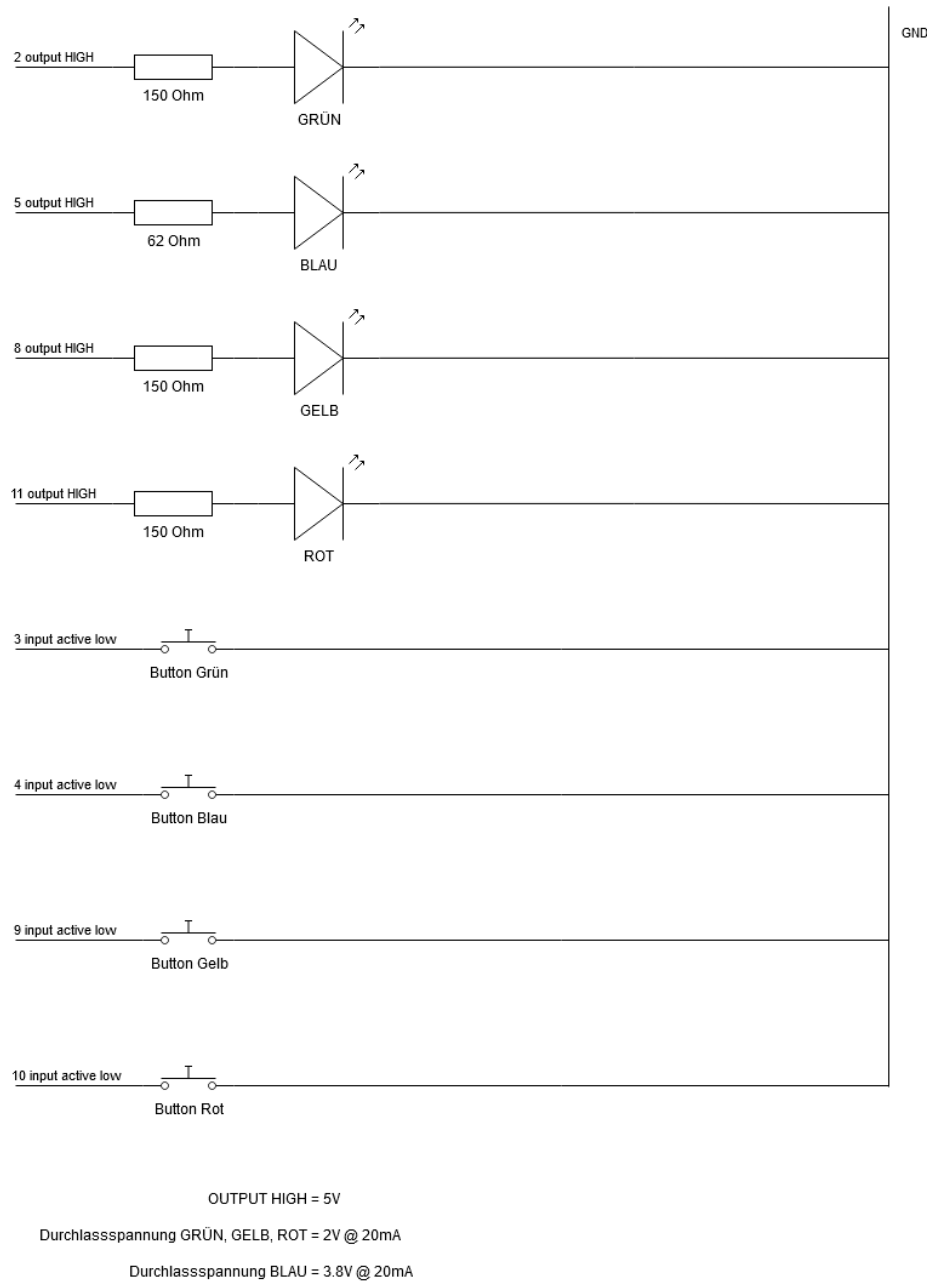


Abbildung 3: Schaltplan