# Embedded Systems Praktikum 1

#### Finn Bechinka

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### 1 Statechart

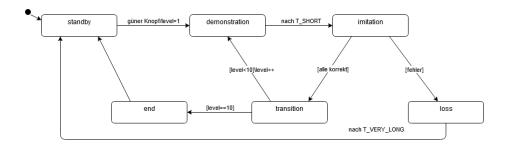


Abbildung 1: State Machine Diagramm (Simpel)

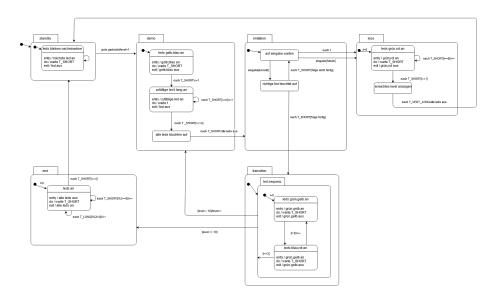
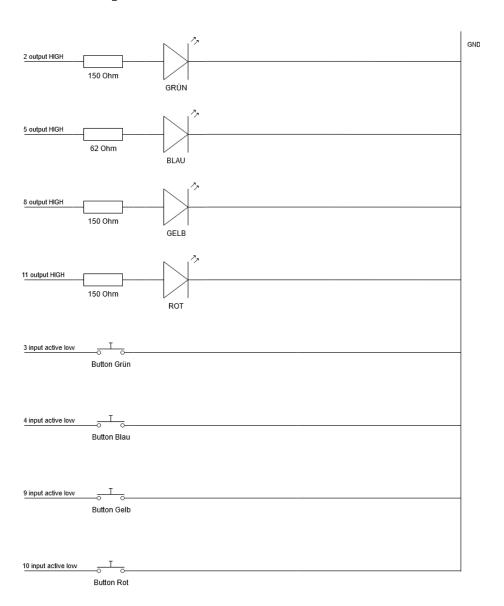


Abbildung 2: State Machine Diagramm

## 2 Schaltplan



OUTPUT HIGH = 5V  $\label{eq:output}$  Durchlassspannung GRÜN, GELB, ROT = 2V @ 20mA  $\label{eq:output}$  Durchlassspannung BLAU = 3.8V @ 20mA

Abbildung 3: Schaltplan