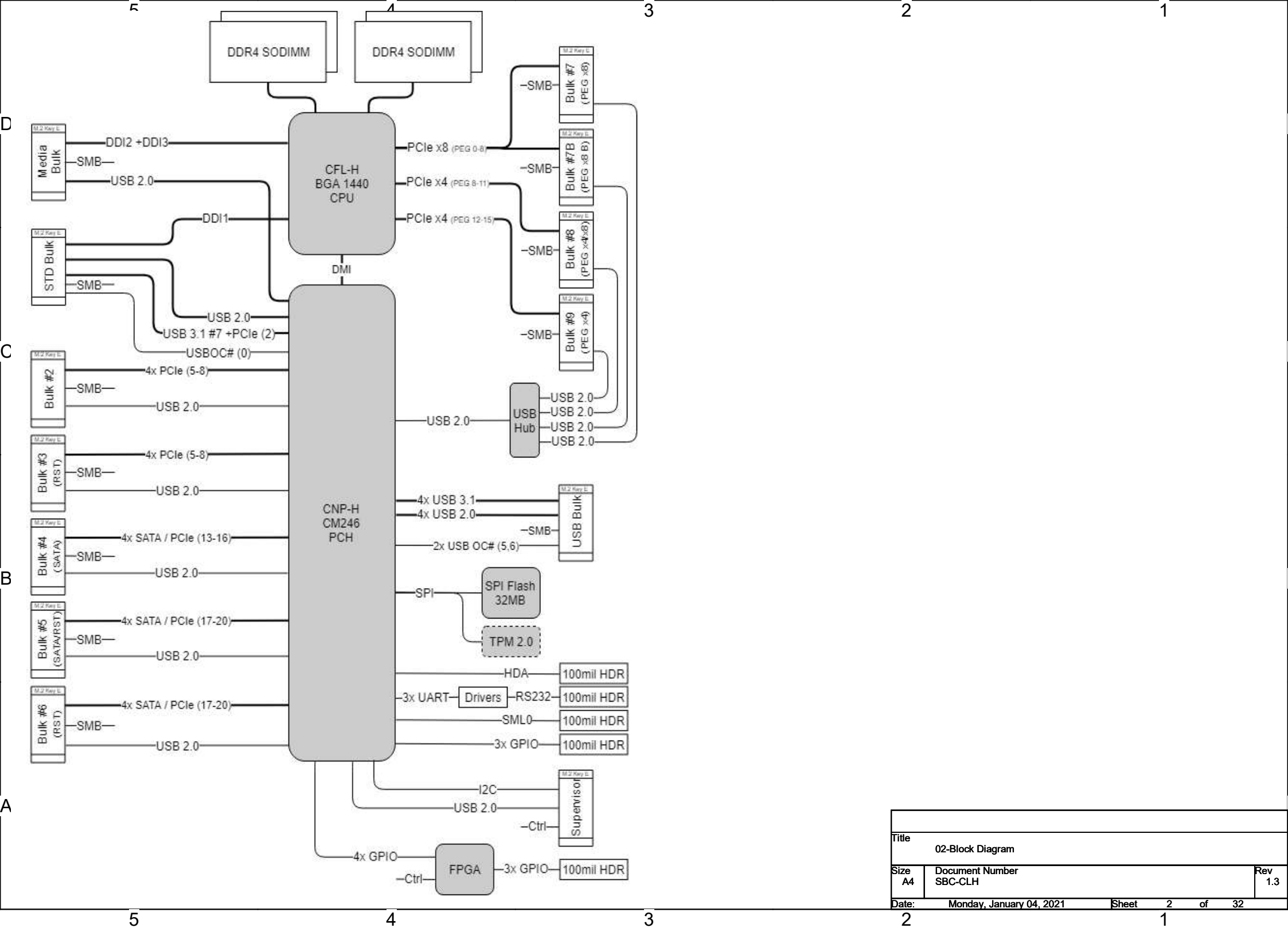


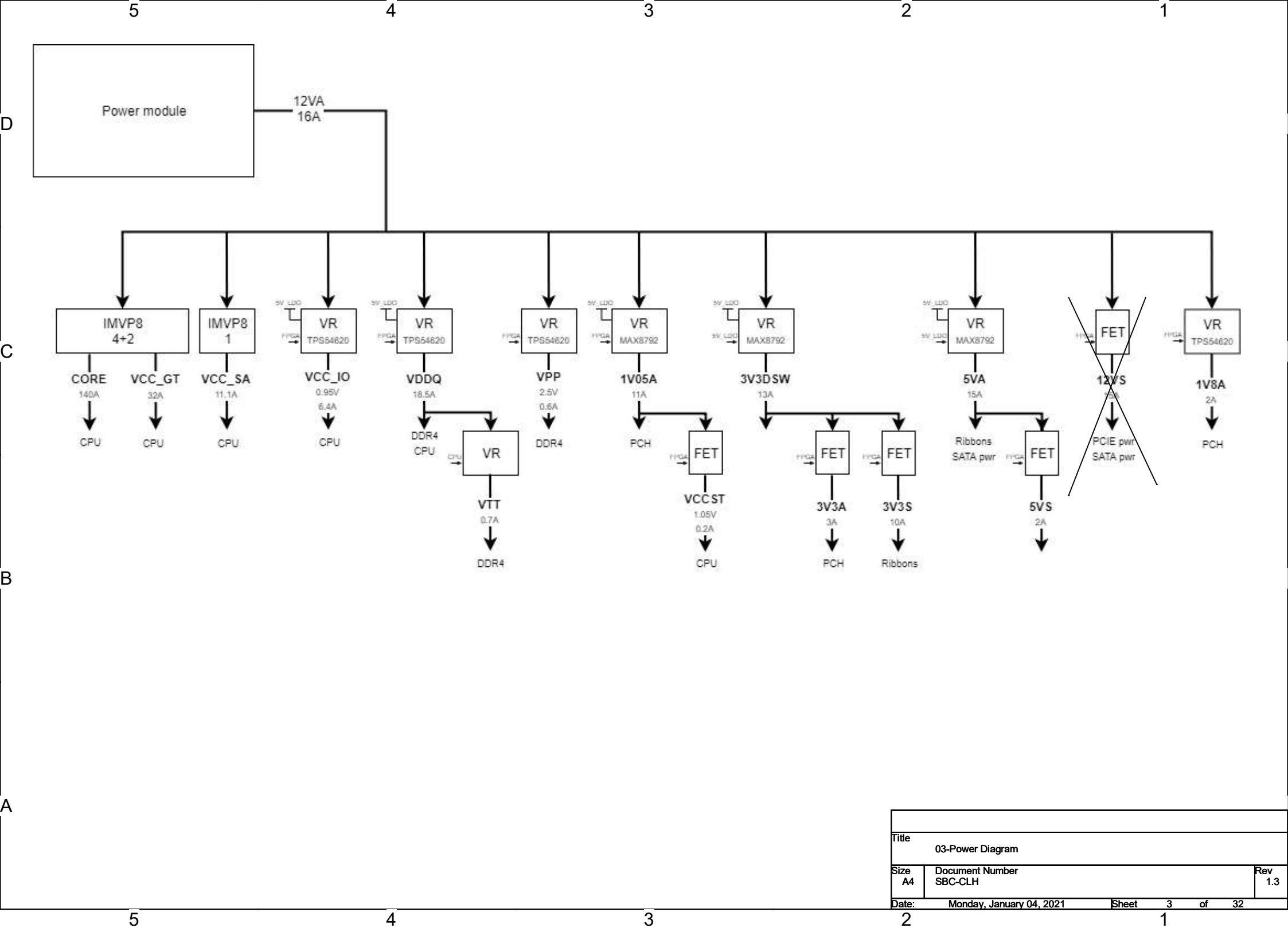
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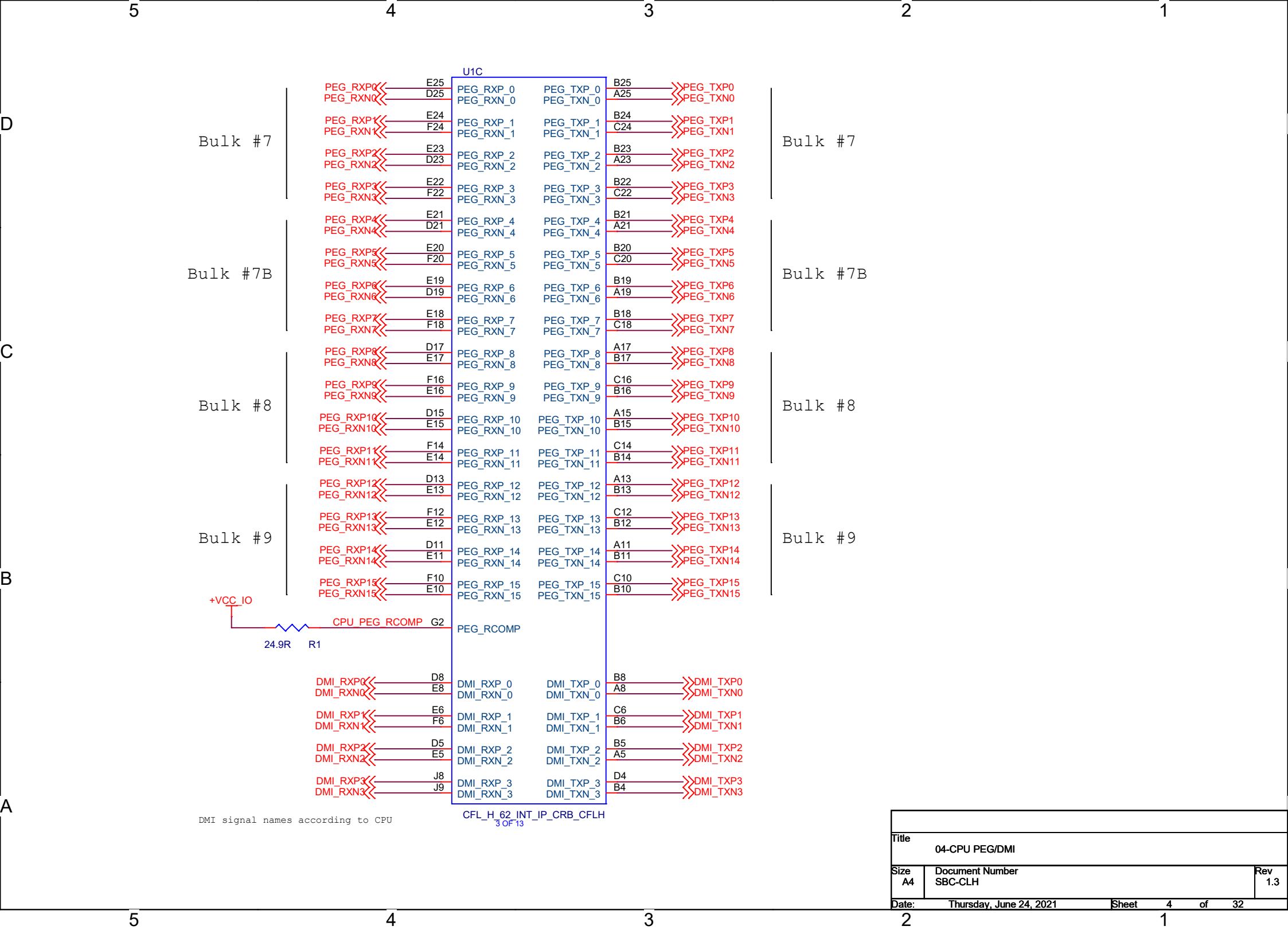
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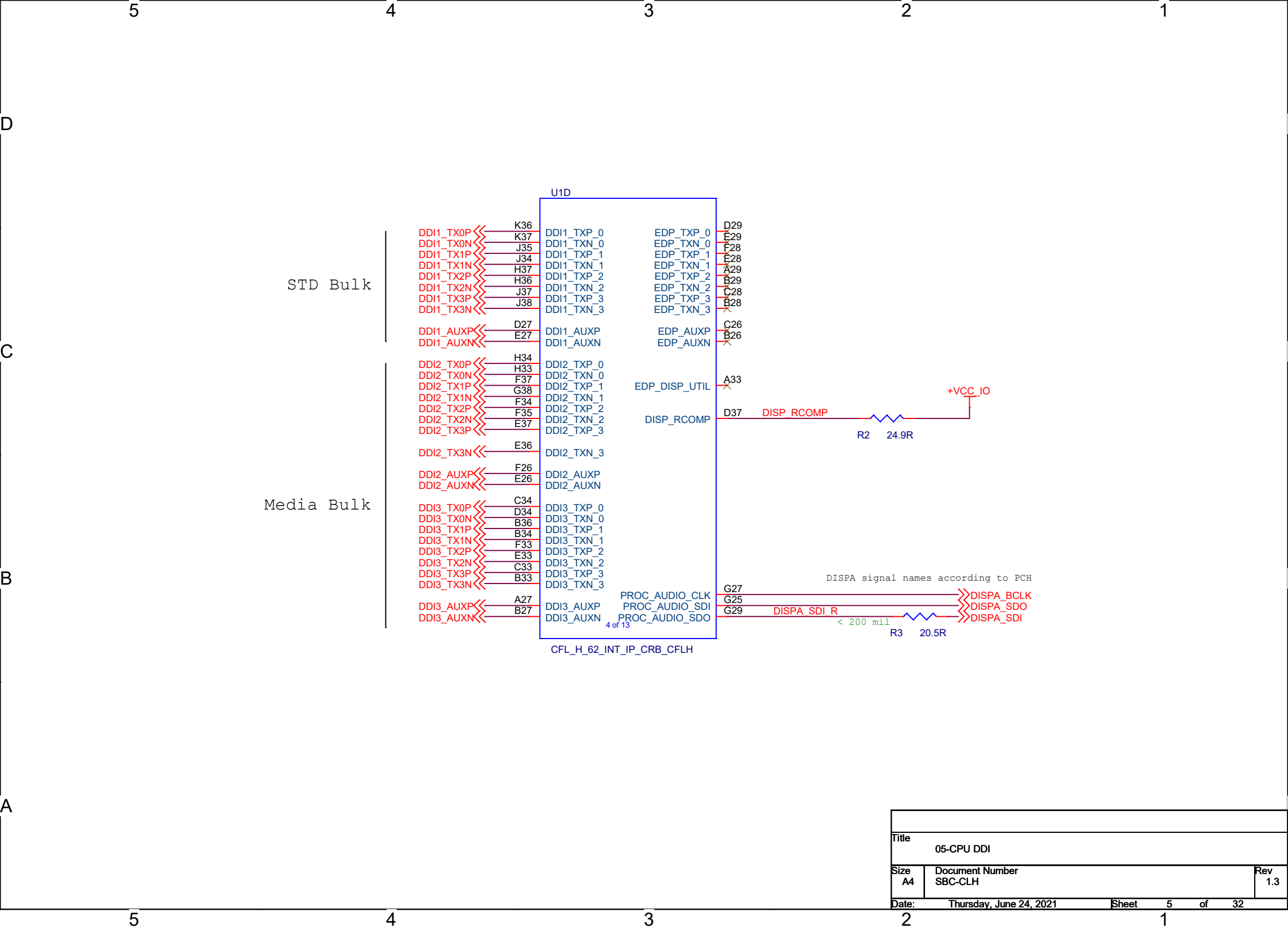
CM246 PCH Flexible I/O																CPU PEG																CPU Graphics																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																										
USB 3.1 #1 Gen1/Gen2	USB 3.1 #2 Gen1/Gen2	USB 3.1 #3 Gen1/Gen2	USB 3.1 #4 Gen1/Gen2	USB 3.1 #5 Gen1/Gen2	USB 3.1 #6 Gen1/Gen2	PCIe #5	PCIe #6	PCIe #7	PCIe #8	PCIe #9	PCIe #10	PCIe #11	PCIe #12	PCIe #13	PCIe #14	PCIe #15	PCIe #16	PCIe #17	PCIe #18	PCIe #19	PCIe #20	PCIe #21	PCIe #22	PCIe #23	PCIe #24	PCIe #0	PCIe #1	PCIe #2	PCIe #3	PCIe #4	PCIe #5	PCIe #6	PCIe #7	PCIe #8	PCIe #9	PCIe #10	PCIe #11	PCIe #12	PCIe #13	PCIe #14	PCIe #15	DDI 3	DDI 2	DDI 1	DDI 0 - eDP																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																													
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02-Block Diagram		
Size	Document Number	Rev
A4	SBC-CLH	1.3
Date:	Monday, January 04, 2021	Sheet 2 of 32



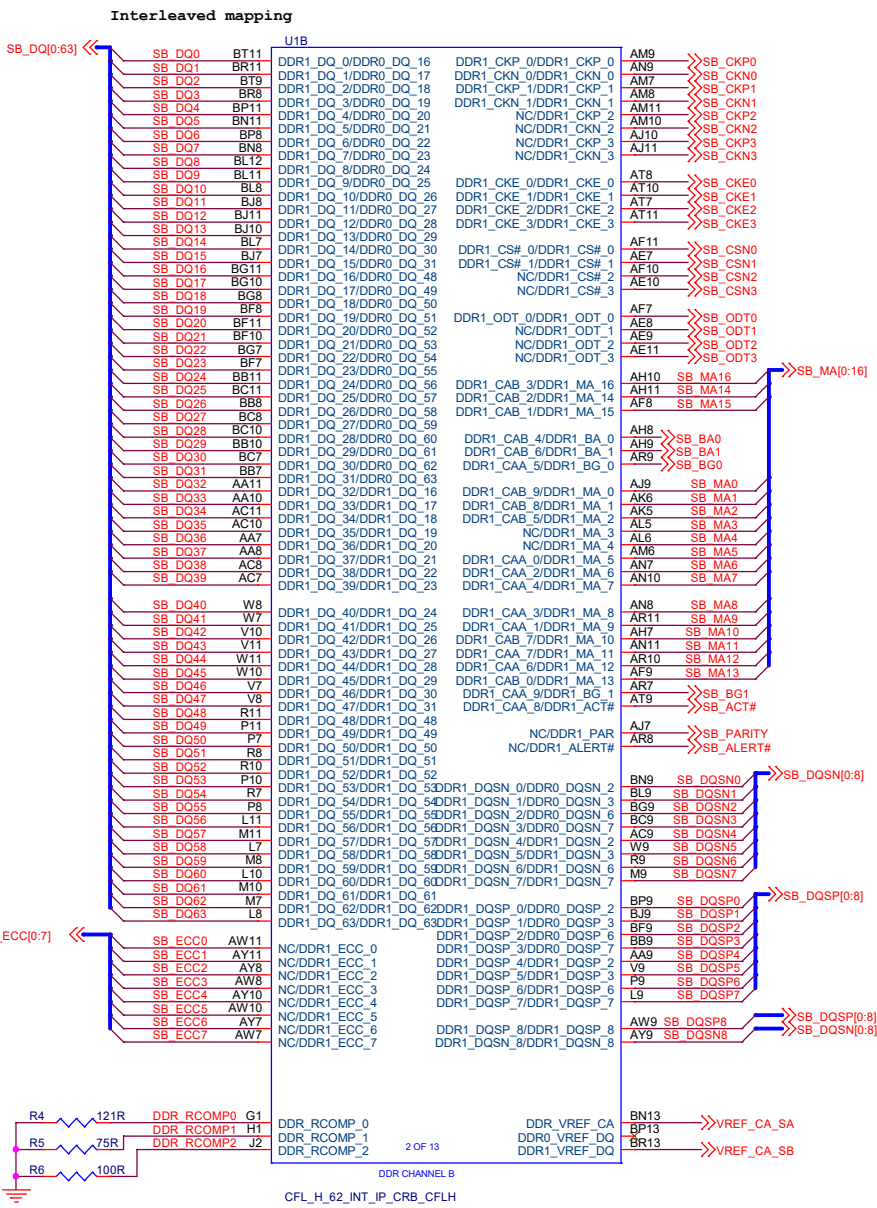
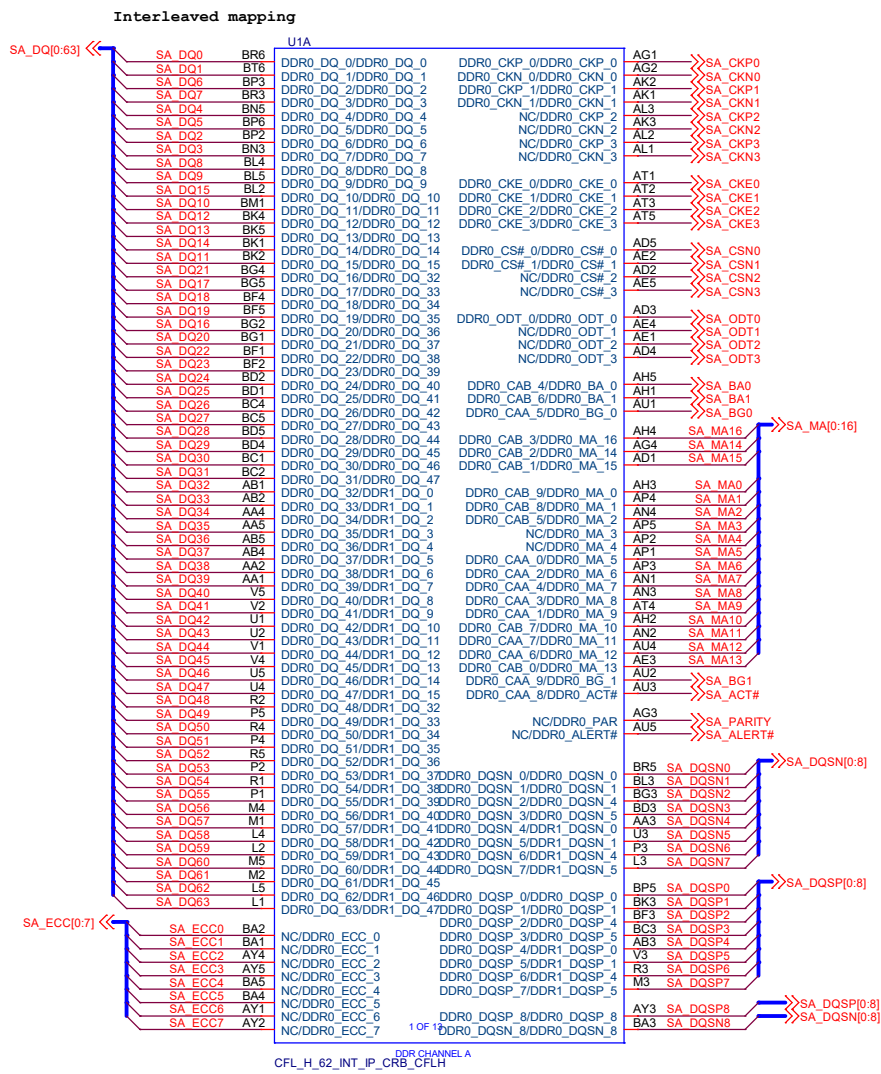




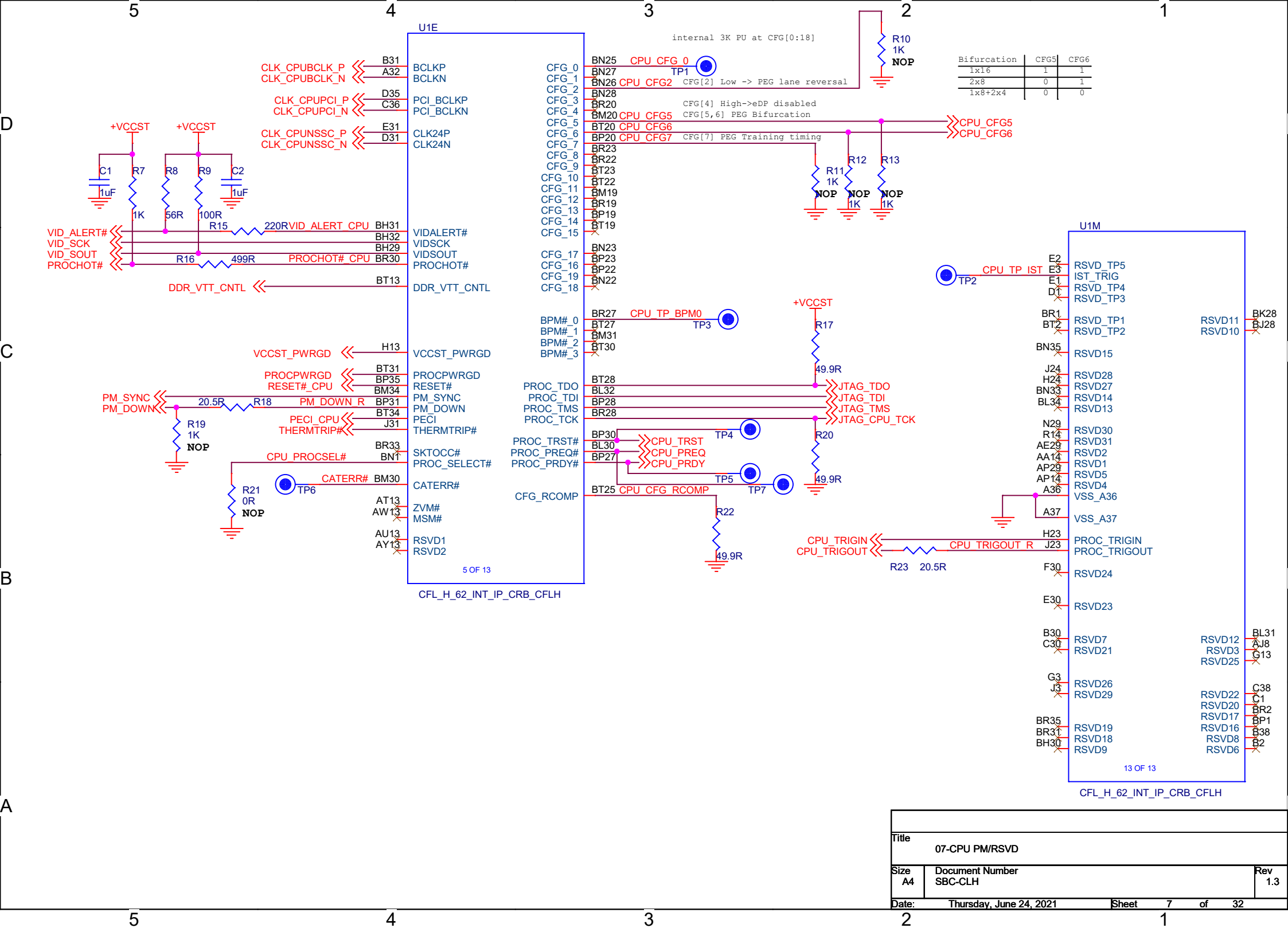
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Date:	Thursday, June 24, 2021	Sheet 5 of 32

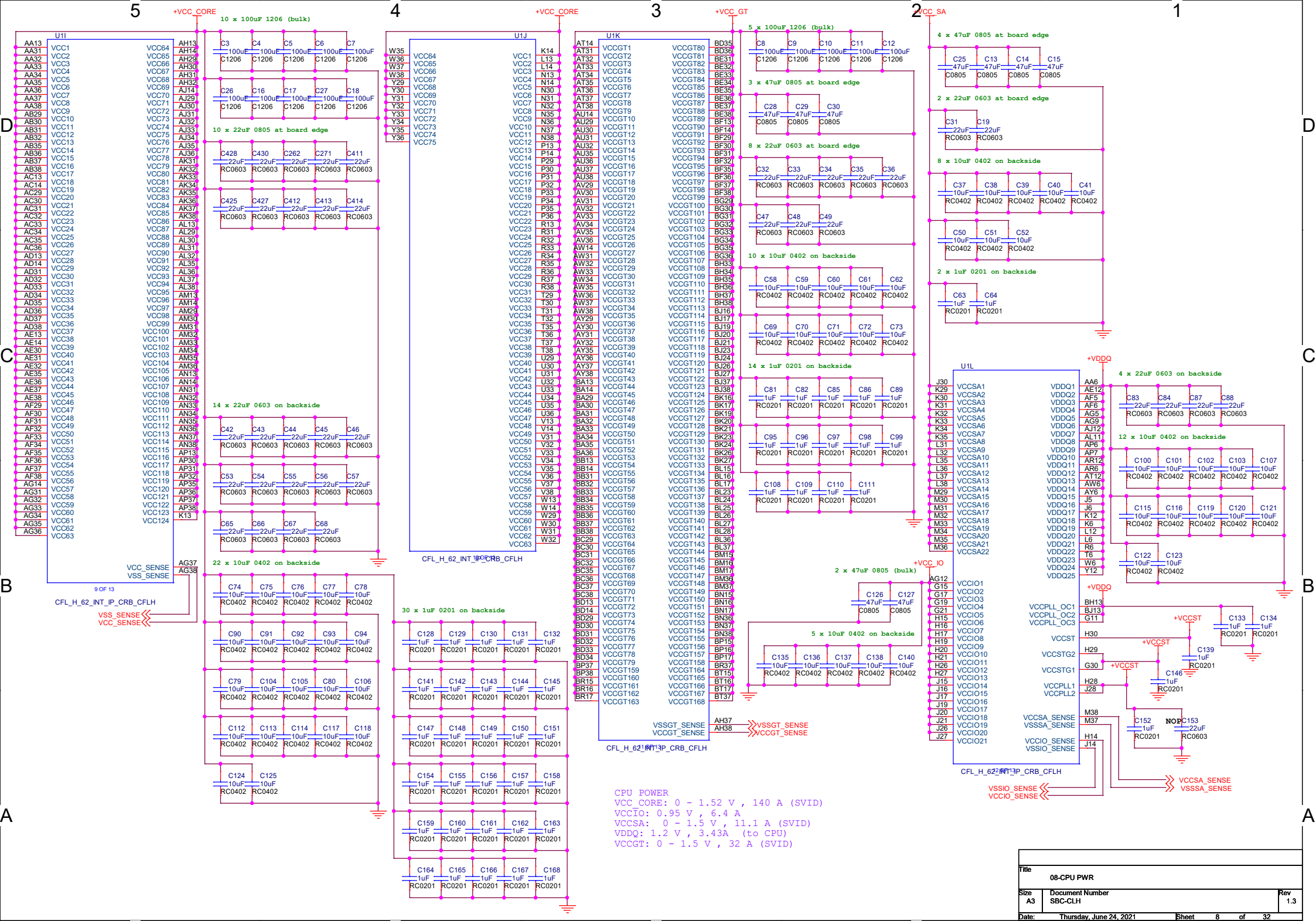
Data bits can be swapped within a byte

Data bits can be swapped within a byte

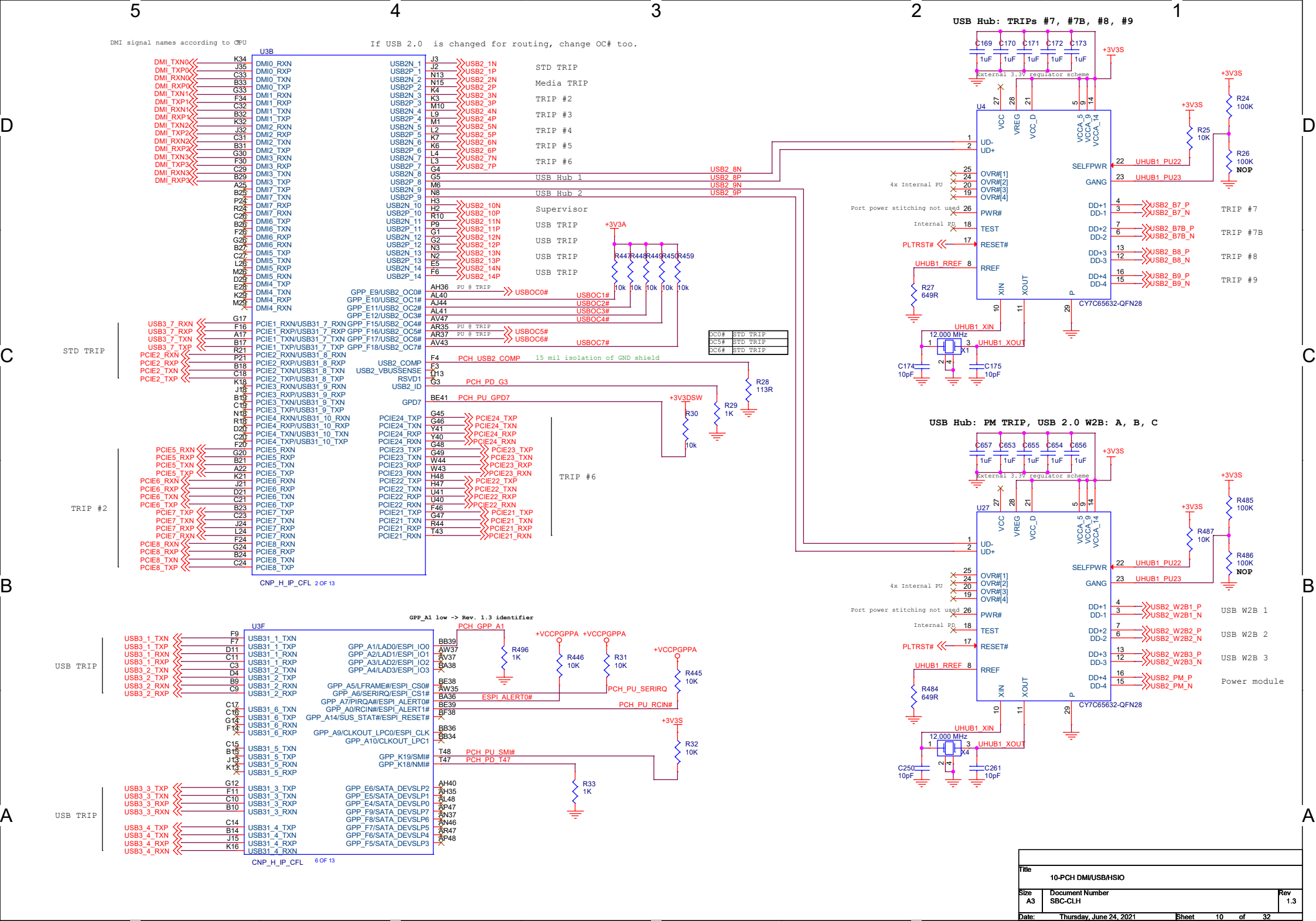


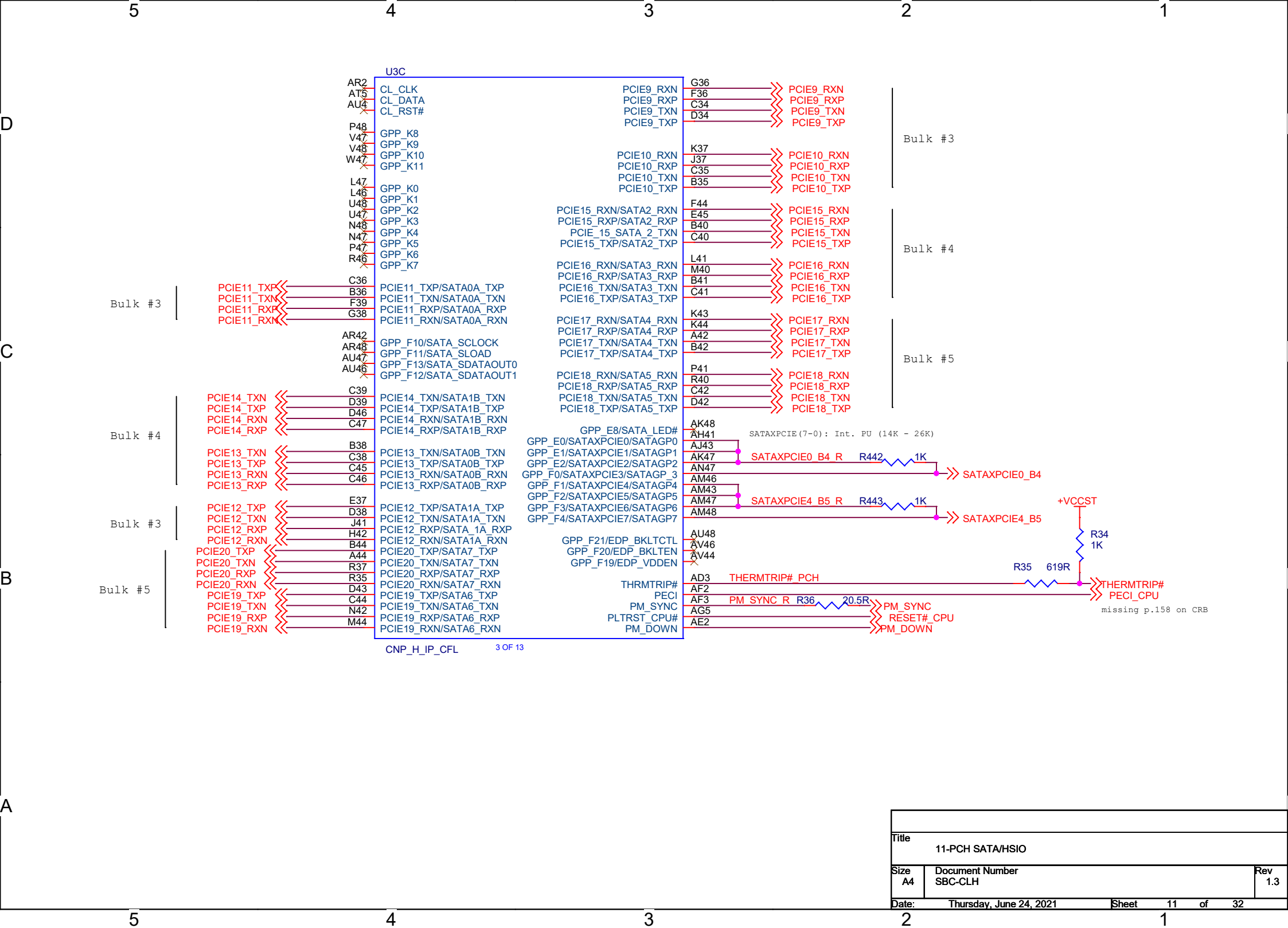
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Size	Document Number	Rev
A3	SBC-CLH	1.3
Date:	Thursday, June 24, 2021	Sheet 6 of 32

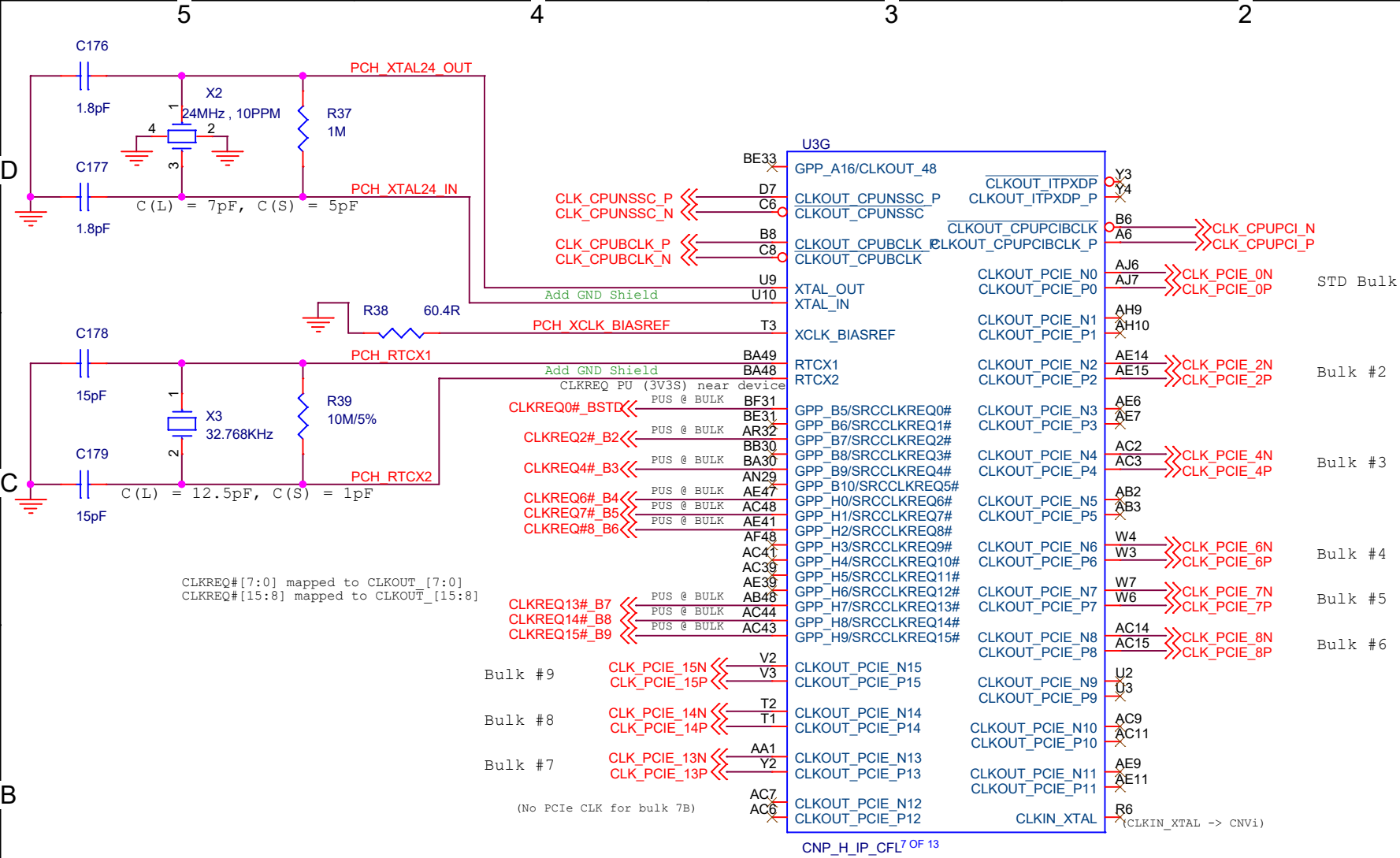




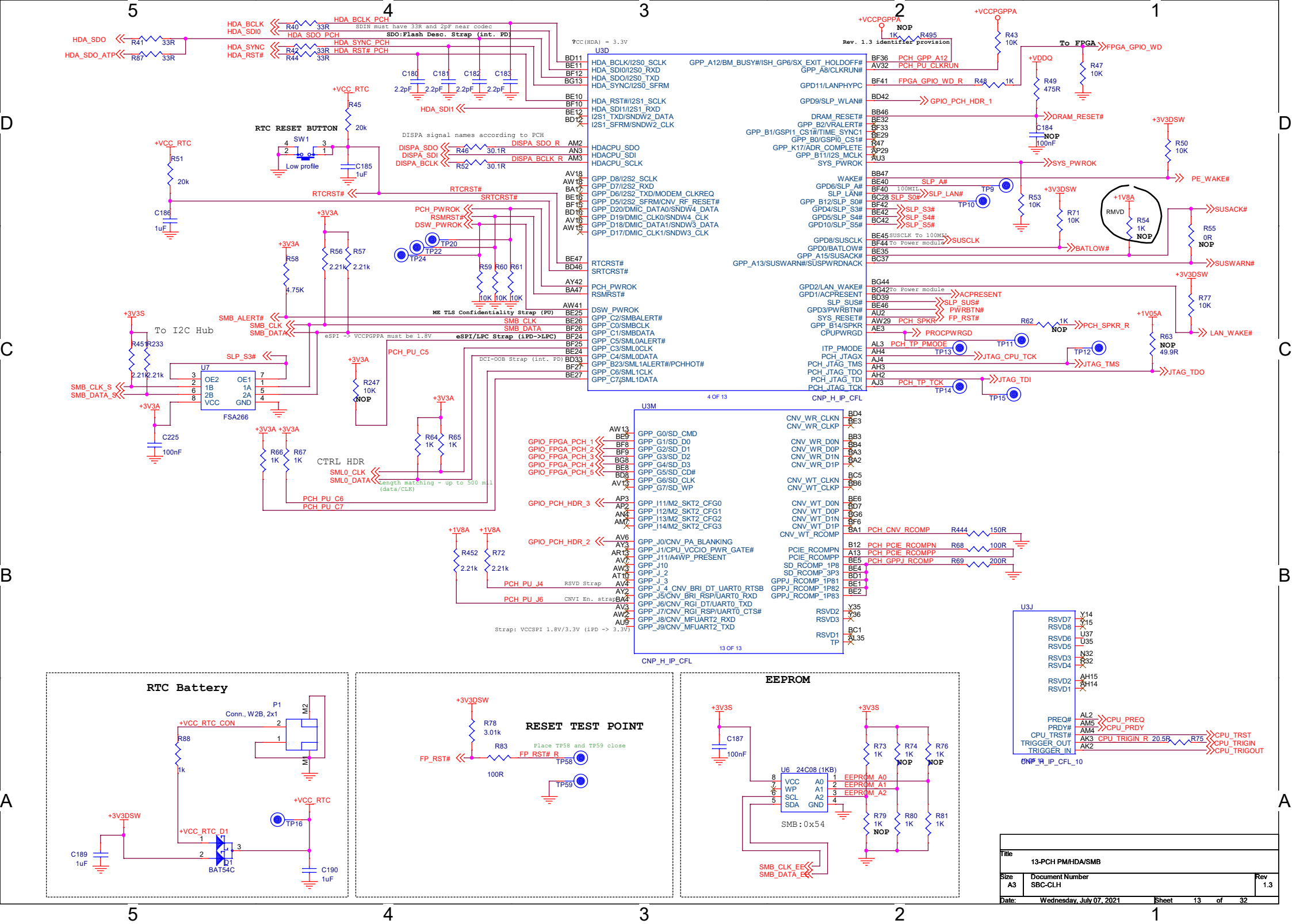
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A16	VSS 3	VSS 84	AL12
A18	VSS 4	VSS 85	AL14
A20	VSS 5	VSS 86	AL33
A22	VSS 6	VSS 87	AL34
A24	VSS 7	VSS 88	AL4
A26	VSS 8	VSS 89	AL7
A28	VSS 9	VSS 90	AL8
A30	VSS 10	VSS 91	AL9
A6	VSS 11	VSS 92	AM1
A9	VSS 12	VSS 93	AM2
AA12	VSS 13	VSS 94	AM3
AA29	VSS 14	VSS 95	AM37
AA30	VSS 15	VSS 96	AM38
AB33	VSS 16	VSS 97	AM4
AB34	VSS 17	VSS 98	AM5
AB6	VSS 18	VSS 99	AN12
AC1	VSS 19	VSS 100	AN29
AC12	VSS 20	VSS 101	AN30
AC2	VSS 21	VSS 102	AN5
AC37	VSS 22	VSS 103	AN6
AC38	VSS 23	VSS 104	AP10
AC4	VSS 24	VSS 105	AP11
AC5	VSS 25	VSS 106	AP12
AC6	VSS 26	VSS 107	AP18
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AD11	VSS 28	VSS 109	AP9
AD12	VSS 29	VSS 110	AR1
AD29	VSS 30	VSS 111	AR12
AD30	VSS 31	VSS 112	AR14
AD6	VSS 32	VSS 113	AR2
AD8	VSS 33	VSS 114	AR29
AD9	VSS 34	VSS 115	AR3
AE33	VSS 35	VSS 116	AR37
AE34	VSS 36	VSS 117	AR38
AE6	VSS 37	VSS 118	AR39
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AF12	VSS 39	VSS 120	BE2
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AF14	VSS 41	VSS 122	BE3
AF2	VSS 42	VSS 123	BE34
AF3	VSS 43	VSS 124	BE4
AF4	VSS 44	VSS 125	BE5
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AJ6	VSS 66	VSS 147	BH5
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W5	VSS 68	VSS 149	BH8
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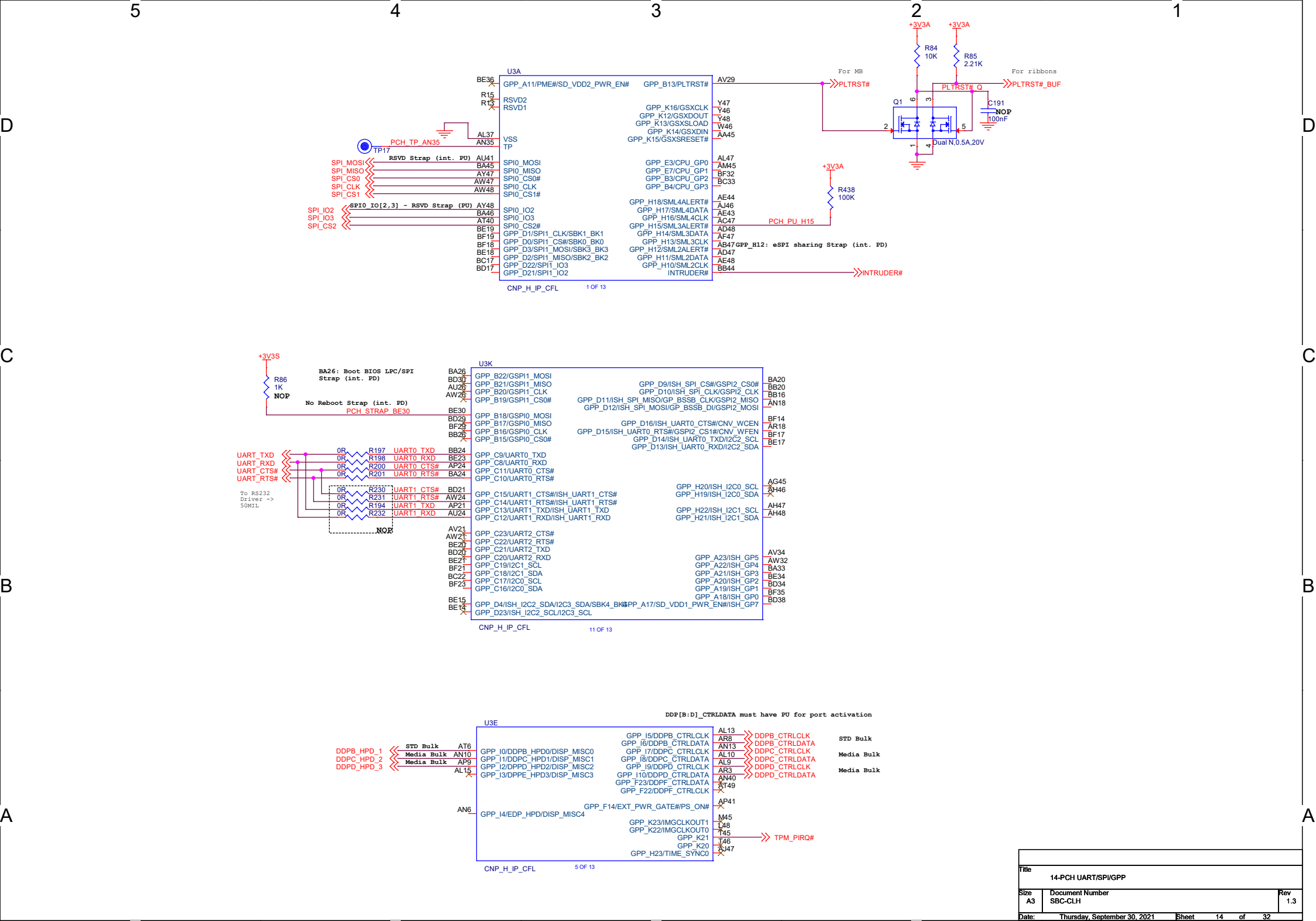


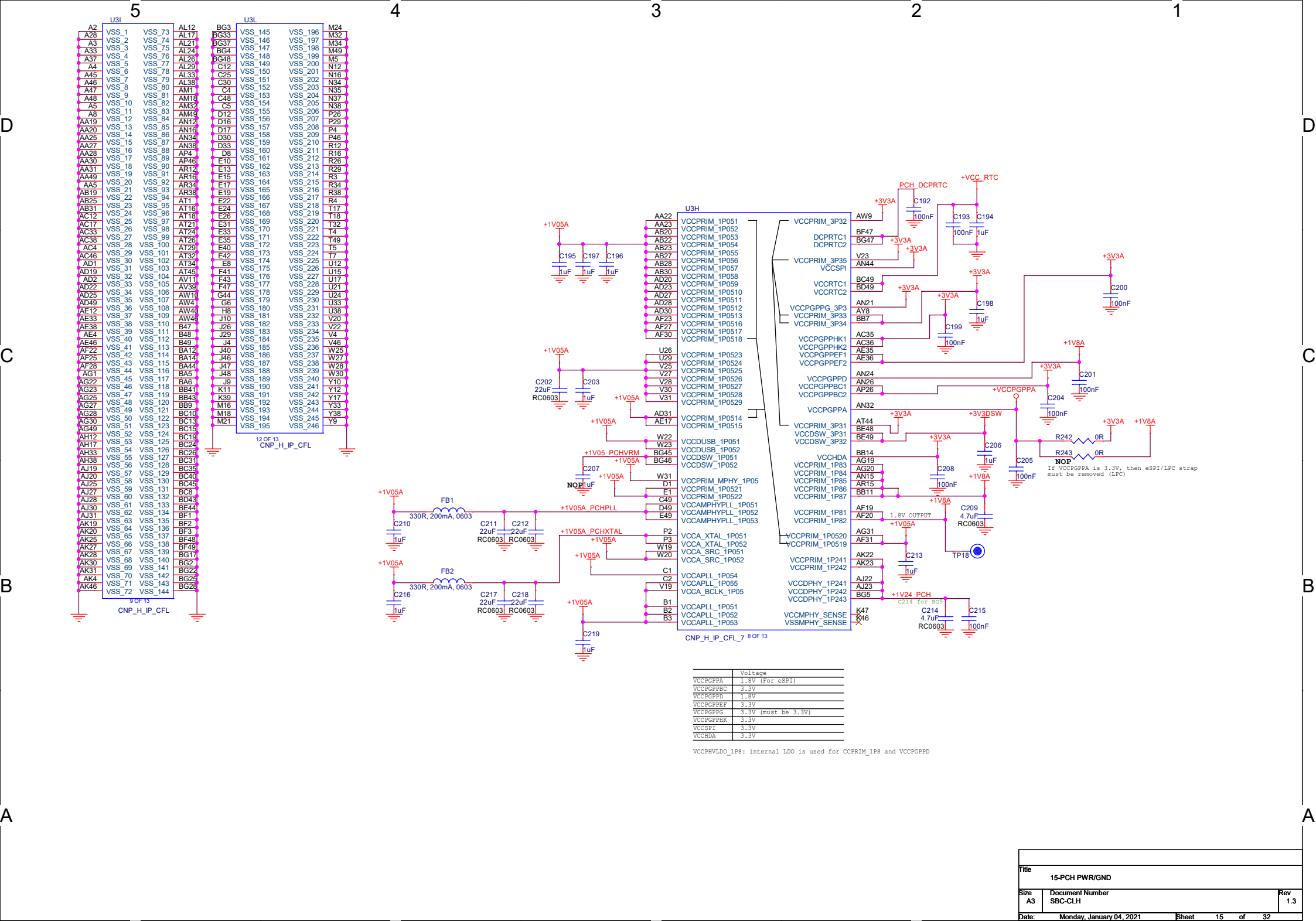




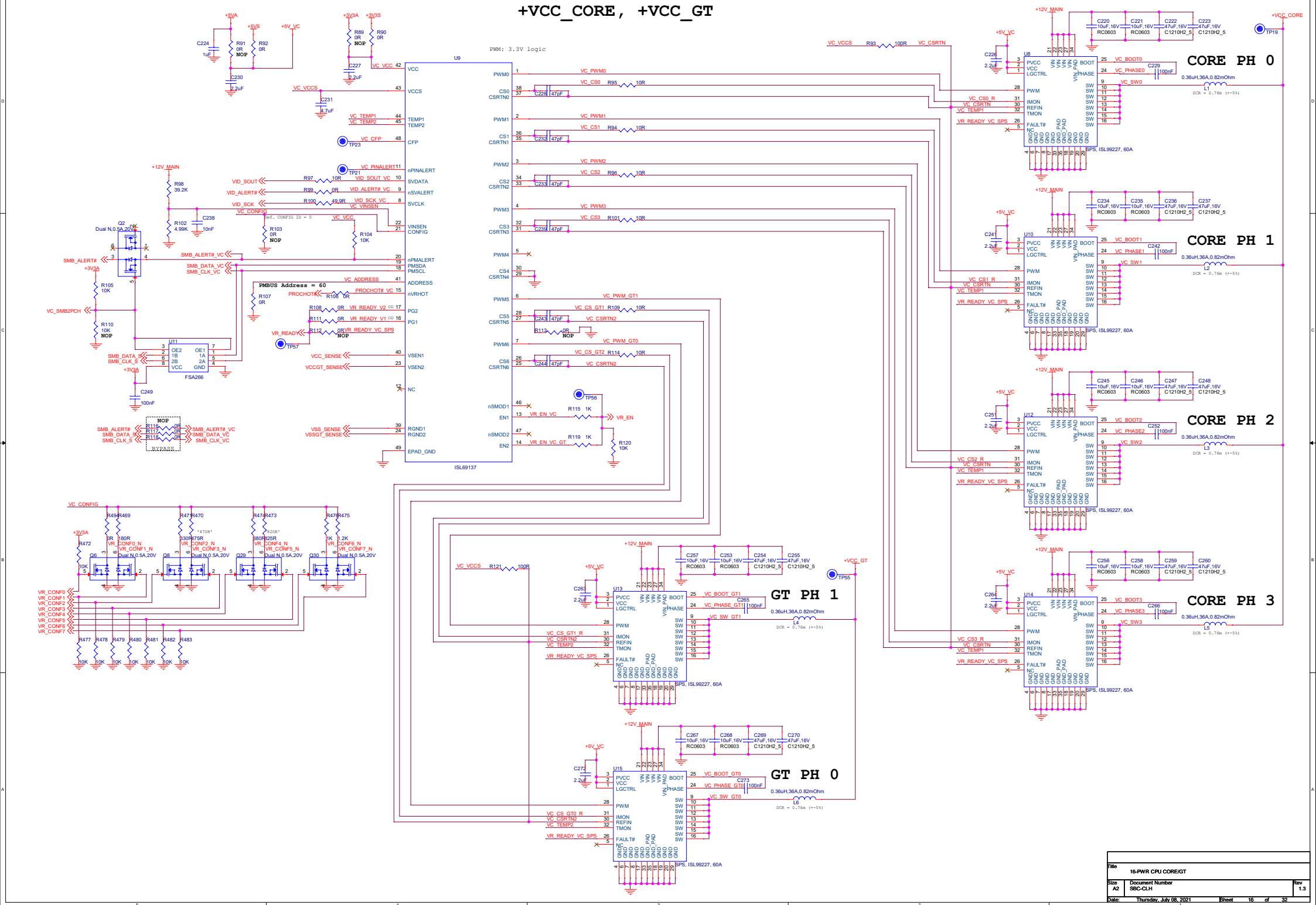
Title 12-PCH CLK			
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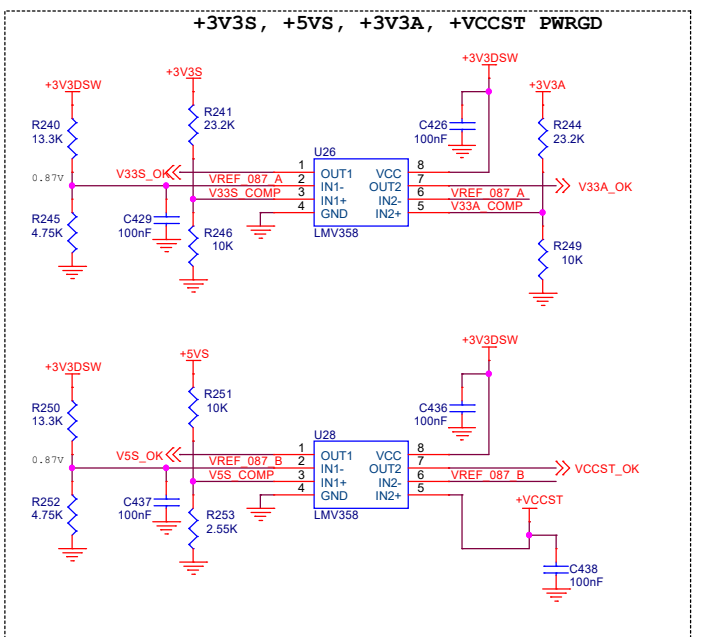
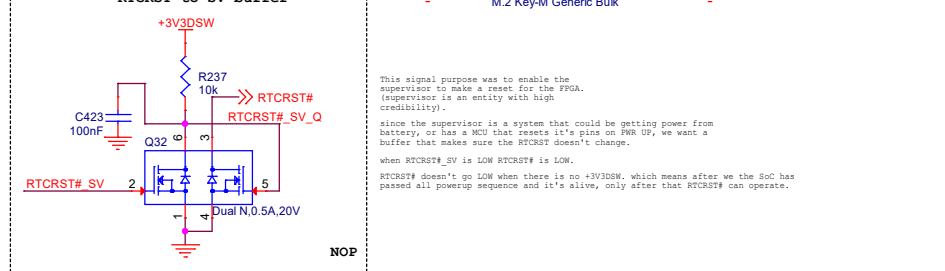
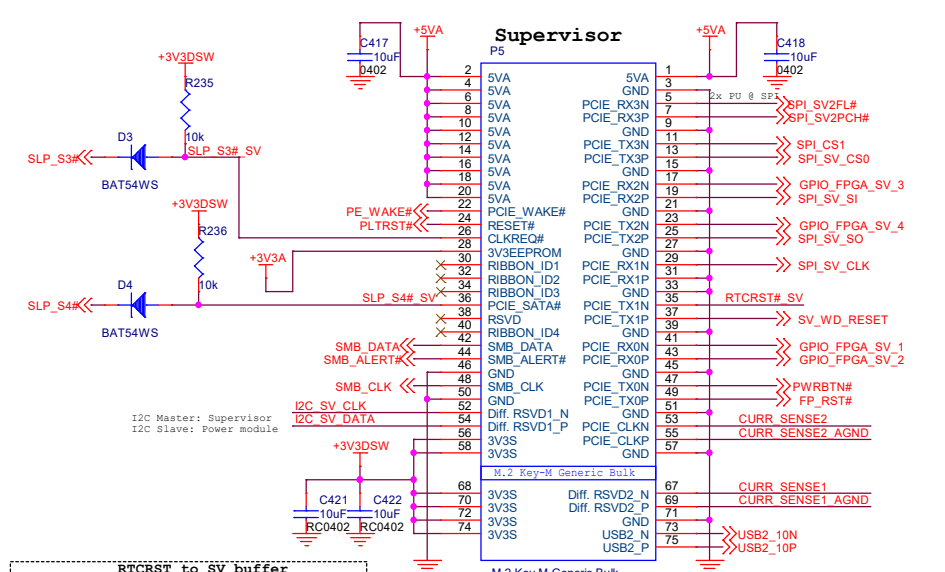
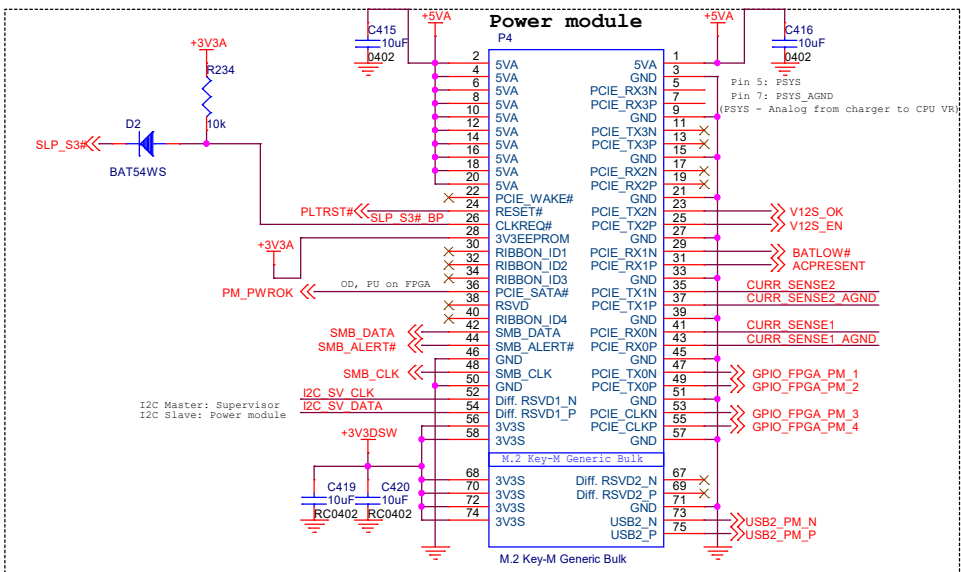




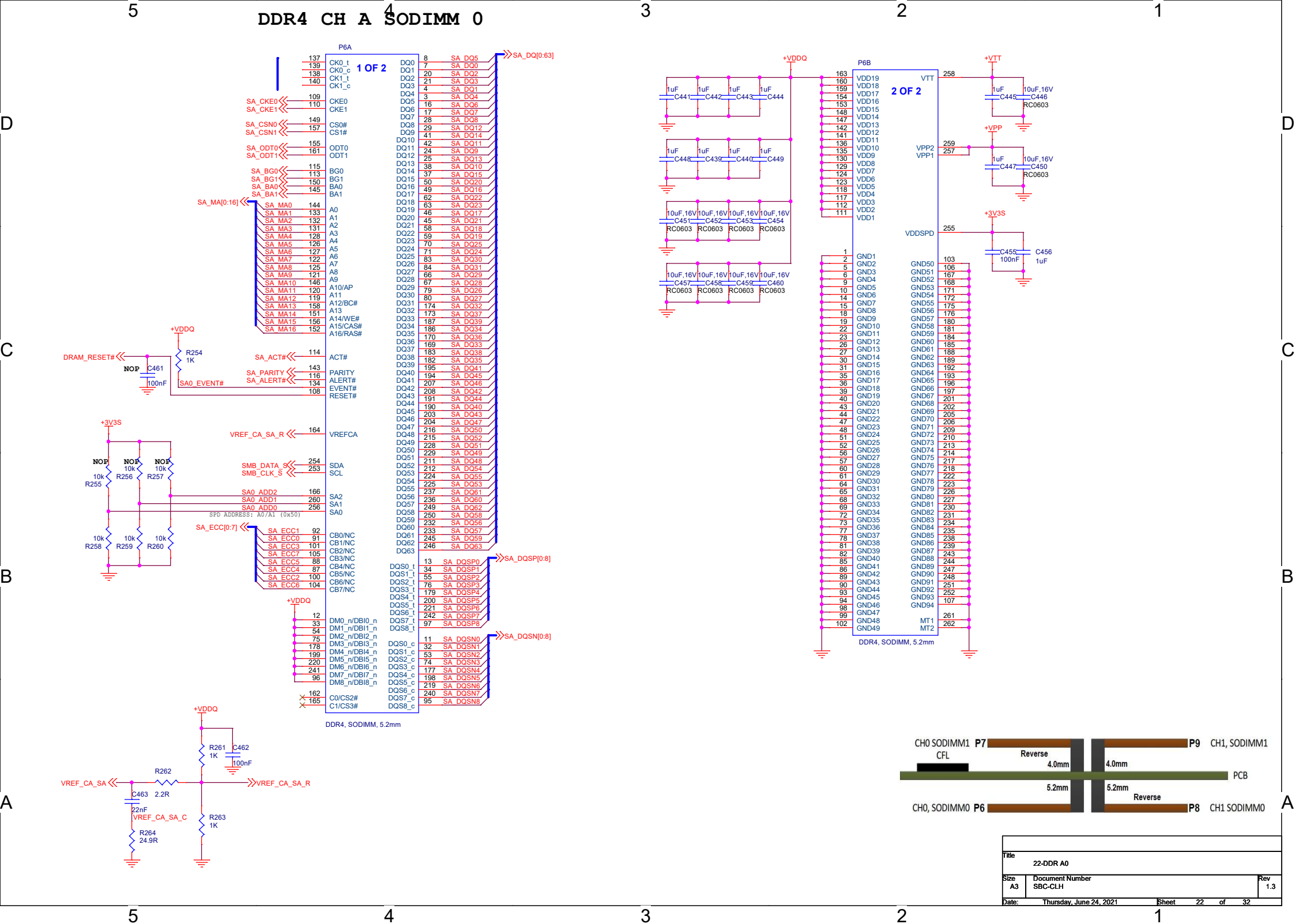
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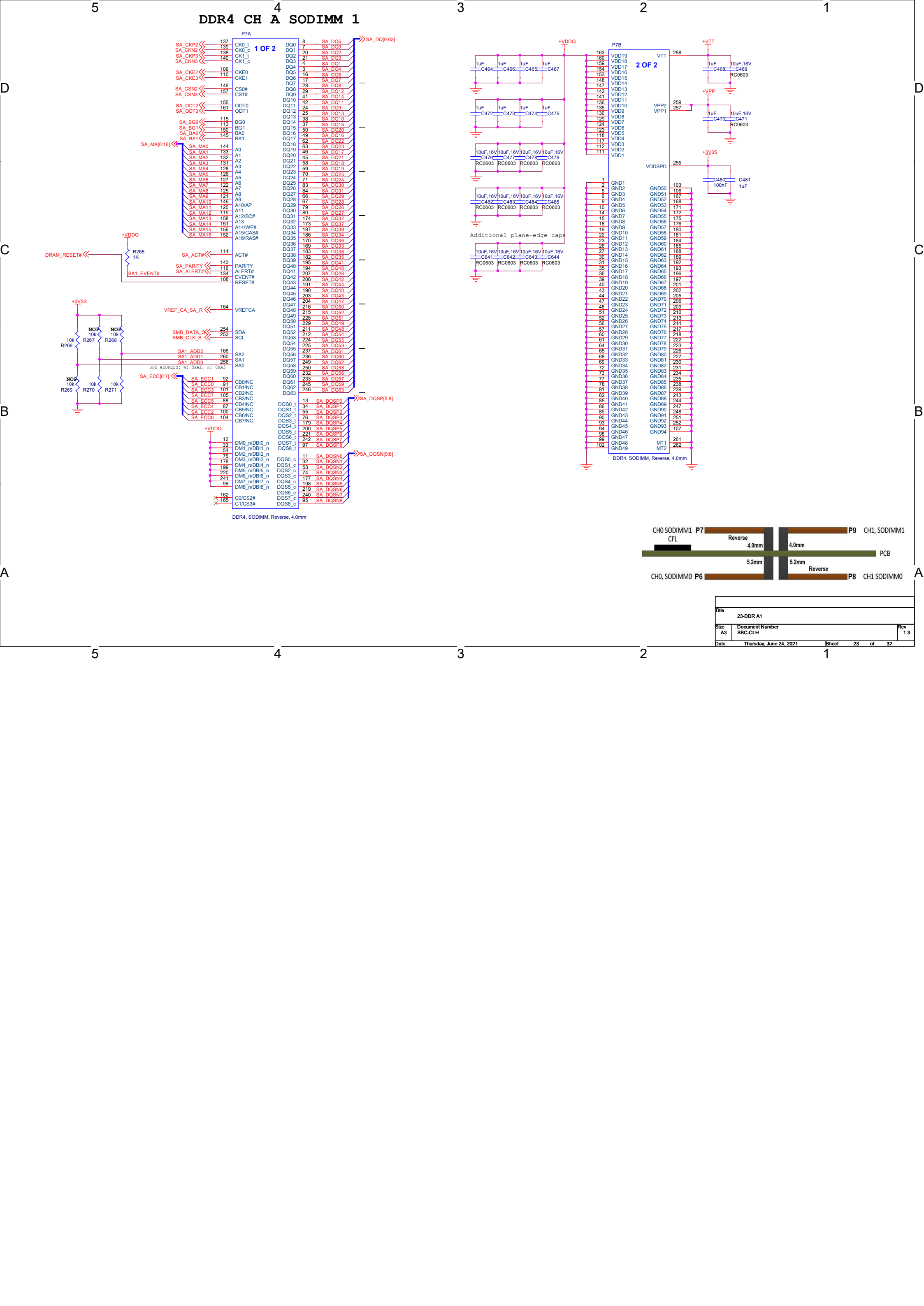


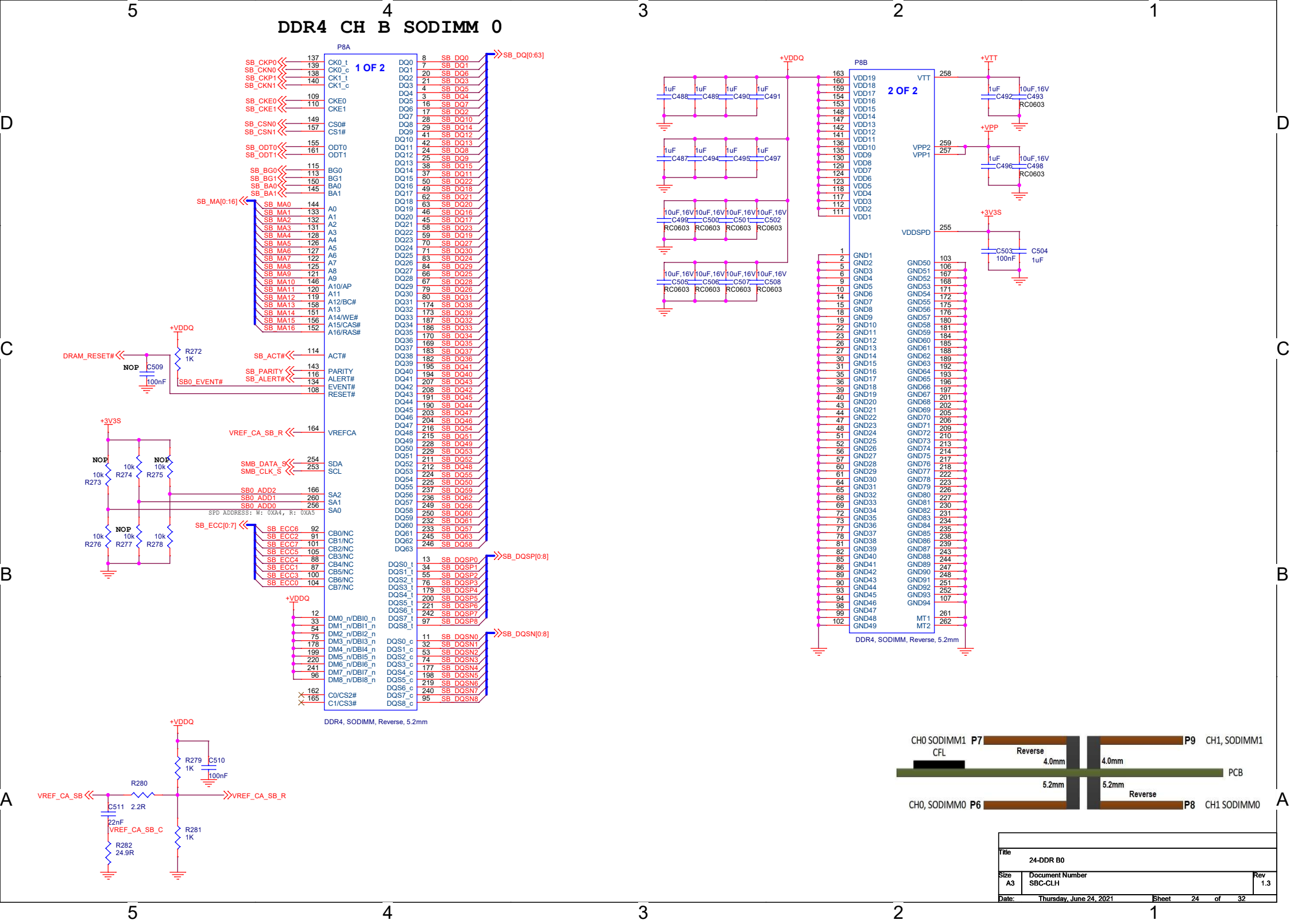
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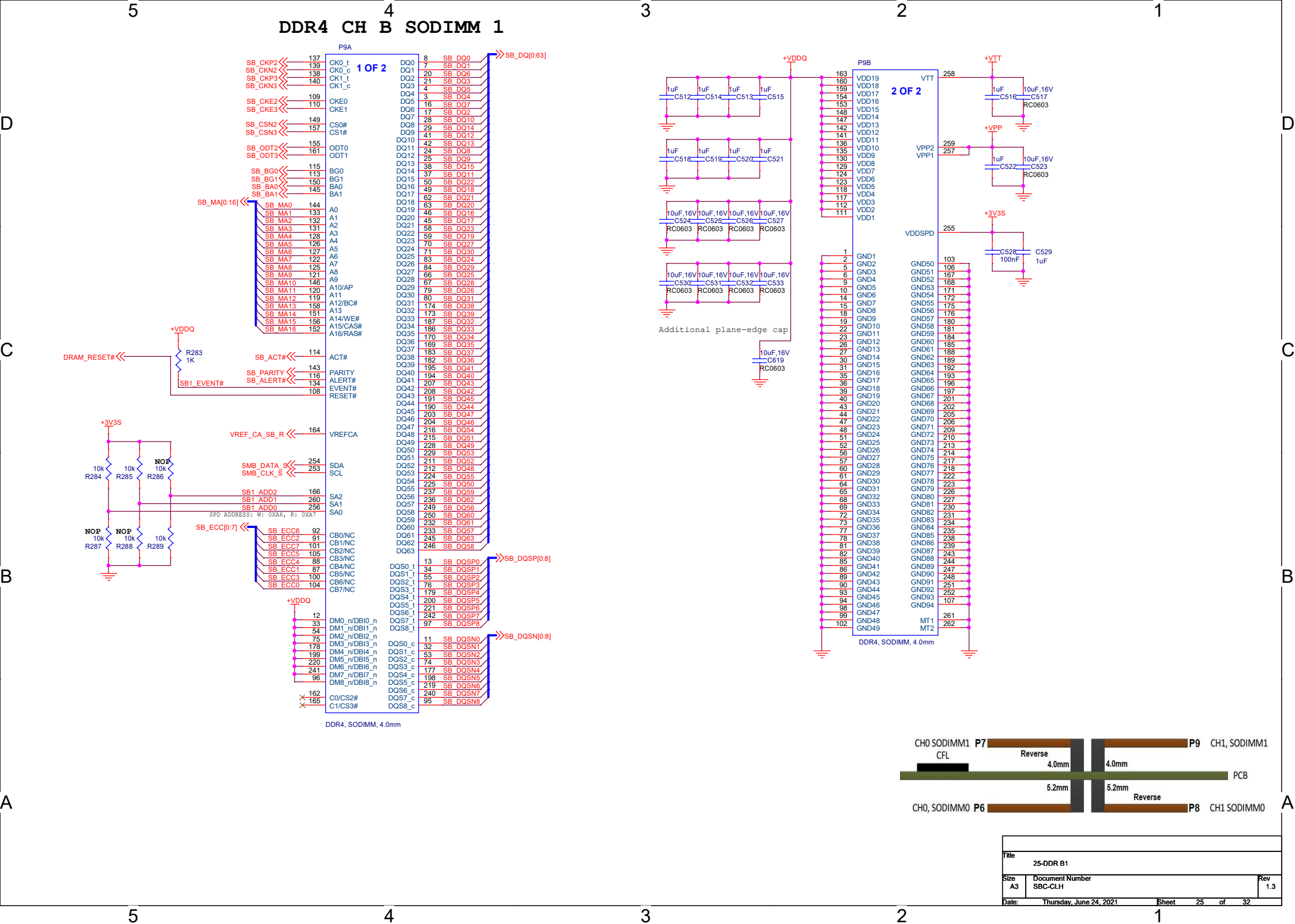


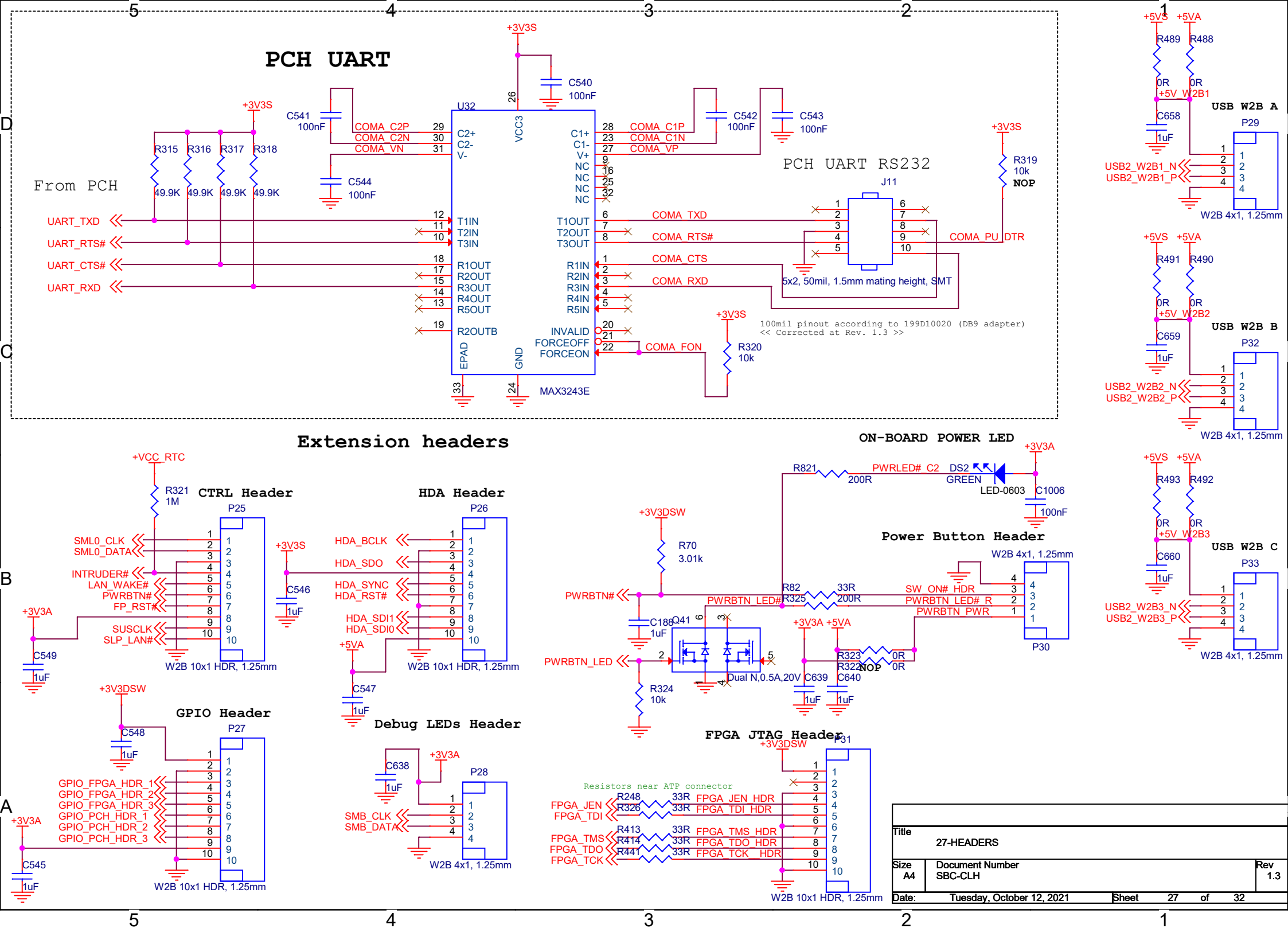
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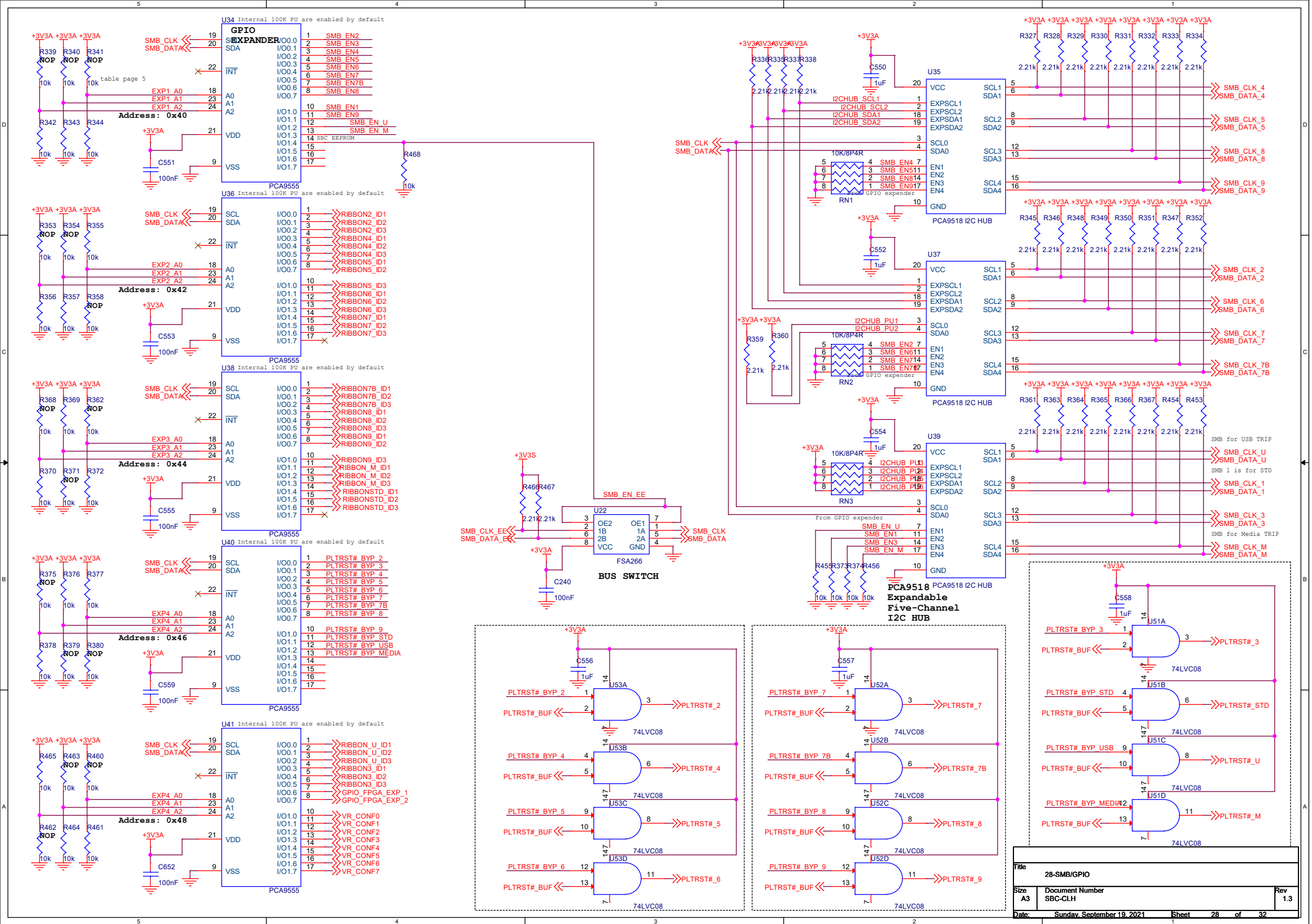




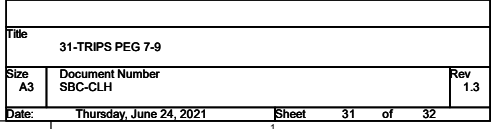
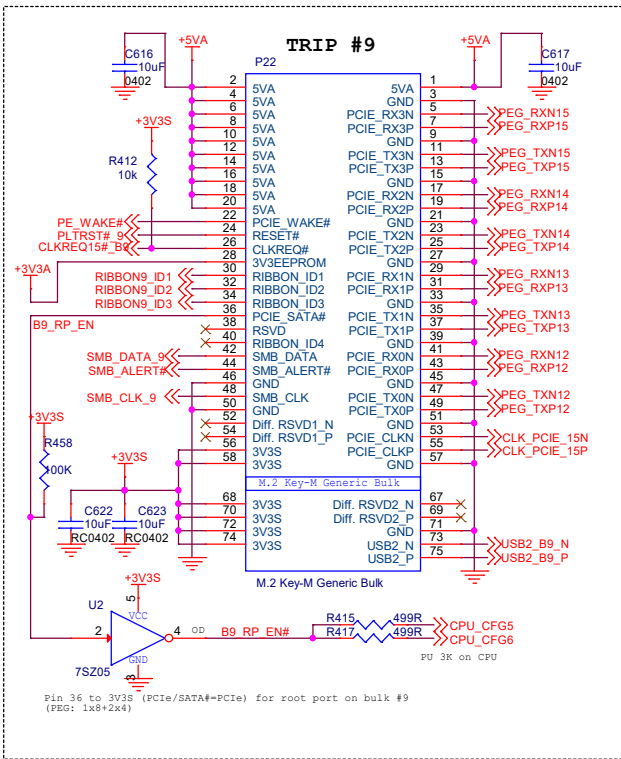
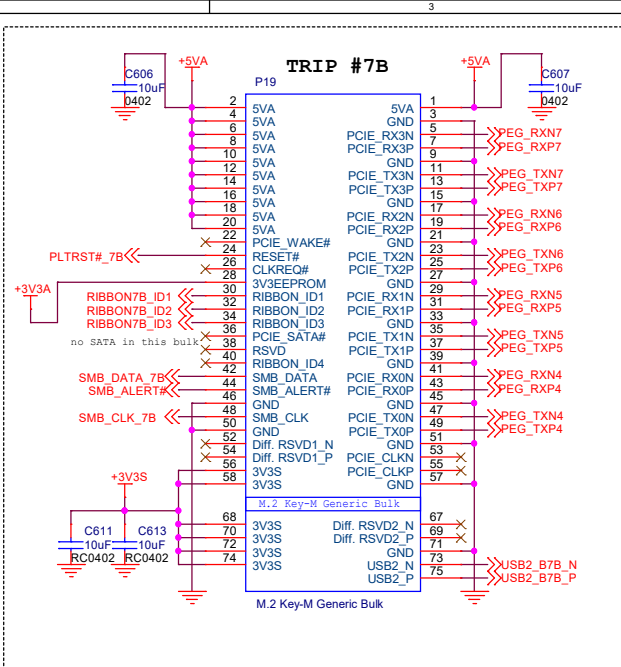


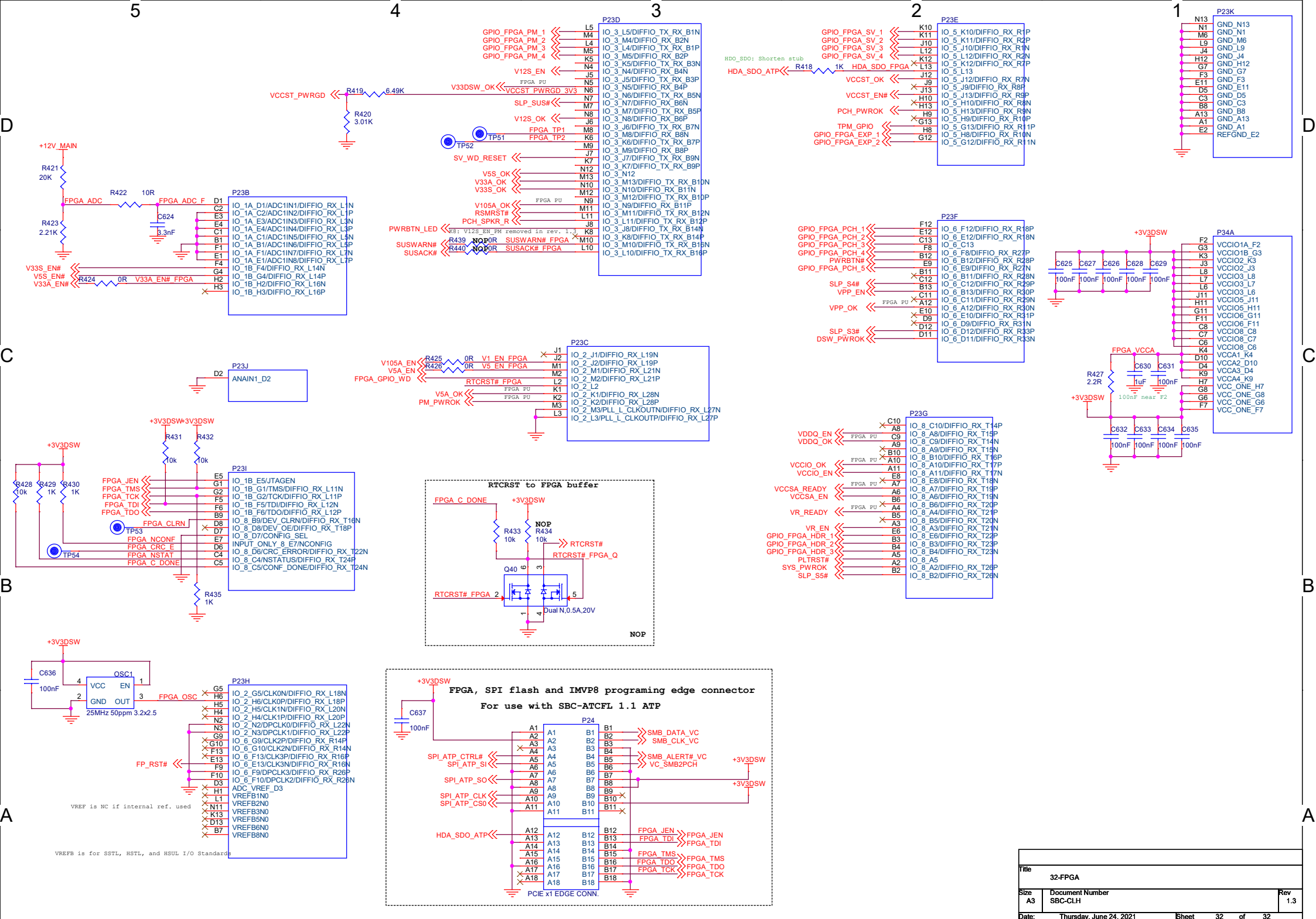






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29-TRIPS STD/MEDIA/USB				
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32-FPGA			
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