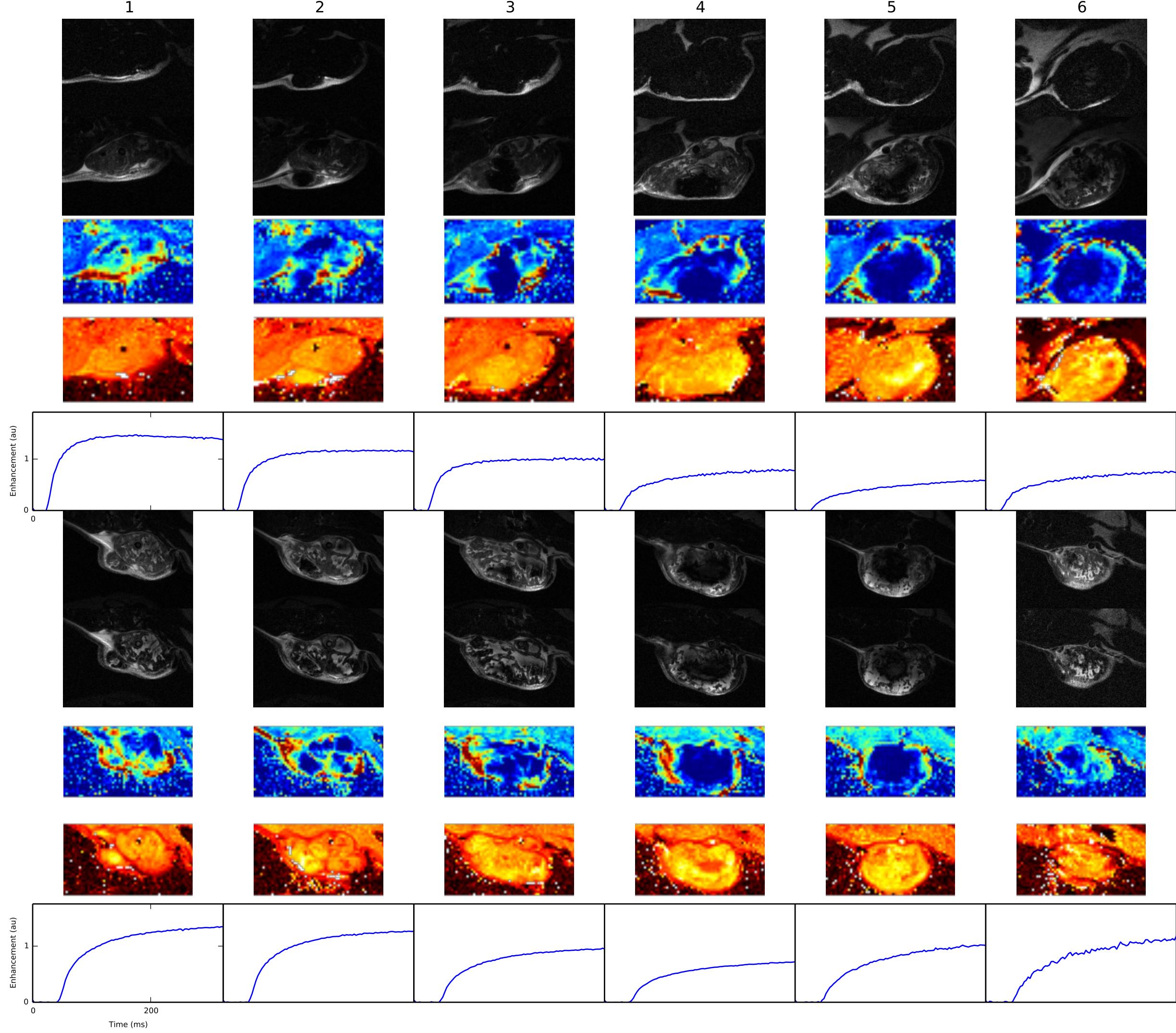


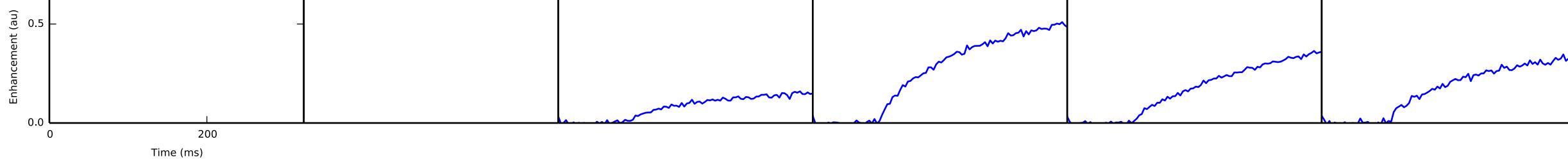
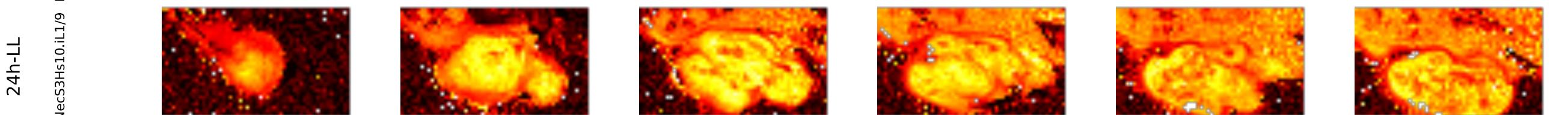
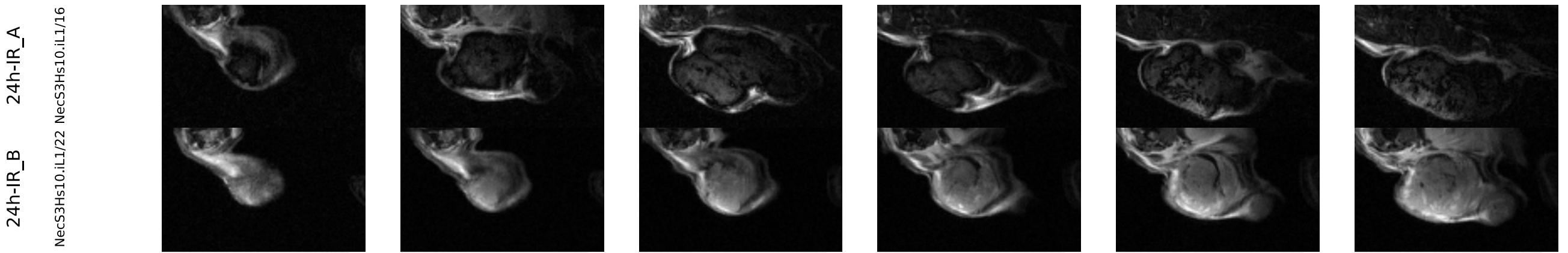
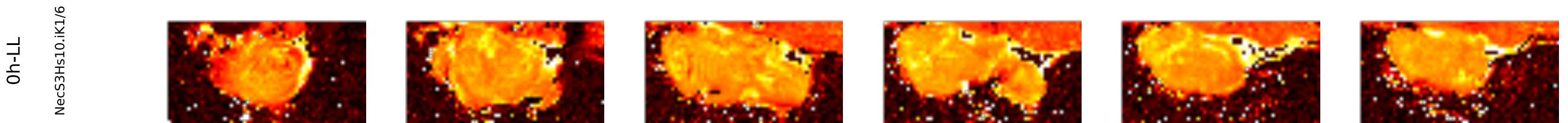
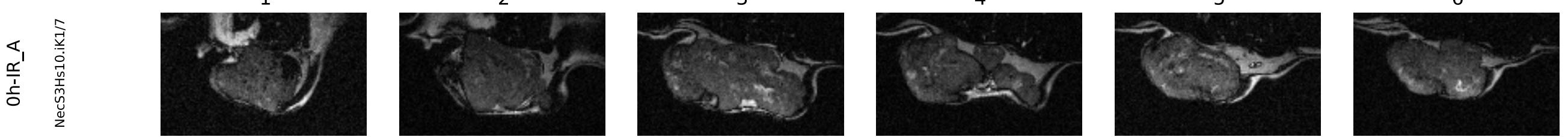
NecS3Hs02

24h-DCE1 24h-LL 24h-DCE1 24h-IR\_B 24h-IR\_A 0h-DCE1 0h-LL 0h-DCE1 0h-IR\_B 0h-IR\_A

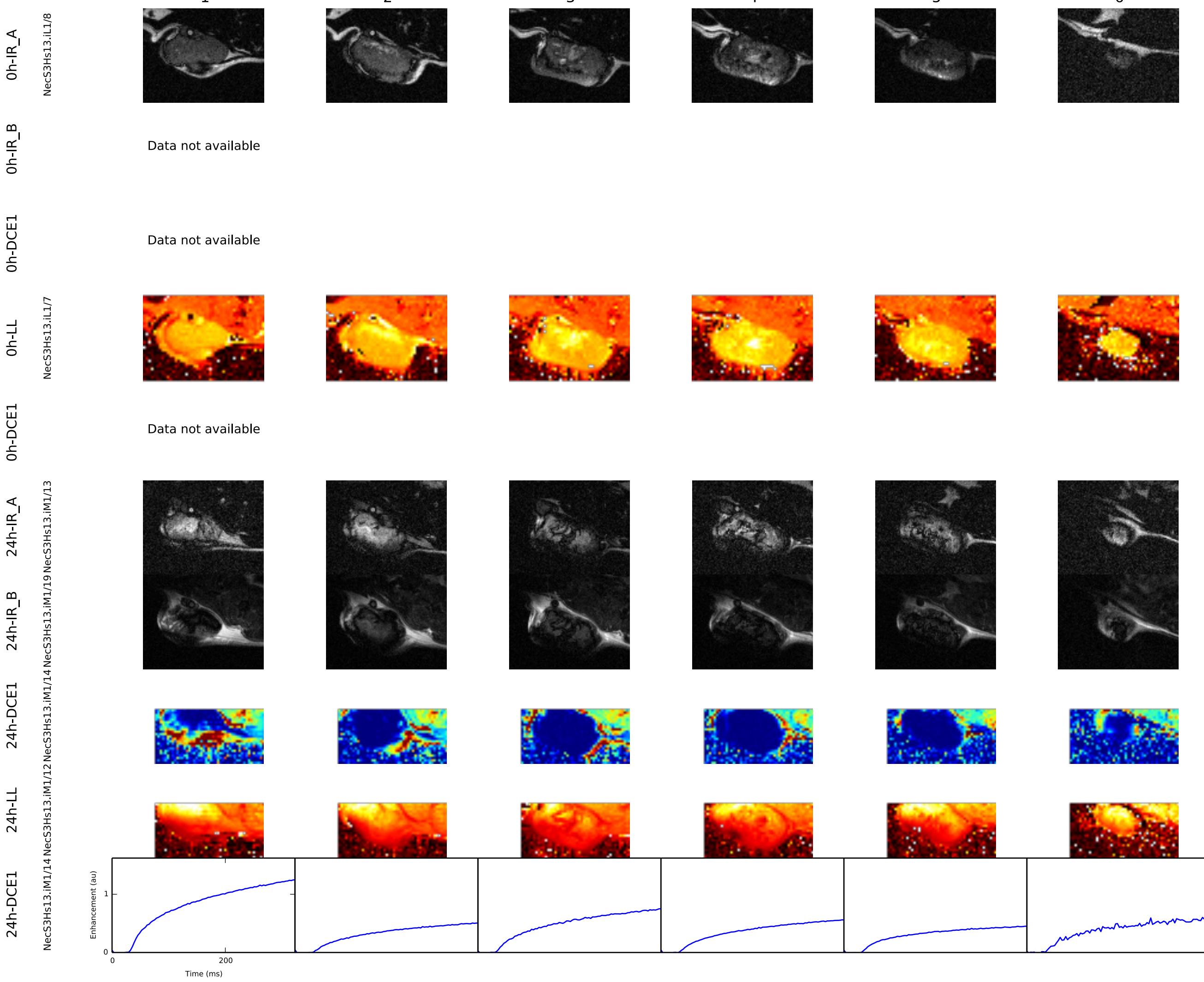
NecS3Hs02.iiK1/14 NecS3Hs02.iiK1/11 NecS3Hs02.iiK1/14 NecS3Hs02.iiK1/14 NecS3Hs02.iiK1/25 NecS3Hs02.iiK1/19 NecS3Hs02.iiK1/12 NecS3Hs02.iiK1/10 NecS3Hs02.iiK1/12 NecS3Hs02.iiK1/17 NecS3Hs02.iiK1/11



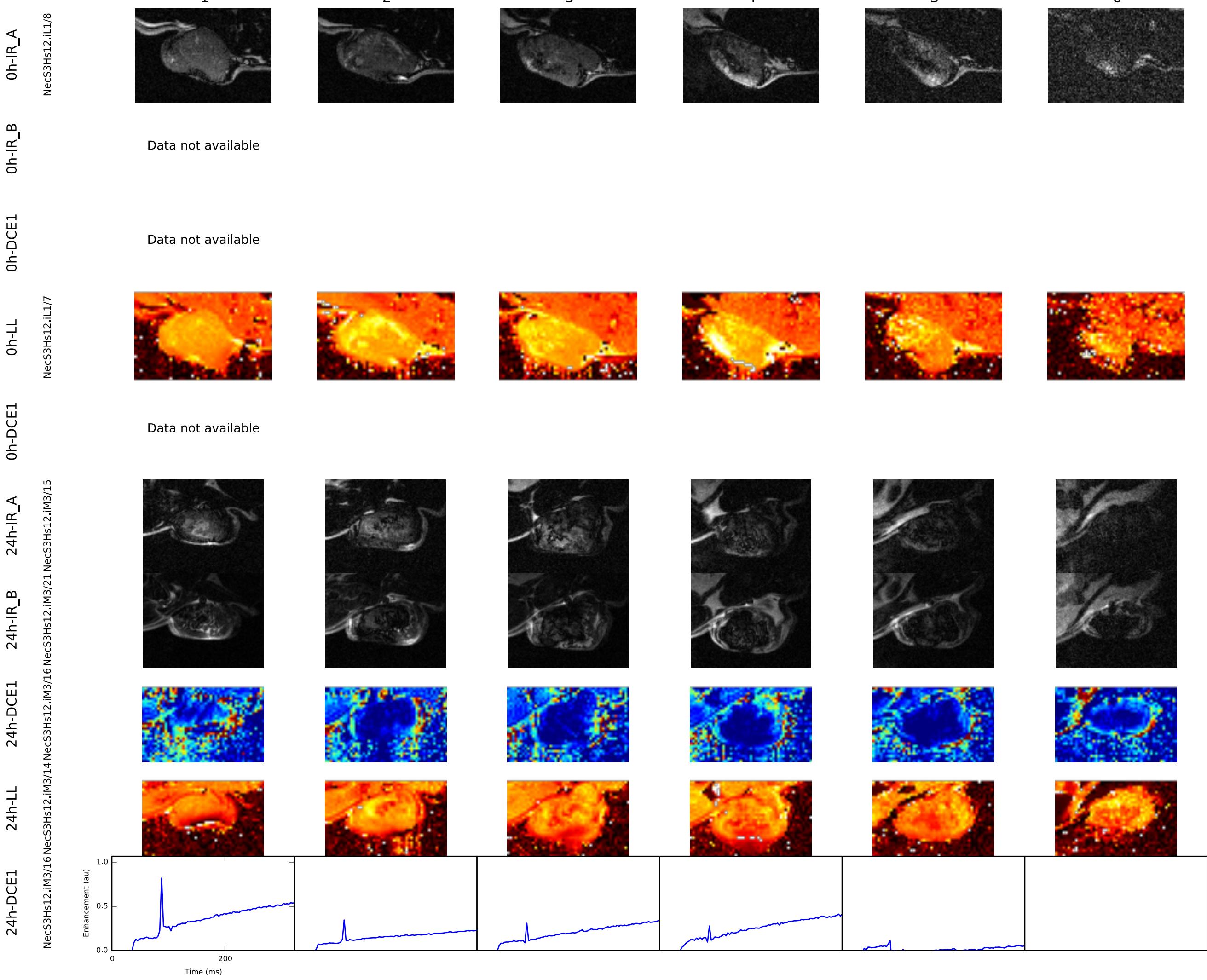
# NecS3Hs10



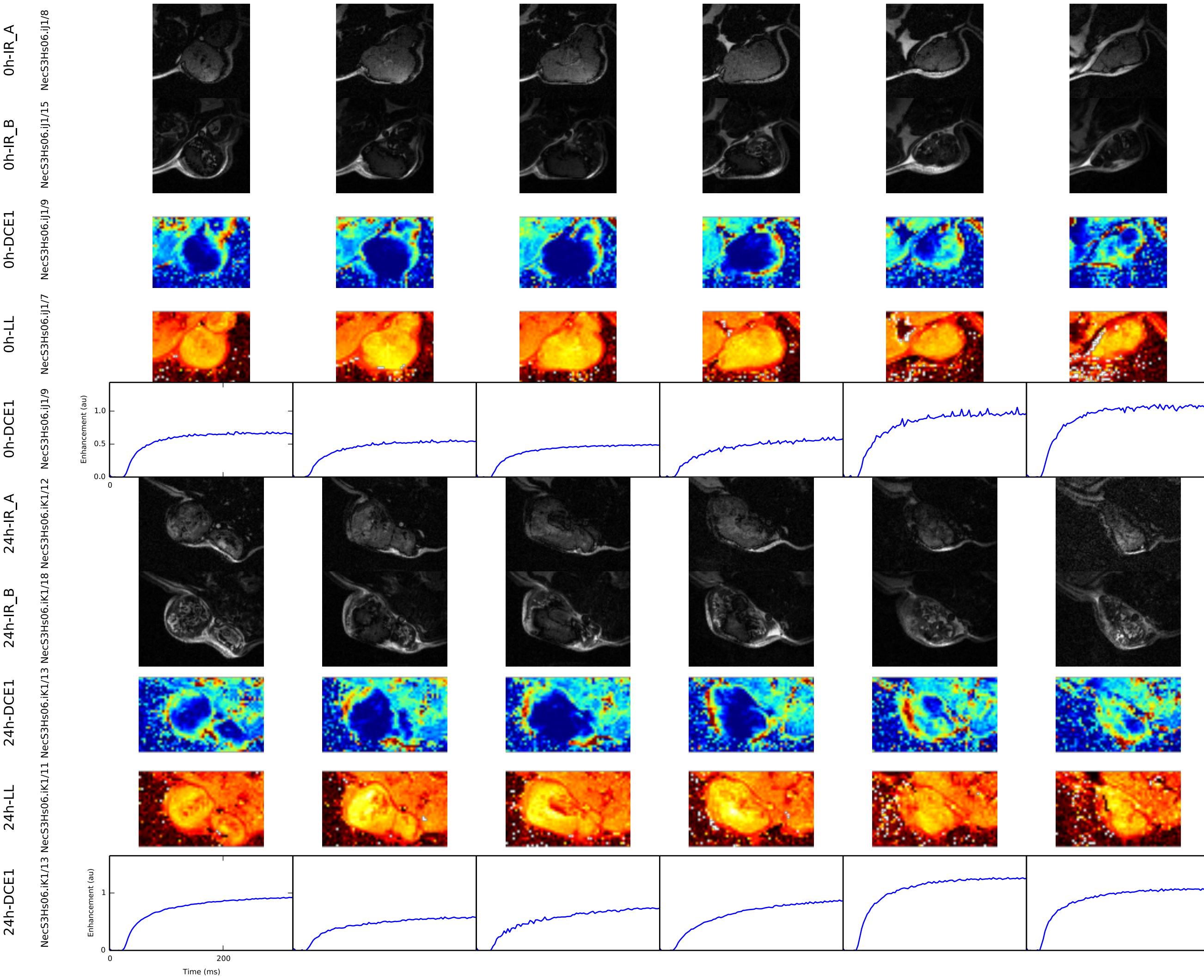
# NecS3Hs13



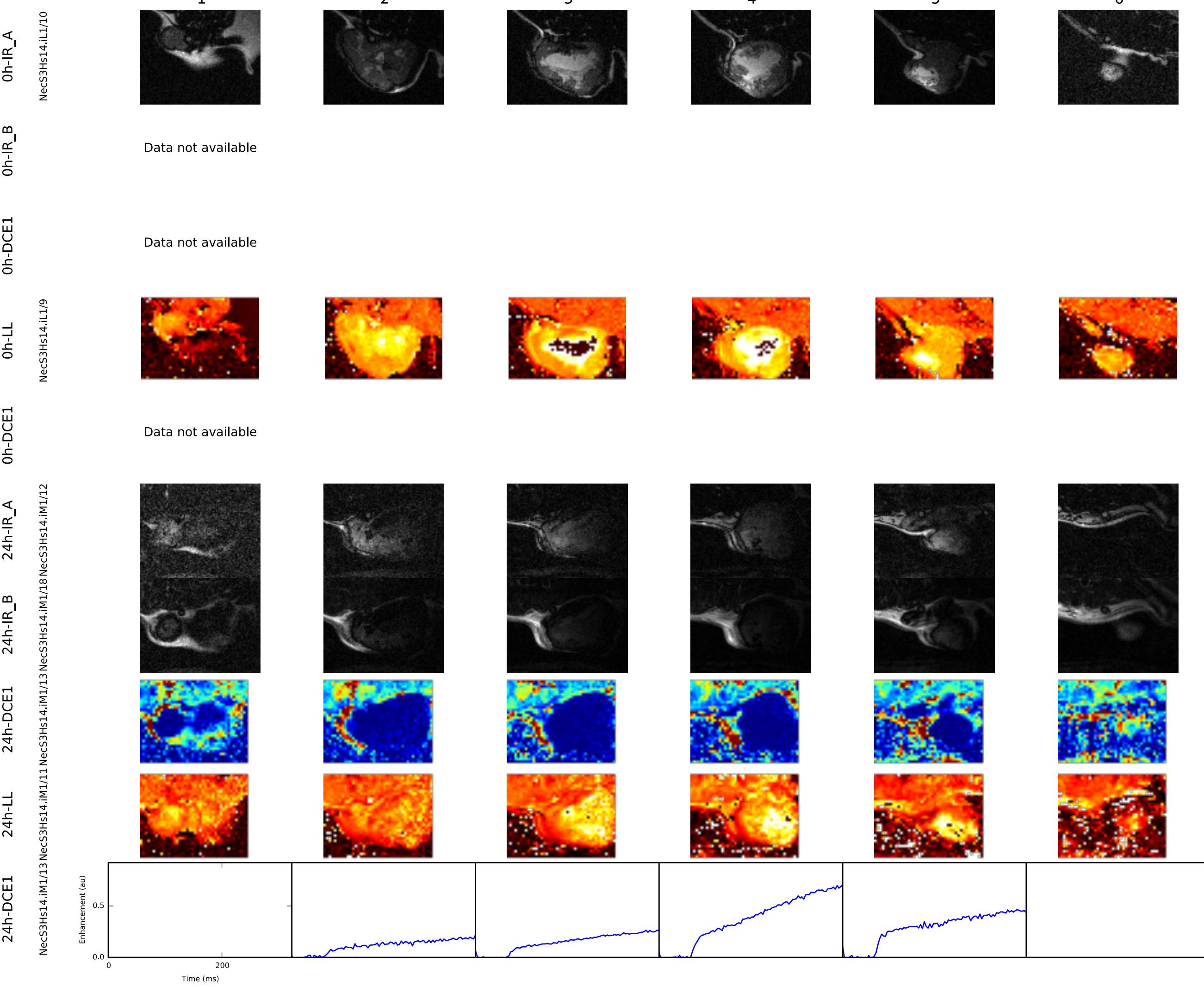
# NecS3Hs12



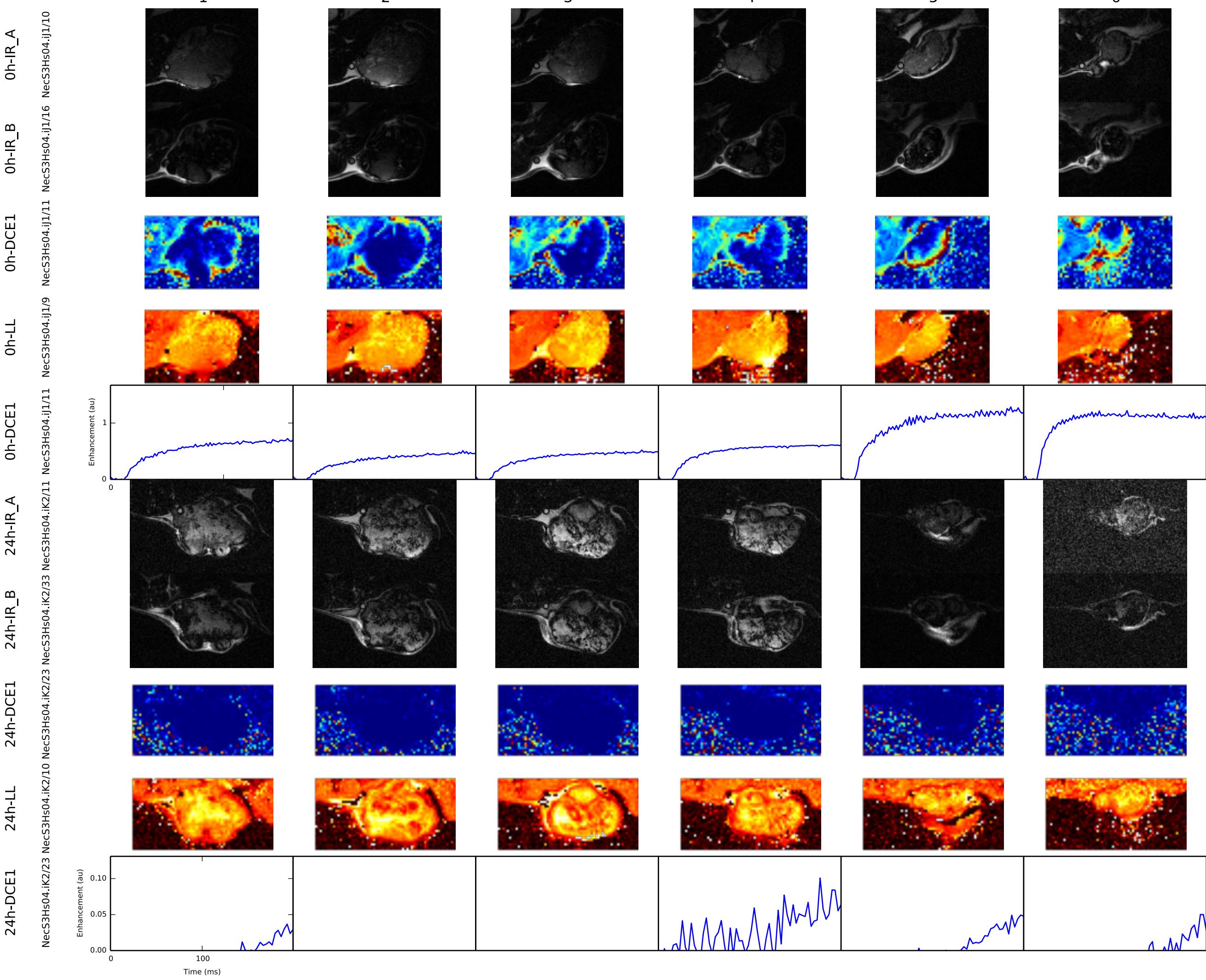
# NecS3Hs06



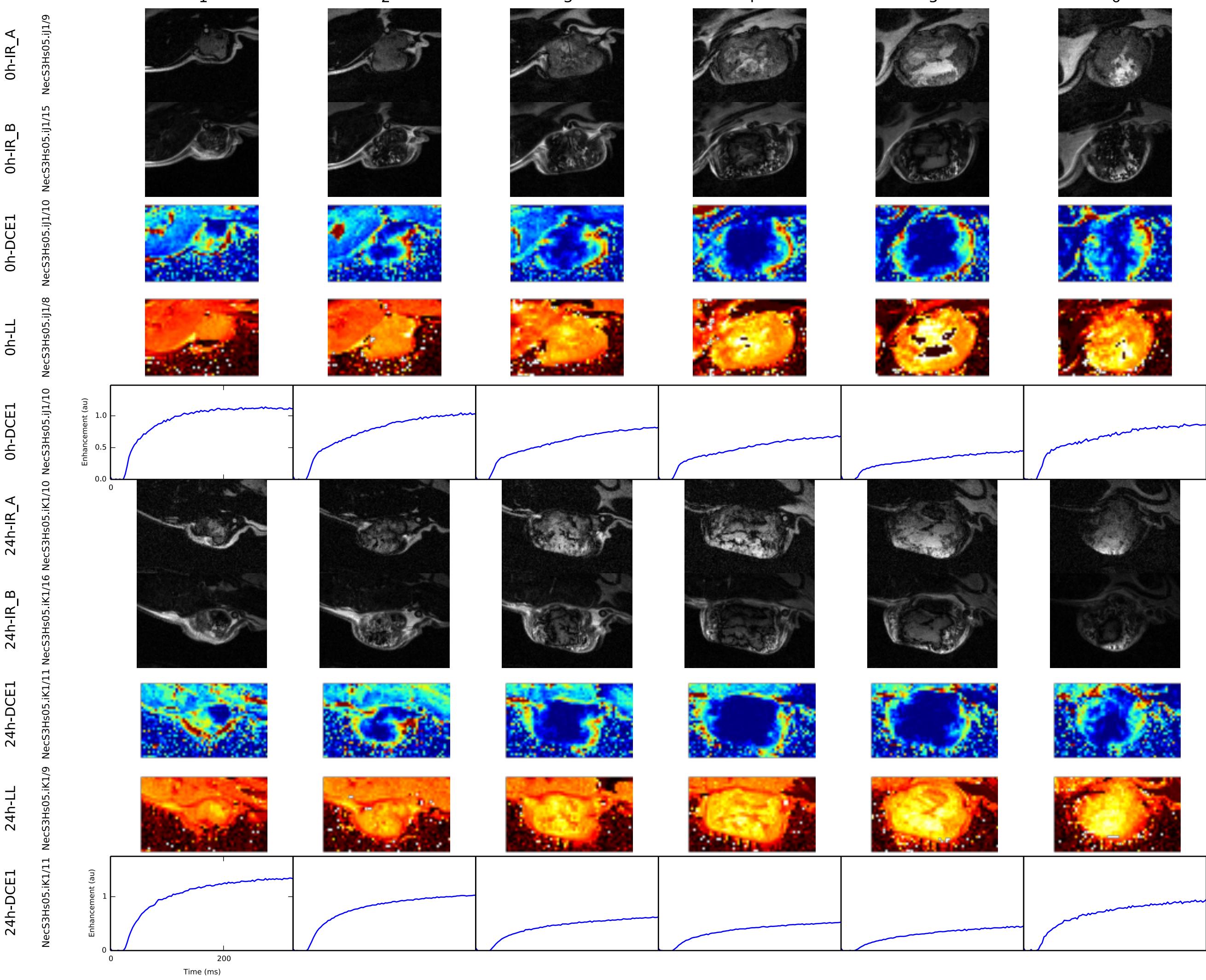
# NecS3Hs14



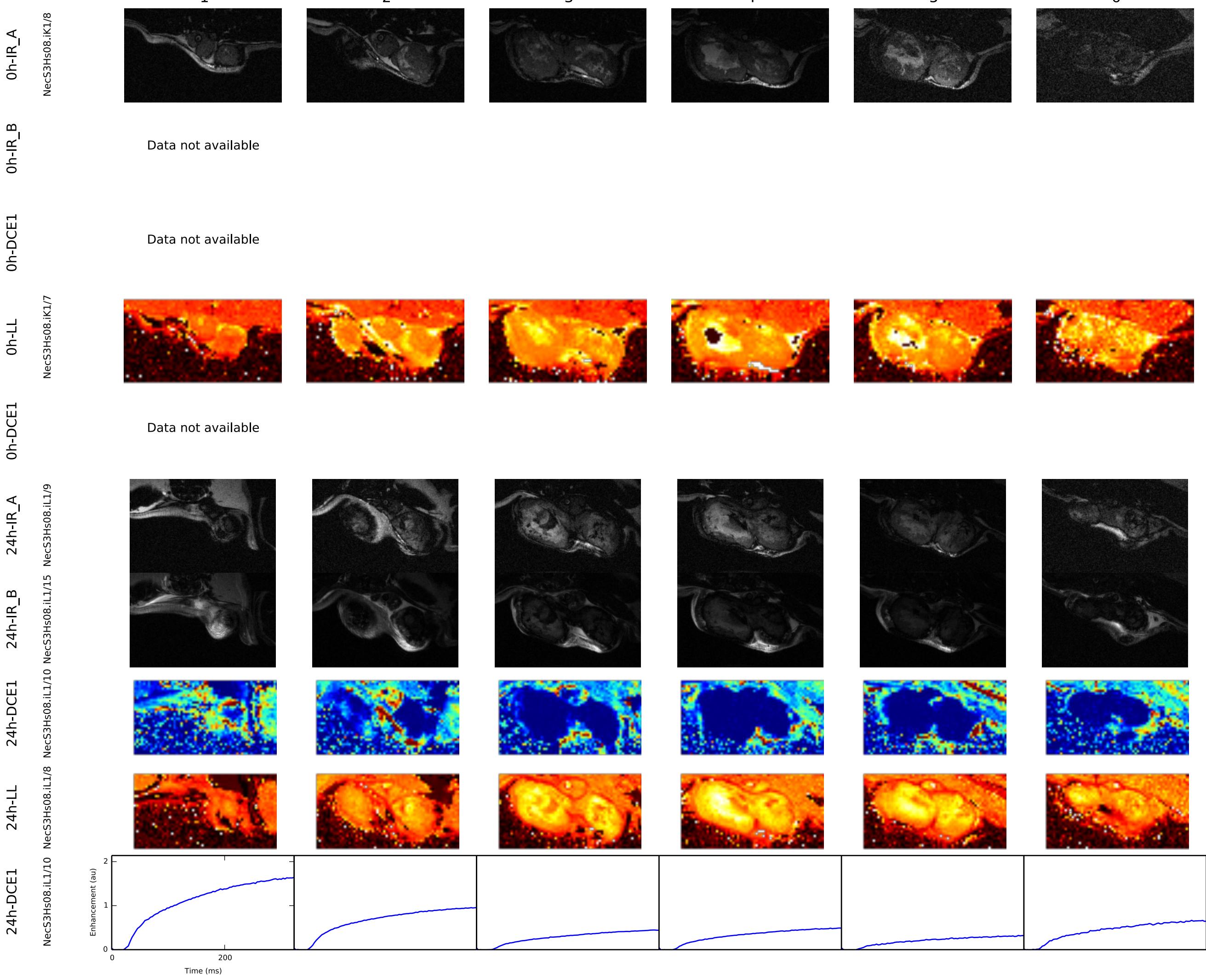
NecS3Hs04



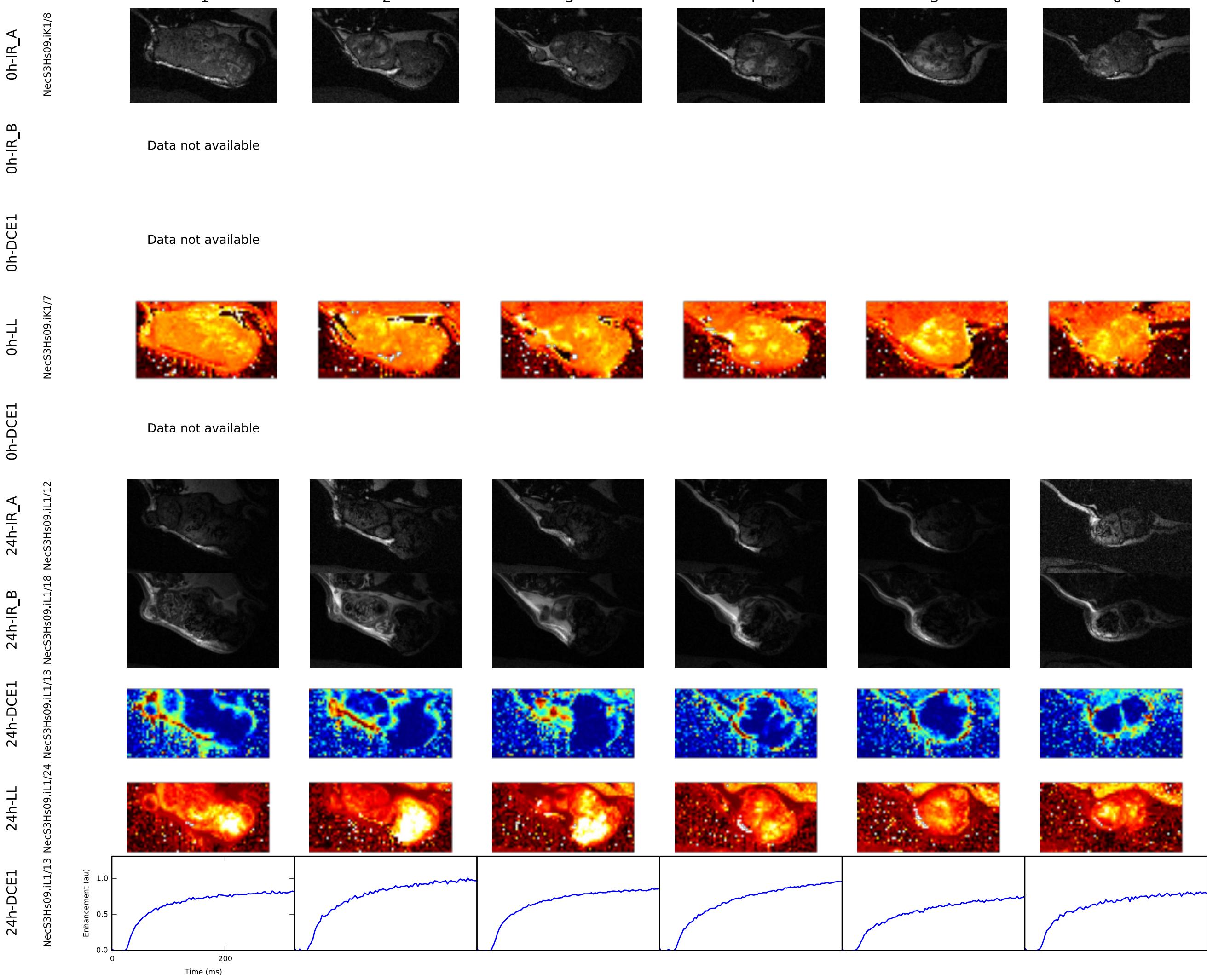
NecS3Hs05



# NecS3Hs08



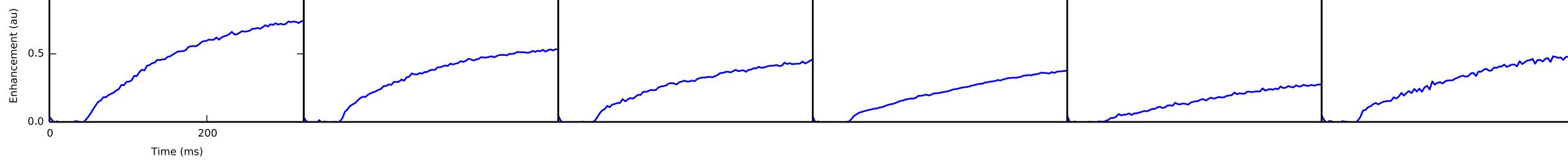
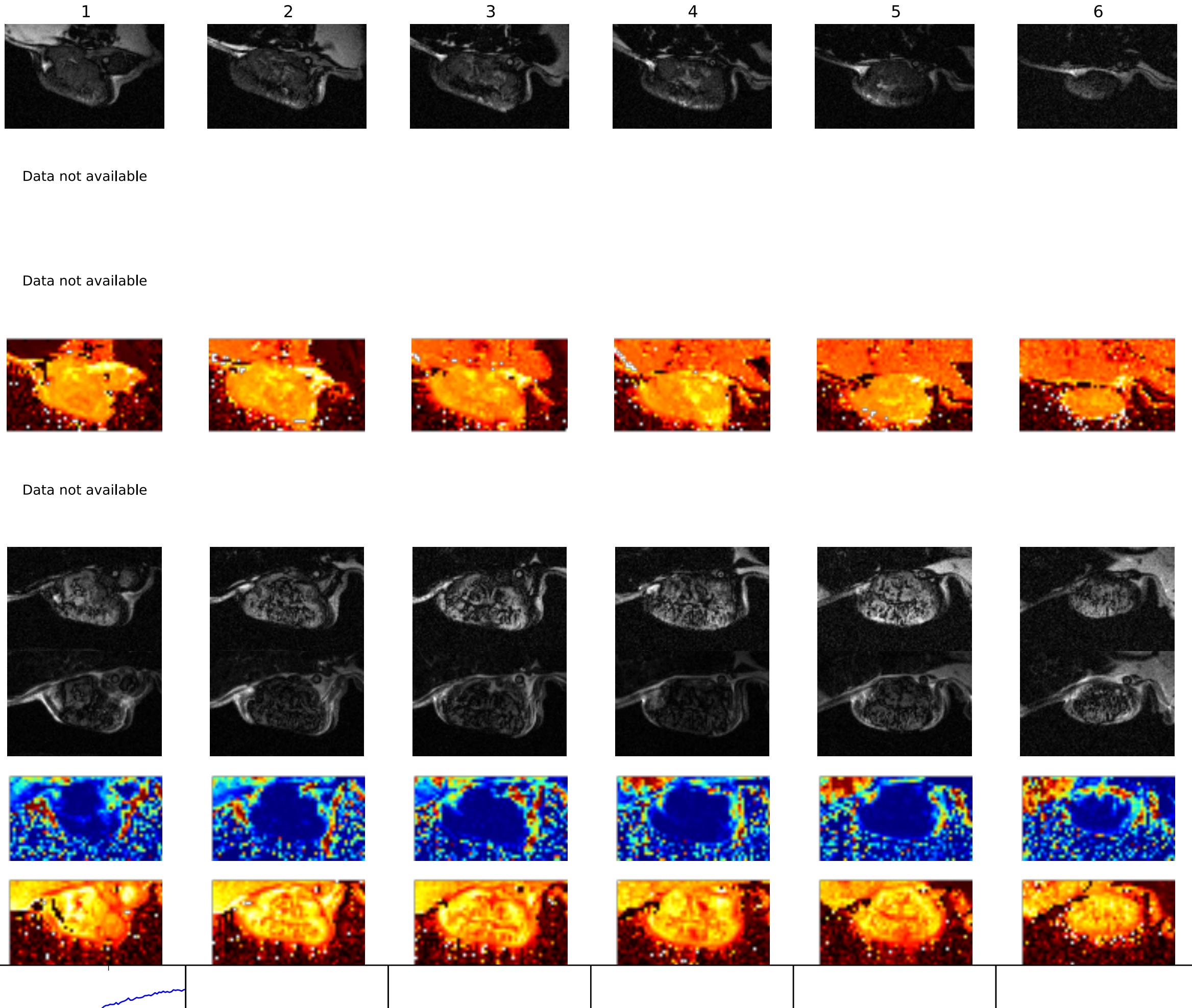
# NecS3Hs09



# NecS3Hs15

NecS3Hs15;il1/9      NecS3Hs15;il1/8      NecS3Hs15;il1/7      NecS3Hs15;il1/6      NecS3Hs15;il1/5      NecS3Hs15;il1/4      NecS3Hs15;il1/3

0h-IR\_A      0h-IR\_B      0h-DCE1      0h-LL      0h-DCE1      24h-IR\_A      24h-IR\_B      24h-DCE1      24h-DCE1      24h-LL      24h-DCE1



# NecS3Hs11

