

Chapter 2 Resistive Circuits

1. Simple circuits analysis.
2. Voltage-division and current-division principles.
3. Node-voltage analysis.
4. Mesh-current analysis.
5. Thévenin and Norton equivalents.
6. Superposition principle.
7. Wheatstone bridge.

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Simple circuits analysis Resistors in series

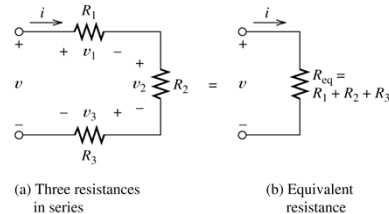


Figure 2.1 Series resistances can be combined into an equivalent resistance.

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Resistors in parallel

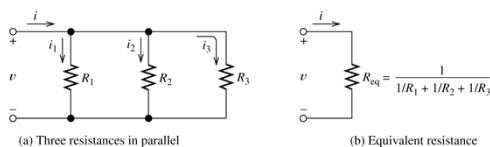


Figure 2.2 Parallel resistances can be combined into an equivalent resistance.

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Example 2.1

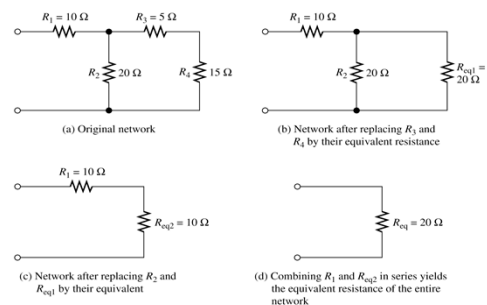


Figure 2.3 Resistive network for Example 2.1.

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Circuit Analysis using Series/Parallel Equivalents

1. Begin by locating a combination of resistances that are in series or parallel. Often the place to start is farthest from the source.
2. Redraw the circuit with the equivalent resistance for the combination found in step 1.
3. Repeat steps 1 and 2 until the circuit is reduced as far as possible. Often (but not always) we end up with a single source and a single resistance.
4. Solve for the currents and voltages in the final equivalent circuit.

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Example 2.2

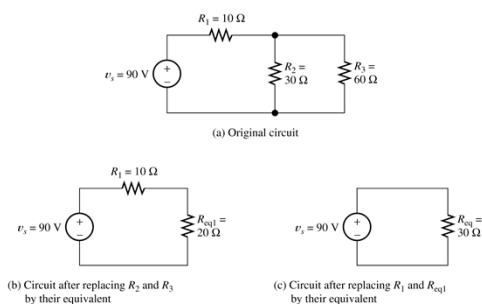
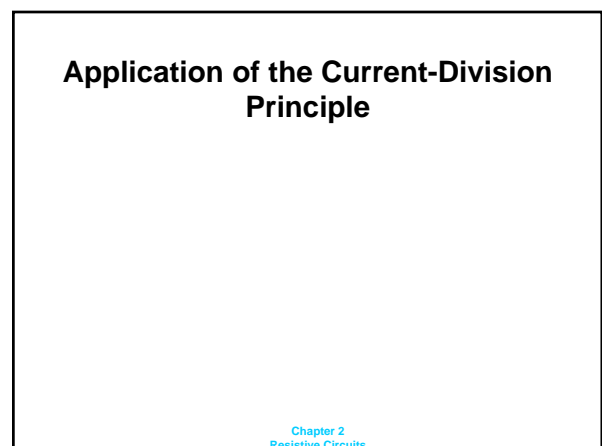
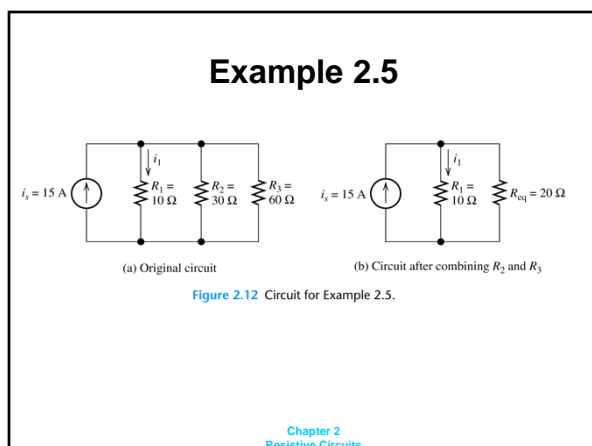
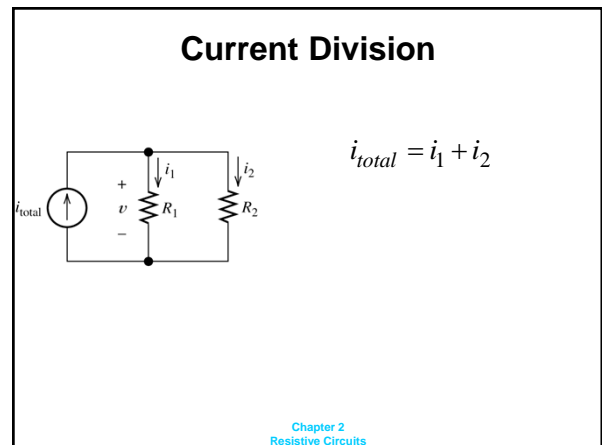
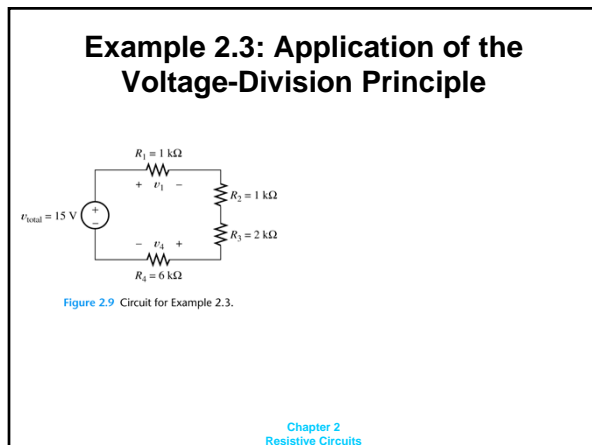
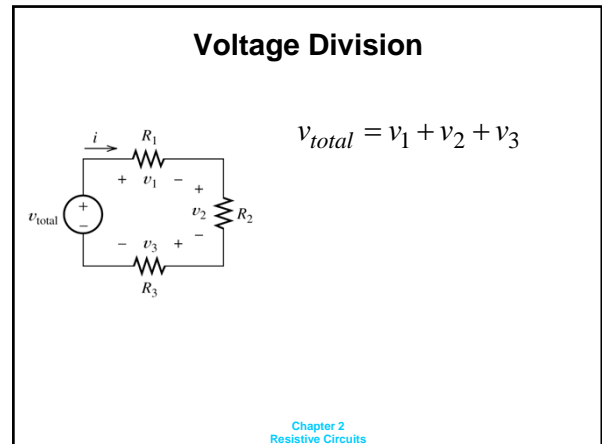
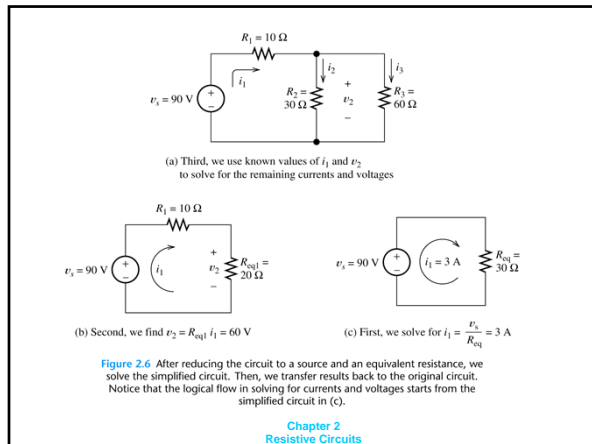


Figure 2.5 A circuit and its simplified versions. See Example 2.2.

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Exercise 2.3

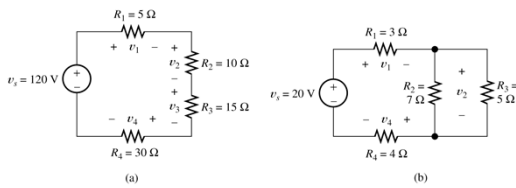


Figure 2.14 Circuits for Exercise 2.3.

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Exercise 2.4

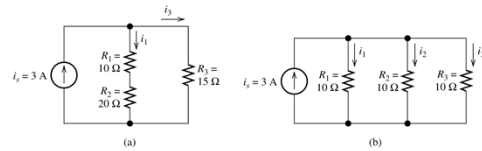


Figure 2.15 Circuits for Exercise 2.4.

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Although they are very important concepts, **series/parallel equivalents** and **the current/voltage division principles** are **NOT** sufficient to solve all circuit problems.

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Example

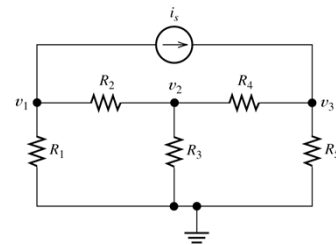


Figure 2.18 Circuit for Example 2.6.

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Node Voltage Analysis

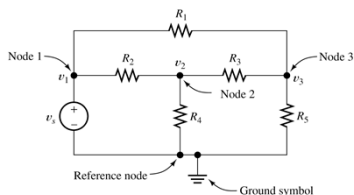


Figure 2.16 The first step in node analysis is to select a reference node and label the voltages at each of the other nodes.

- One of two **key** methods, the other is **mesh analysis**
- Based on KCL, more **systematic**
- All voltages have a **common reference point**

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Writing KCL Equations in Terms of the Node Voltages

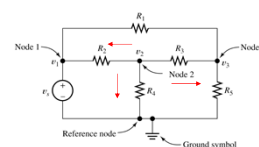


Figure 2.16 The first step in node analysis is to select a reference node and label the voltages at each of the other nodes.

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Example 2.6 Write equations

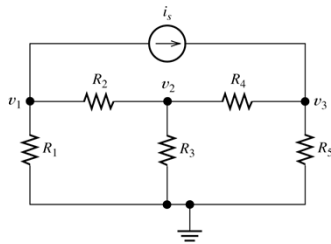
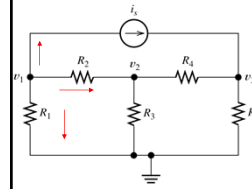


Figure 2.18 Circuit for Example 2.6.

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Example 2.8

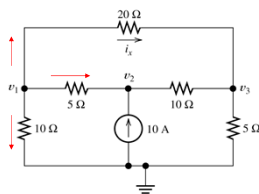


Figure 2.21 Circuit for Example 2.8.

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Different reference node

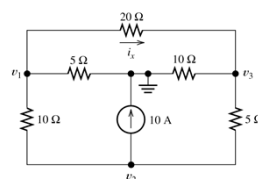


Figure 2.23 Circuit of Example 2.8 with a different choice for the reference node. See Exercise 2.9.

$$i_x = ?$$

How about v_1, v_2, v_3 ?

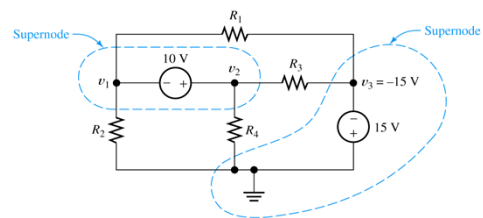
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Circuits with Voltage Sources

- We obtain **dependent** equations if we use all of the nodes in a network to write KCL equations

Introducing **super-nodes**

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Exercise 2.13

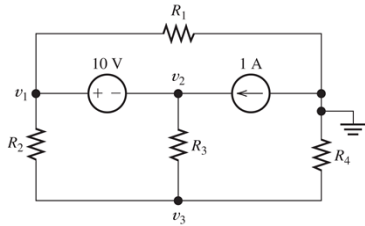
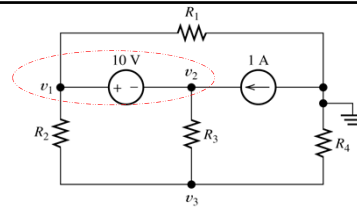


Figure 2.27 Circuit for Exercise 2.13.

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Summary: Node-Voltage Analysis

1. Select a reference node and assign variables for the unknown **node voltages**.
2. Write **current equations** for nodes and supernodes using **KCL**.
3. Put the equations into standard form and solve for the node voltages.
4. Use the values found for the node voltages to calculate any other currents or voltages of interest.

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Example 2.10

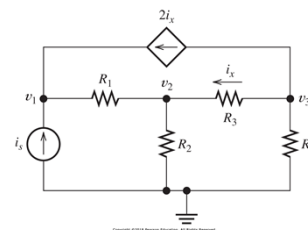
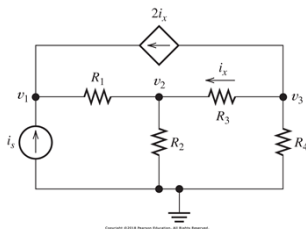


Figure 2.28 Circuit containing a current-controlled current source. See Example 2.10.

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Exercise 2.14

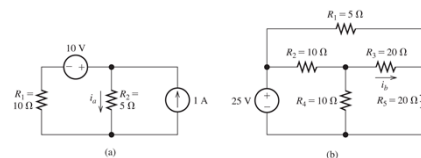


Figure 2.31 Circuits for Exercise 2.14.

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Review – Chapter 2

1. Series and parallel Resistors
2. Voltage and current division
3. Node Voltage Approach
KCL, Node voltages, voltage sources (super case)

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Mesh Current Analysis

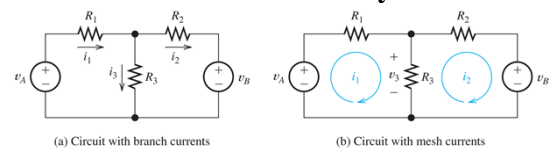


Figure 2.33 Circuit for illustrating the mesh-current method of circuit analysis.

- One of two key methods, the other is Nodal analysis
- Based on KVL, more systematic
- Work with imaginary currents

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Step 1: Choosing the Mesh Currents

When several mesh currents flow through one element, we consider the current in that element to be the algebraic sum of the mesh currents.

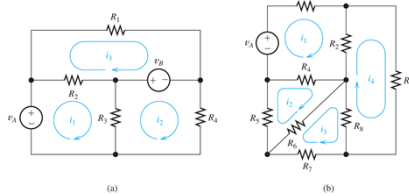


Figure 2.34 Two circuits and their mesh-current variables.

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Step 2: Writing Equations to Solve for Mesh Currents via KVL

Figure 2.34a

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Exercise 2.19

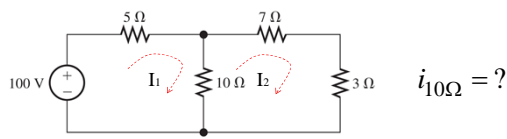


Figure 2.36 Circuit of Exercise 2.19.

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Mesh Currents in Circuits Containing Current Sources

A common mistake made by beginning students is to assume that the voltages across current sources are zero. In Figure 2.38, we have:

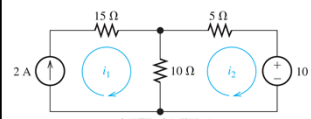


Figure 2.38 In this circuit, we have $i_1 = 2$ A.

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More complex example

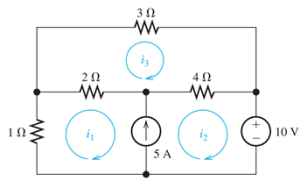


Figure 2.39 A circuit with a current source common to two meshes.

Combine meshes 1 and 2 into a **supermesh**. In other words, we write a KVL equation around the periphery of meshes 1 and 2 combined.

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Summary: Mesh-Current Analysis

1. Define the **mesh currents** flowing around each of the open areas defined by the network. For consistency, we usually select a **clockwise** direction for each of the mesh currents, but this is not a requirement.
2. Write **voltage equations** for the mesh loop by using the **KVL**.
3. Put the equations into standard form. Solve for the mesh currents.
4. Use the values found for the mesh currents to calculate any other currents or voltages of interest.

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Example 2.16

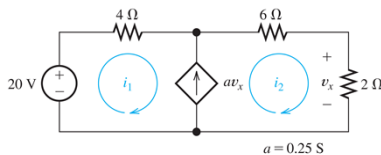


Figure 2.42 A circuit with a voltage-controlled current source. See Example 2.16.

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Nodal versus Mesh analysis

- Both are efficient
- Which is more efficient depends on the problem
- **Nodal analysis used more often**
- Nodal: KCL, Node voltages, voltage sources (**super case**)
- Mesh: KVL, Mesh currents, current sources (**super case**)

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Thévenin Equivalent Circuits

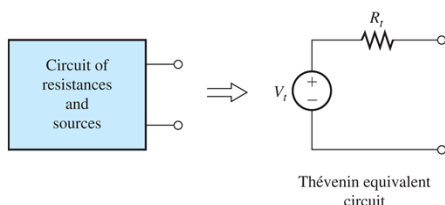


Figure 2.44 A two-terminal circuit consisting of resistances and sources can be replaced by a Thévenin equivalent circuit.

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How to find equivalent circuit?

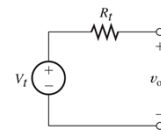


Figure 2.45 Thévenin equivalent circuit with open-circuited terminals. The open-circuit voltage v_{OC} is equal to the Thévenin voltage v_t .

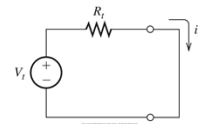


Figure 2.46 Thévenin equivalent circuit with short-circuited terminals. The short-circuit current is $i_{sc} = V_t/R_t$.

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Example 2.18

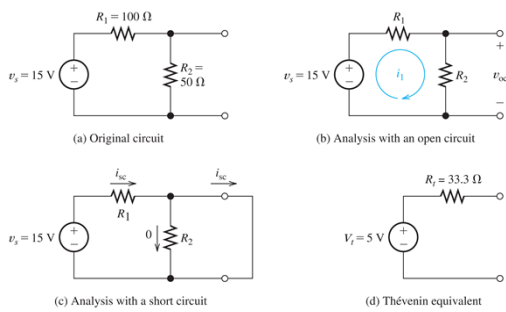


Figure 2.47 Circuit for Example 2.18.

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Finding the Thévenin Resistance Directly

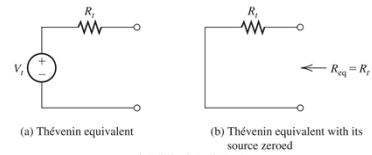


Figure 2.49 When the source is zeroed, the resistance seen from the circuit terminals is equal to the Thévenin resistance.

We can find the Thévenin resistance by **zeroing** the sources in the original network and then computing the resistance between the terminals.

When zeroing a **voltage** source, it becomes a **short** circuit.

When zeroing a **current** source, it becomes an **open** circuit.

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Example 2.19

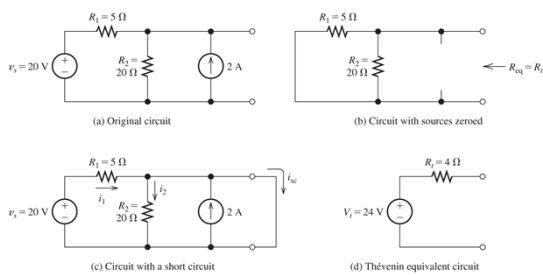


Figure 2.50 Circuit for Example 2.19.

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Norton Equivalent Circuit

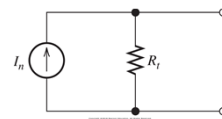


Figure 2.53 The Norton equivalent circuit consists of an independent current source I_n in parallel with the Norton resistance R_t .

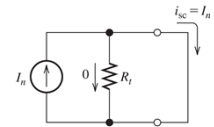


Figure 2.54 The Norton equivalent circuit with a short circuit across its terminals.

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Step-by-step Thévenin/Norton-Equivalent-Circuit Analysis

- Perform two of these:
 - Determine the open-circuit voltage $V_t = v_{oc}$.
 - Determine the short-circuit current $I_n = i_{sc}$.
 - Zero the sources and find the Thévenin resistance R_t looking back into the terminals.

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- Use the equation $V_t = R_t I_n$ to compute the remaining value.

- The Thévenin equivalent consists of a voltage source V_t in **series** with R_t .

- The Norton equivalent consists of a current source I_n in **parallel** with R_t .

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Source Transformations

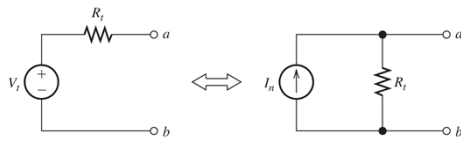
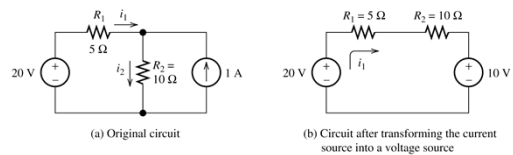


Figure 2.57 A voltage source in series with a resistance is externally equivalent to a current source in parallel with the resistance, provided that $I_s = V_s/R_s$.

The two circuits are **equivalent** if they have the same current-voltage relationship at their terminals

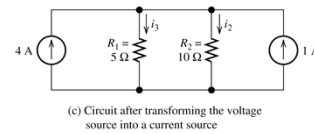
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Example 2.22



(a) Original circuit

(b) Circuit after transforming the current source into a voltage source

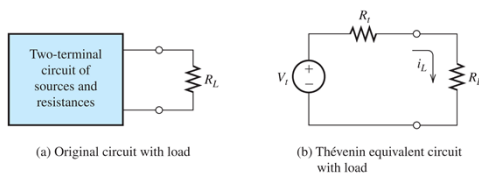


(c) Circuit after transforming the voltage source into a current source

Figure 2.54 Circuit for Example 2.18.

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Maximum Power Transfer



(a) Original circuit with load

(b) Thévenin equivalent circuit with load

Figure 2.60 Circuits for analysis of maximum power transfer.

The load resistance that absorbs the **maximum** power from a two-terminal circuit is equal to the Thévenin resistance.

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Example 2.23

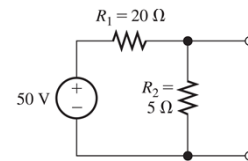


Figure 2.61 Circuit for Example 2.23.

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Superposition Principle

The **superposition principle** states that the total response is the algebraic **sum** of the responses to each of the independent sources acting individually. In equation form, this is

$$r_T = r_1 + r_2 + \cdots + r_n$$

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Foundation of the principle: **linearity**

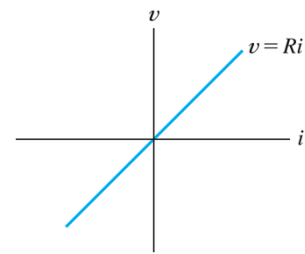


Figure 2.63 A resistance that obeys Ohm's law is linear.

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Example 2.24

$$V_T = V_1 + V_2 = 5 + 6.66 = 11.66V$$

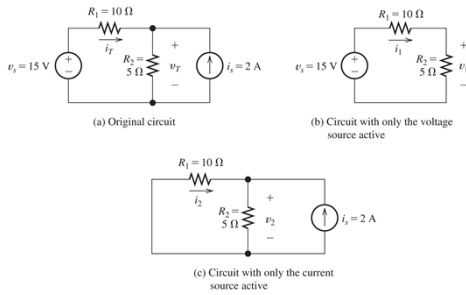
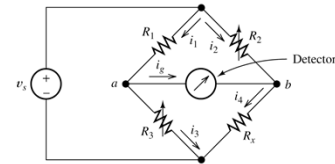


Figure 2.64 Circuit for Example 2.24 and Exercise 2.31.

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Wheatstone Bridge

The **Wheatstone bridge** is used by mechanical and civil engineers to measure the **resistances** of strain gauges in experimental stress studies of machines and buildings.



$$i_g = 0$$

$$R_x = \frac{R_2}{R_1} R_3$$

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