

Chapter 12

Bipolar Junction Transistors

1. Bipolar Junction Transistor (BJT) operation in amplifier circuits.
2. Analysis of amplifiers:
 - Load-line technique
 - Large signal model
 - Small signal model
3. Amplifier performance

Chapter 12
Bipolar Junction Transistors

BJT Structure and Symbol

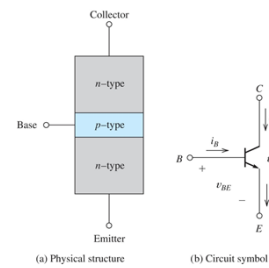


Figure 12.1 The npn BJT.

Chapter 12
Bipolar Junction Transistors

Equations of Operation

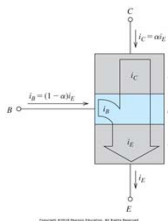


Figure 12.3 Only a small fraction of the emitter current flows into the base (provided that the collector-base junction is reverse biased and the base-emitter junction is forward biased).

$$i_E = I_{ES} \left[\exp\left(\frac{v_{BE}}{V_T}\right) - 1 \right]$$

Shockley equation

$$i_E = i_C + i_B \quad i_C = \beta i_B$$

$$\alpha = \frac{i_C}{i_E} \quad \beta = \frac{i_C}{i_B} = \frac{\alpha}{1 - \alpha}$$

Chapter 12
Bipolar Junction Transistors

Case Study: Exercise 12.1

$$\beta = 50, I_{ES} = 10^{-14} \text{ A}, v_{CE} = 5 \text{ V}$$

$$i_E = 10 \text{ mA}, V_T = 0.026 \text{ V}$$

Find v_{BE} , v_{BC} , i_C , i_B , and α

$$i_E = I_{ES} \left[\exp\left(\frac{v_{BE}}{V_T}\right) - 1 \right]$$

Chapter 12
Bipolar Junction Transistors

Common emitter circuit

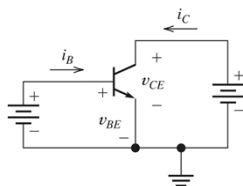


Figure 12.4 Common-emitter circuit configuration for the npn BJT.

$$v_{BC} = v_{BE} - v_{CE}$$

Chapter 12
Bipolar Junction Transistors

Common-emitter characteristics

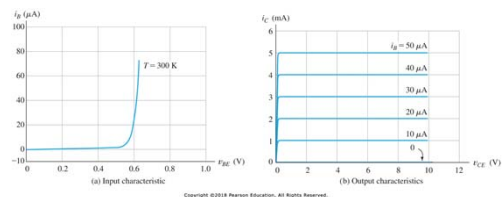


Figure 12.5 Common-emitter characteristics of a typical npn BJT.

Input Characteristic:

$$i_B \leftrightarrow v_{BE}$$

Output Characteristics:

$$i_C \leftrightarrow v_{CE}$$

Chapter 12
Bipolar Junction Transistors

Example 12.1: Find β

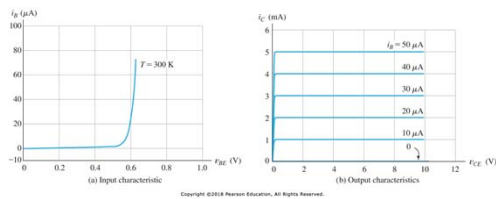


Figure 12.5 Common-emitter characteristics of a typical npn BJT.

$$\beta = 100$$

Chapter 12
Bipolar Junction Transistors

How does an amplifier work?

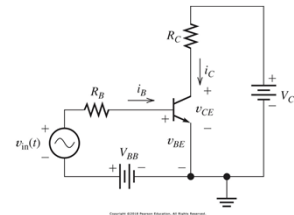


Figure 12.7 A simple common-emitter amplifier that can be analyzed by load-line techniques.

- DC circuit to provide basis
- AC signal to be amplified

Chapter 12
Bipolar Junction Transistors

Analysis of common emitter amplifier

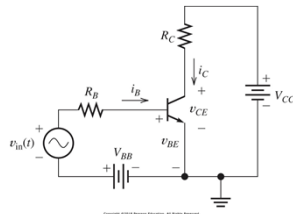


Figure 12.7 A simple common-emitter amplifier that can be analyzed by load-line techniques.

$$V_{BB} + v_{in}(t) = R_B i_B(t) + v_{BE}(t)$$

$$V_{CC} = R_C i_C + v_{CE}$$

Chapter 12
Bipolar Junction Transistors

Load-line analysis

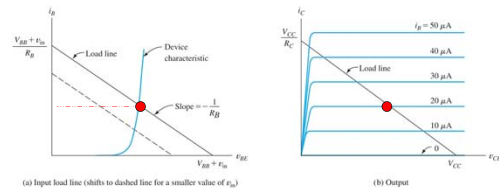


Figure 12.8 Load-line analysis of the amplifier of Figure 12.7.

$$V_{BB} + v_{in}(t) = R_B i_B(t) + v_{BE}(t) \quad V_{CC} = R_C i_C + v_{CE}$$

Operating point: Q-point with $v_{in} = 0$

Chapter 12
Bipolar Junction Transistors

Example 12.2

$$\frac{V_{BB} + v_{in}}{R_B}$$

$$V_{CC} = 10V$$

$$V_{BB} = 1.6V$$

$$R_B = 40k\Omega$$

$$R_C = 2k\Omega$$

$$v_{in}(t) = 0.4 \sin(2000\pi t)$$

Find maximum, minimum and Q-point values of v_{CE}

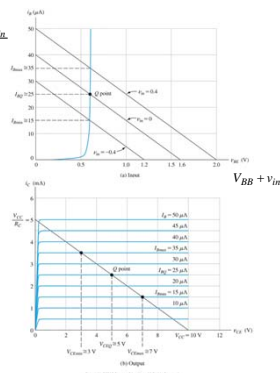


Figure 12.9 Load-line analysis for Example 12.2.

Chapter 12
Bipolar Junction Transistors

Example 12.2- continued

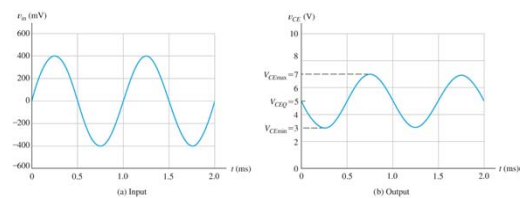


Figure 12.10 Voltage waveforms for the amplifier of Figure 12.7. See Example 12.2.

Inverting amplifier

Chapter 12
Bipolar Junction Transistors

Nonlinear distortion

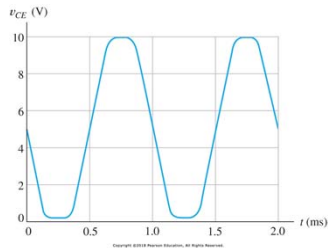


Figure 12.11 Output of the amplifier of Example 12.2 for $v_{BE}(t) = 1.2 \sin(2000\pi t)$ showing gross nonlinear distortion.

Chapter 12
Bipolar Junction Transistors

Saturation and cut-off

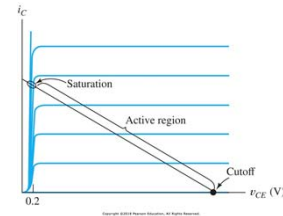


Figure 12.12 Amplification occurs in the active region. Clipping occurs when the instantaneous operating point enters saturation or cutoff. In saturation, $v_{CE} \approx 0.2$ V.

When i_C becomes zero, we say that the transistor is **cutoff**.
When v_{CE} is around 0.2 V, we say that the transistor is in **saturation**.

Chapter 12
Bipolar Junction Transistors

Application: Electronic switches

One particular application of Transistors working as a switch is electrical ignition in auto-industry to replace "points" (a mechanically operated switch)

Advantages:

- No wear out
- Many more improvements in ignition control

Chapter 12
Bipolar Junction Transistors

PNP Bipolar junction transistors

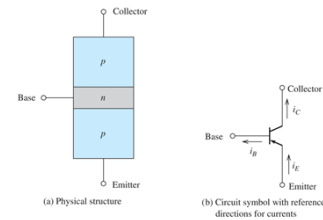


Figure 12.13 The *pn*p BJT.

Except for **reversal** of current directions and voltage polarities, the *pn*p BJT is almost identical to the *np*n BJT.

Chapter 12
Bipolar Junction Transistors

Common-emitter characteristics

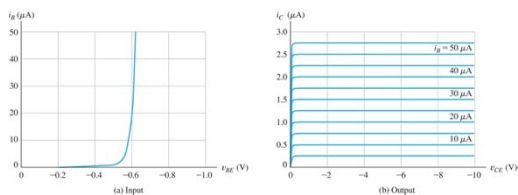
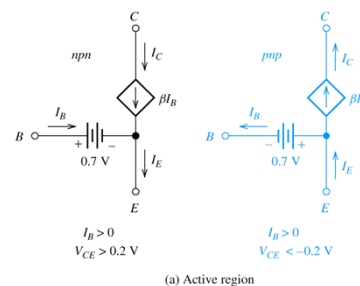


Figure 12.14 Common-emitter characteristics for a *pn*p BJT.

Chapter 12
Bipolar Junction Transistors

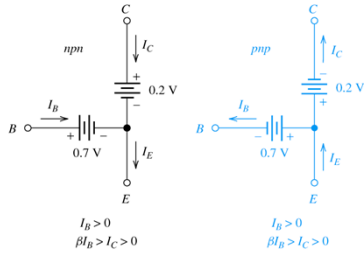
Large-signal dc circuit models (Active-Region Model)



(a) Active region

Chapter 12
Bipolar Junction Transistors

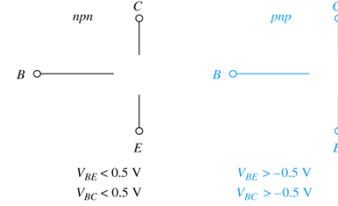
Large-signal dc circuit models (Saturation-Region Model)



(b) Saturation region

Chapter 12
Bipolar Junction Transistors

Large-signal dc circuit models (Cutoff-Region Model)



(c) Cutoff region

Chapter 12
Bipolar Junction Transistors

Case Study: Example 12.6

$$I_C = ?$$

$$V_{CE} = ?$$

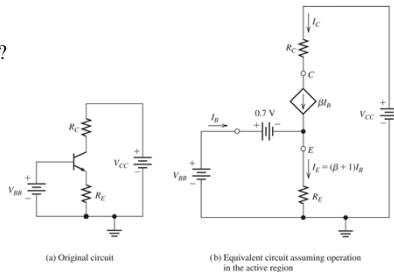


Figure 12.21 Circuit for Example 12.6.

Chapter 12
Bipolar Junction Transistors

Example 12.6

$$V_{CC} = 15 \text{ V}, V_{BB} = 5 \text{ V}, R_E = 2 \text{ k}\Omega, R_C = 2 \text{ k}\Omega, \beta = 100$$

$$V_{BB} = 0.7 + I_E R_E \Rightarrow I_E = \frac{V_{BB} - 0.7}{R_E} = 2.15 \text{ mA}$$

$$I_C = \beta I_B$$

$$I_E = I_B + I_C = (\beta + 1) I_B$$

Chapter 12
Bipolar Junction Transistors

Analysis of Four-Resistor Bias Circuit

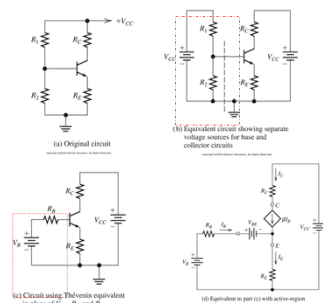
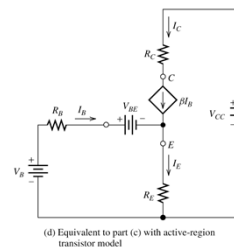


Figure 12.22 Four-resistor bias circuit.

Chapter 12
Bipolar Junction Transistors

Equivalent circuit



$$R_B = \frac{1}{1/R_1 + 1/R_2} = R_1 \parallel R_2$$

$$V_B = V_{CC} \frac{R_2}{R_1 + R_2}$$

$$I_B = \frac{V_B - V_{BE}}{R_B + (\beta + 1)R_E}$$

Chapter 12
Bipolar Junction Transistors

Case Study: Example 12.7

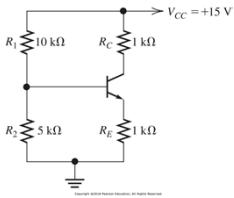


Figure 12.23 Circuit for Example 12.7.

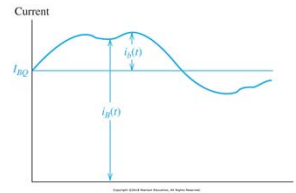
$$R_B = \frac{1}{1/R_1 + 1/R_2} = 3.33k\Omega$$

$$V_B = V_{CC} \frac{R_2}{R_1 + R_2} = 5V$$

$$I_B = \frac{V_B - V_{BE}}{R_B + (\beta + 1)R_E} = 41.2\mu A$$

Chapter 12
Bipolar Junction Transistors

Small signal equivalent circuits



$$i_B(t) = i_{BQ} + i_b(t)$$

$$v_{BE}(t) = V_{BEQ} + v_{be}(t)$$

Figure 12.24 Illustration of the Q-point base current I_{BQ} , signal current $i_b(t)$, and total current $i_B(t)$.

$i_b(t)$: the signal current flowing into the base
 I_{BQ} : the dc current that flows when the signal is absent
 $i_B(t)$: the total base current.

Chapter 12
Bipolar Junction Transistors

Small-Signal Equivalent Circuit for the BJT

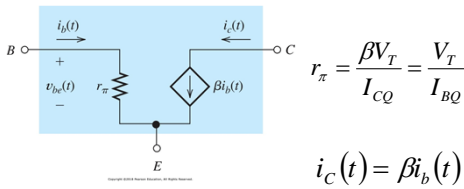
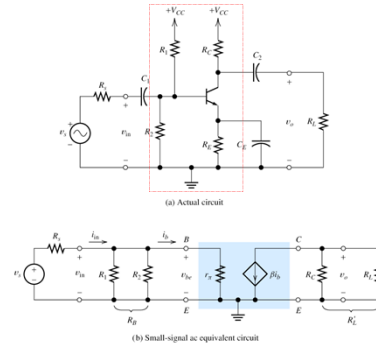


Figure 12.26 Small-signal equivalent circuit for the BJT.

Chapter 12
Bipolar Junction Transistors

Common-emitter amplifiers



Chapter 12
Bipolar Junction Transistors

$$R'_L = R_L \parallel R_C = \frac{1}{1/R_L + 1/R_C}$$

Voltage gain

$$A_v = \frac{v_o}{v_{in}} = -\frac{R'_L \beta}{r_\pi}$$

Open loop voltage gain

$$A_{vo} = \frac{v_o}{v_{in}} = -\frac{R_C \beta}{r_\pi}$$

Chapter 12
Bipolar Junction Transistors

Input impedance

$$Z_{in} = \frac{v_{in}}{i_{in}} = \frac{1}{1/R_B + 1/r_\pi}$$

Current gain

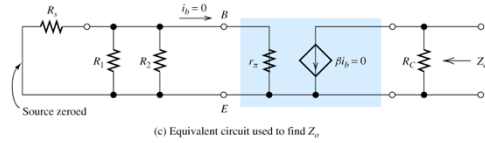
$$A_i = \frac{i_o}{i_{in}} = A_v \frac{Z_{in}}{R_L}$$

Power gain

$$G = A_i A_v$$

Chapter 12
Bipolar Junction Transistors

Output impedance



$$Z_o = R_C$$

The common-emitter amplifier is **inverting** and has **large voltage gain** magnitude, **large current gain**, and **large power gain**.

Chapter 12
Bipolar Junction Transistors

Example 12.8

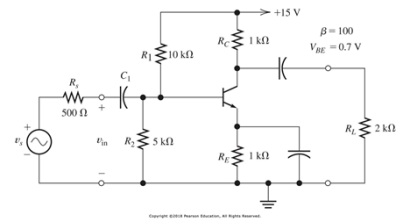


Figure 12.28 Common-emitter amplifier of Example 12.8.

Find $A_v, A_{vO}, Z_{in}, A_i, G, Z_o$

Chapter 12
Bipolar Junction Transistors

From example 12.7, one finds

$$I_{CQ} = 4.12\text{mA} \quad V_T = 0.026\text{V}, \beta = 100$$

$$r_\pi = \frac{\beta V_T}{I_{CQ}} = 631\Omega$$

$$R_B = R_1 \parallel R_2 = 3.33\text{k}\Omega, \quad R'_L = R_L \parallel R_C = 667\Omega$$

$$A_v = -\frac{R'_L \beta}{r_\pi} = -106 \quad A_{vO} = -\frac{R_C \beta}{r_\pi} = -158$$

$$Z_{in} = \frac{1}{1/R_B + 1/r_\pi} = 531\Omega \quad A_i = A_v \frac{Z_{in}}{R_L} = -28.1$$

$$G = A_i A_v = 2980 \quad Z_o = R_C = 1\text{k}\Omega$$

Chapter 12
Bipolar Junction Transistors

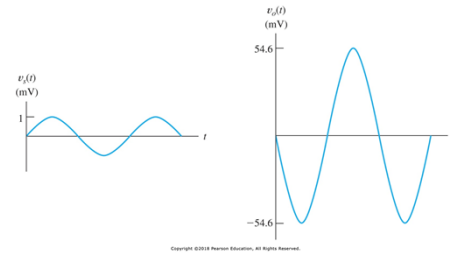
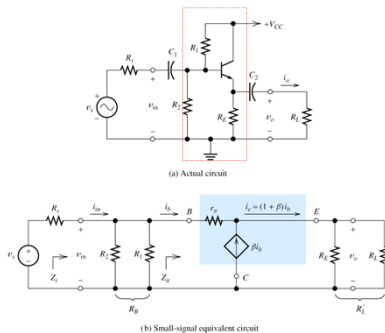


Figure 12.29 Source and output voltages for Example 12.8.

Chapter 12
Bipolar Junction Transistors

Emitter followers



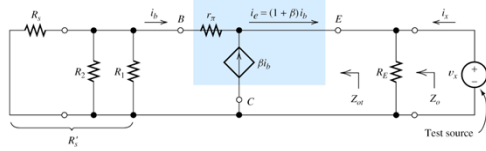
Chapter 12
Bipolar Junction Transistors

$$R_B = R_1 \parallel R_2 = \frac{1}{1/R_1 + 1/R_2} \quad R'_L = R_L \parallel R_E = \frac{1}{1/R_L + 1/R_E}$$

$$v_o = R'_L (1 + \beta) i_b \quad v_{in} = r_\pi i_b + R'_L (1 + \beta) i_b$$

Chapter 12
Bipolar Junction Transistors

Output Impedance



(c) Equivalent circuit used to find output impedance Z_o

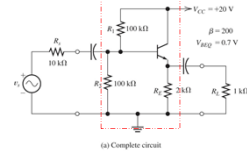
$$R'_s = \frac{1}{1/R_s + 1/R_1 + 1/R_2}$$

$$i_b + \beta i_b + i_x = \frac{v_x}{R_E}$$

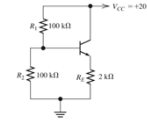
$$v_x + r_\pi i_b + R'_s i_b = 0$$

Chapter 12
Bipolar Junction Transistors

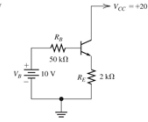
Case Study: Example 12.9



(a) Complete circuit



(b) DC bias circuit



(c) Equivalent bias circuit

Figure 12.31 Emitter follower of Example 12.9.

Chapter 12
Bipolar Junction Transistors

$$V_B = R_B I_{BQ} + V_{BEQ} + R_E (1 + \beta) I_{BQ} = 6.72V$$

$$I_{BQ} = 20.6\mu A \quad I_{CQ} = \beta I_{BQ} = 4.12mA \quad V_{CEQ} = V_{CC} - R_E I_{EQ} = 11.7V$$

$$r_\pi = \frac{\beta V_T}{I_{CQ}} = 1260\Omega$$

$$R_B = R_1 \parallel R_2 = 50k\Omega, \quad R'_L = R_L \parallel R_E = 667\Omega$$

$$A_v = \frac{(1 + \beta) R'_L}{r_\pi + (1 + \beta) R'_L} = 0.991$$

Chapter 12
Bipolar Junction Transistors

Comments on Emitter follower

Even though the voltage gain of the emitter follower is less than **unity**, the current gain and power gain can be **large**.

Chapter 12
Bipolar Junction Transistors